



SPECIFICATION

ORTUSTECH

COM48H4P03ULC

4.8" - HD - MIPI

Version: 2.0

Date: 14.01.2022

Note: This specification is subject to change without prior notice

Specifications for

Blanview TFT-LCD Monitor

(4.8" HD 720 x RGB x 1280 Portrait)

Version 2.0

(Please be sure to check the specifications latest version.)

MODEL COM48H4P03ULC

Customer's Approval	
Signature :	
Name :	
Section :	
Title:	
Date :	
ORTUSTECI	H
	TOPPAN INC. Electronics Division Ortus Subdivision
	Approved by Epuchi
	Checked by J. Matsumak V
	Prepared by

TOPPAN INC.

Issue:Jan.14,2022

Version History

Ver.	Date	Page		Description
0.0	2021.07.28	-	-	Tentative issue
1.0	2021.11.25	-	-	First issue
		P.13		8.1 DC Characteristics
			Add	Rating
<u>/</u> A ×9		P.14	Correct	Error correct
		P.17		8.2 AC Characteristics
			Correct	Error correct
		P.22		10.1 Power-ON Sequence
			Correct	Error correct
				10.2 Power-OFF Sequence
			Correct	Error correct
		P.23		11. External Power on/off Sequence
			Correct	Error correct
		P.24		12.1 Optical Characteristics
			Correct	Driving condition
		P.25		12.2 Temperature Characteristics
			Correct	Driving condition
		P.28		14. Reliability Test
			Add	Test condition Voltage (Surface discharge test (Non operation))
	2222 1 1 1		Add	number of failures / number of examinations
,	2022.1.14	P.11		5. Pin Assignment
<u>B</u>		D 40	Change	VCC rating
×7		P.12		7. Recommended Operating Conditions
		P.13	Change	VCC rating 8.1.1 Display Module
		F.13	Change	VCC rating
		P.15,16	_	8.2 AC Characteristics
		1 .10,10	Change	VCC rating
		P.24	0.16.1.90	12.1 Optical Characteristics
			Change	VCC rating
		P.25		12.2 Temperature Characteristics
			Change	VCC rating
		<u> </u>		

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1. Application

This Specification is applicable to 120.7 mm (4.8 inch) Blanview TFT-LCD monitor for non-military use.

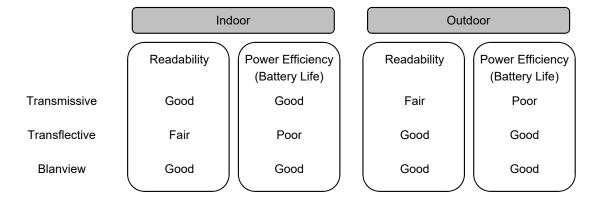
- TOPPAN makes no warranty or assume no liability that use of this Product and/or any information including drawings in this Specification by Purchaser is not infringing any patent or other intellectual property rights owned by third parties, and TOPPAN shall not grant to Purchaser any right to use any patent or other intellectual property rights owned by third parties. Since this Specification contains TOPPAN's confidential information and copy right, Purchaser shall use them with high degree of care to prevent any unauthorized use, disclosure, duplication, publication or dissemination of TOPPAN's confidential information and copy right.
- If Purchaser intends to use this Products for an application which requires higher level of reliability
 and/or safety in functionality and/or accuracy such as transport equipment (aircraft, train, automobile, etc.),
 disaster-prevention/security equipment or various safety equipment,
 Purchaser shall consult TOPPAN on such use in advance.
- This Product shall not be used for application which requires extremely higher level of reliability and/or safety such as aerospace equipment, telecommunication equipment for trunk lines, control equipment for nuclear facilities or life-support medical equipment.
- It must be noted as an mechanical design manner, especial attention in housing design to prevent arcuation/flexure caused by stress to the LCD module shall be considered.
- TOPPAN assumes no liability for any damage resulting from misuse, abuse, and/or miss-operation of the Product deviating from the operating conditions and precautions described in the Specification.
- It shall be mutually conferred if nonconforming defect which result from unspecified cause in this specification arises.
- If any issue arises as to information provided in this Specification or any other information, TOPPAN and Purchaser shall discuss them in good faith and seek solution.
- TOPPAN assumes no liability for defects such as electrostatic discharge failure occurred during peeling off the protective film or Purchaser's assembly process.

Object substance	Maximum content [ppm]
Cadmium and its compound	100
Hexavalent Chromium Compound	1000
Lead & Lead compound	1000
Mercury & Mercury compound	1000
Polybrominated biphenyl series (PBB series)	1000
Polybrominated biphenyl ether series (PBDE series)	1000
Bis(2-ethylhexyl)phthalate series(DEHP series)	1000
Butyl benzyl phthalate series(BBP series)	1000
Dibutyl phthalate series(DBP series)	1000
Diisobutyl phthalate series(DIBP series)	1000

2. Outline Specifications

2.1 Features of the Product

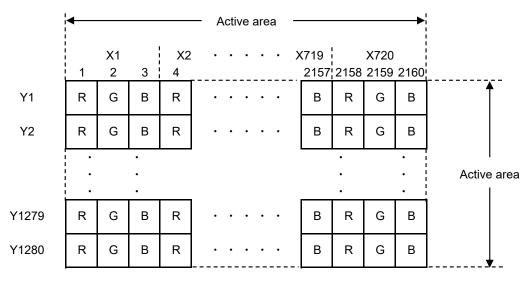
- 4.8 inch diagonal display, 720 x RGB [H] x 1280 [V] dots.
- 24bitRGB (8-8-8 Format) / 16.7 Million colors.
- MIPI DSI as high-speed interface. Video mode only.
- Timing generator [TG], Counter-electrode driving circuitry, Built-in power supply circuit.
- Various display controls and functional selection.
- Blanview TFT-LCD, improved outdoor readability.
 - * MIPI: Mobile Industrial Processor Interface, DSI: Display Serial Interface



2.2 Display Method

Items	Specifications	Remarks
Display type	VA 16.7 Million colors.	
	Blanview, Normally black.	
Driving method a-Si TFT Active matrix.		
	Line-scanning, Non-interlace.	
Dot arrangement	RGB stripe arrangement.	Refer to "Dot arrangement"
Signal input method	MIPI DSI 4-lanes : 4 data lanes and 1 clock lane	in foot note 1
Backlight type	Long life & High bright white LED.	
NTSC Ratio	50%	

Foot note1: when use MIPI DSI 2-lanes, Please contact Toppan.

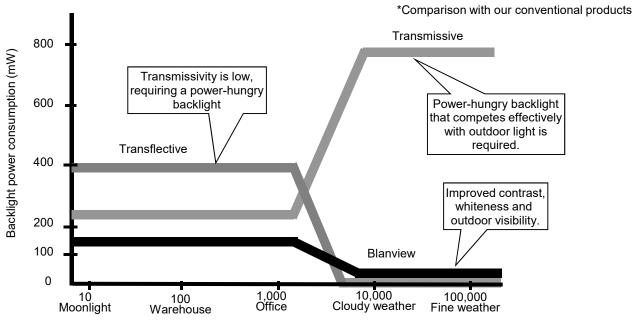


Dot arrangement (FPC cable placed low)

<Features of Blanview>

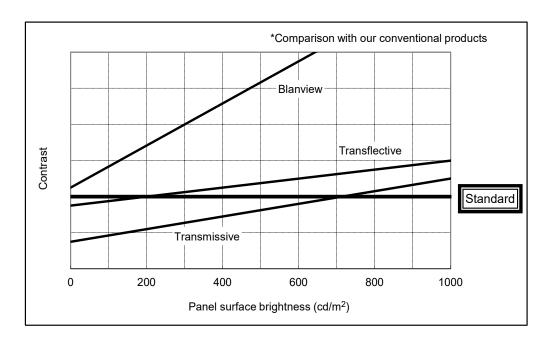
(TOPPAN criteria)

- Backlight power consumption required to assure visibility. (equivalent to 3.5"QVGA)



Surrounding illumination (lx)

Contrast characteristics under 100,000lx. (same condition as direct sunlight.)
 With better contrast (higher contrast ratio), Blanview TFT-LCD has the best outdoor readability in three different types of TFT-LCD.
 Below chart shows contrast value against panel surface brightness. (Horizontal: Panel surface brightness/ Vertical: Contrast value) LCD panel has enough outdoor readability above our Standard line.

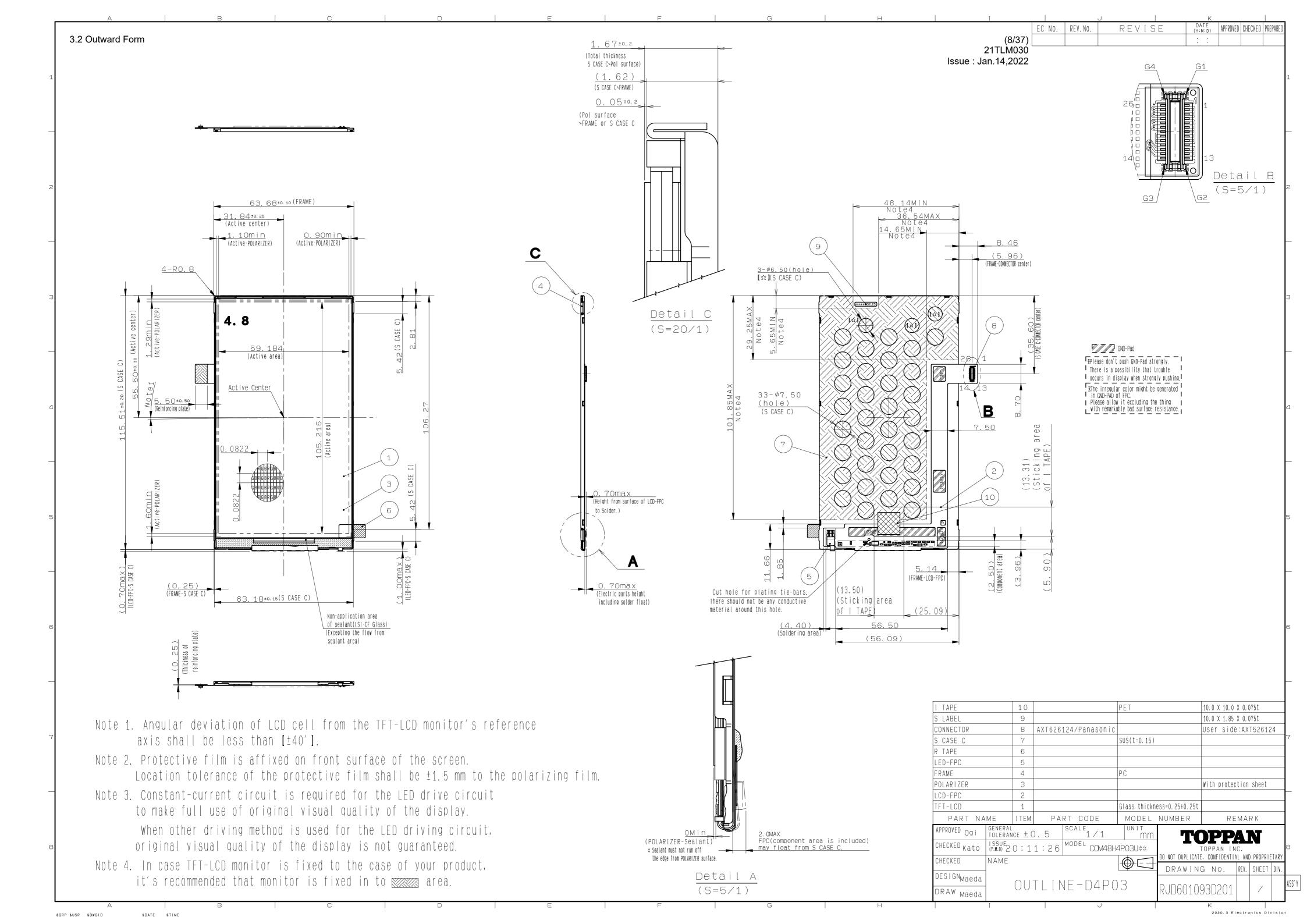


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3. Dimensions and Shape

3.1 Dimensions

Items	Specifications	Unit	Remarks
Outline dimensions	63.68 [H] × 115.51 [V] ×1.67 [D]	mm	Exclude FPC cable
Active area	59.184[H] × 105.216[V]	mm	120.7 mm diagonal
Number of dots	2160[H] × 1280[V]	dot	
Dot pitch	27.4[H] × 82.2[V]	um	
Surface hardness of the polarizer	2	Н	Load: 2.94N
Weight	25.2	g	



3.3 Serial Label (S-label)

3.3.1 Display items

S-label indicates the least significant digit of manufacture year (1digit), manufacture month with below alphabet (1letter), model code (5characters), serial number (6digits).

* Contents of Display

* *		****	*****
_	_		
а	b	С	d

	Contents of display								
а	The least significant digit of manufacture year								
b	Manufacture month	Jan-A	Jan-A May-E Sep-I						
		Feb-B Jun-F Oct-J							
		Mar-C Jul-G Nov-K							
		Apr-D Aug-H Dec-L							
С	Model code	48BKC (Made in Japan)							
		48BLC (Made in Malaysia)							
d	Serial number								

^{*} Example of indication of Serial label (S-label)

1L48BKC000125

means "manufactured in December 2021, 4.8" BK type, C specifications, serial number 000125"

· Made in Malaysia

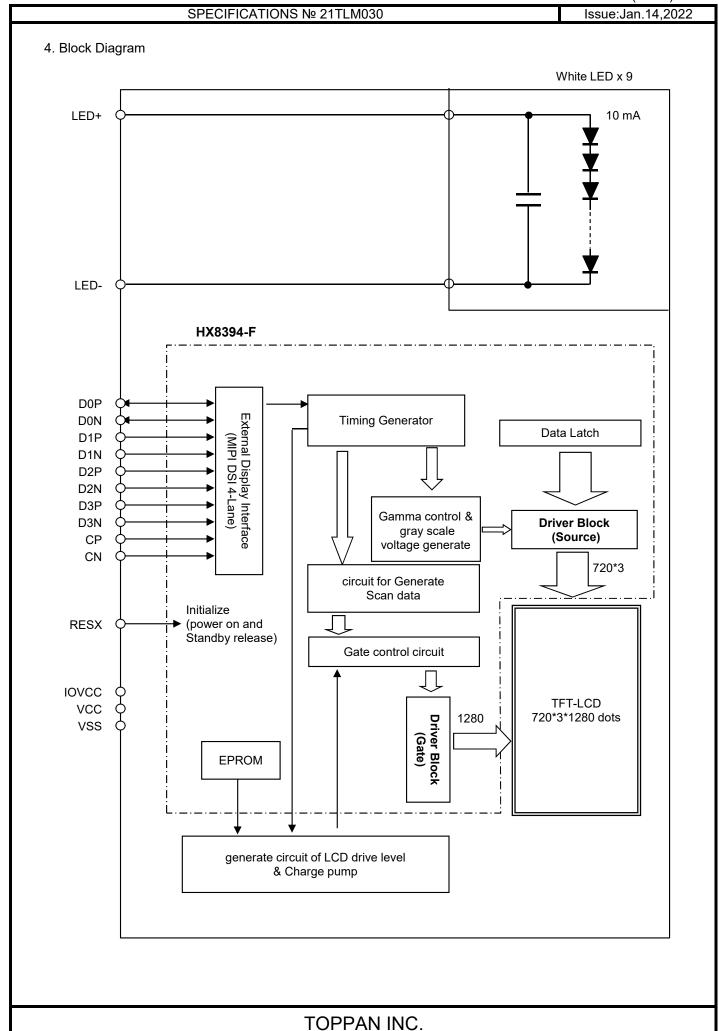
1L48BLC000125

means "manufactured in December 2021, 4.8" BL type, C specifications, serial number 000125"

3.3.2 Location of Serial Label (S-label)

Refer to 3.2 "Outward Form".

Made in Japan



5. Pin Assignment

A

No.	Symbol	Details	Remark	Ю
1	VSS	Ground		Р
2	LED+	Backlight LED Anode		Р
3	LED-	Backlight LED Cathode		Р
4	VSS	Ground		Р
5	VCC	Analog Power Supply	VCC = 2.9V (TYP)	Р
6	IOVCC	DSI and I/O Power Supply	IOVCC = 1.8V (TYP)	Р
7	VSS	Ground		Р
8	RESX	LCD Reset	L:Initialize , Power_ON Reset is Required when Turning on the Power	ı
9	TEST OUT	TEST Signal output	Test Pin. Please Open this pin	0
10	VSS	Ground		Р
11	D3N	MIPI-DSI Data differential signal pin (Data lane 3)		ı
12	D3P	MIPI-DSI Data differential signal pin (Data lane 3)		ı
13	VSS	Ground		Р
14	VSS	Ground		Р
15	D2P	MIPI-DSI Data differential signal pin (Data lane 2)		ı
16	D2N	MIPI-DSI Data differential signal pin (Data lane 2)		ı
17	VSS	Ground		Р
18	СР	MIPI-DSI Clock differential signal pin		I
19	CN	MIPI-DSI Clock differential signal pin		I
20	VSS	Ground		Р
21	D1P	MIPI-DSI Data differential signal pin (Data lane 1)		ı
22	D1N	MIPI-DSI Data differential signal pin (Data lane 1)		ı
23	VSS	Ground		Р
24	D0P	MIPI-DSI Data differential signal pin (Data lane 0)		Ю
25	D0N	MIPI-DSI Data differential signal pin (Data lane 0)		Ю
26	VSS	Ground		Р

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6. Absolute Maximum Rating

VSS=0V

Item	Symbol	Condition	Ra	Rating		Applicable terminal
			MIN	MAX		
Supply voltage	VCC	Ta=25 ℃	-0.3	3.6	V	VCC
DSI and I/O Power Supply voltage	IOVCC		-0.3	3.6	V	IOVCC
Input voltage for logic	VI		-0.3	IOVCC+0.3	V	RESX
DSI Input voltage	VDSIIN		-0.3	2.0	V	D0P/N D2P/N D1P/N D3P/N CP/N
LED forward current	IL	Ta = 25 ℃	-	35	mA	LED+ - LED-
		Ta = 70 °C	-	15		
Storage temperature range	Tstg		-30	80	°C	
Storage humidity range	Hstg	Non condensing moisture at or les				

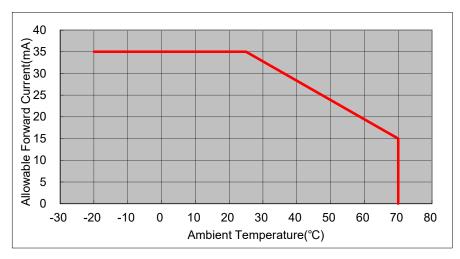
7. Recommended Operating Conditions

VSS=0V

								V33=0V
Item	Symbol	Condition		Rating		Unit	Applica	able terminal
			MIN	TYP	MAX			
Supply voltage	VCC		2.7	2.9	3.0	V	VCC	
DSI and I/O Power Supply voltage	IOVCC		1.7	1.8	2.0	V	IOVCC	
Input voltage for logic	VI		0	-	IOVCC	V	RESX	
DSI Input voltage	VDSIIN		-0.15	-	1.45	V	D0P/N D1P/N CP/N	D2P/N D3P/N
Operational temperature range	Тор	Note1,2	-20	25	70	°C	Panel su	ırface temperature
Operating humidity range		Ta<=40 °C	20	-	85	%		
	Нор	Ta>40 °C	Non conder	•				

Note1: This monitor is operatable in this temperature range. With regard to optical characteristics, refer to Item 12."CHARACTERISTICS".

Note 2: Acceptable Forward Current to LED is up to 15 mA, when Ta=+70 °C. Do not exceed Allowable Forward Current shown on the chart below.



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8. Electrical Characteristics

A

8.1 DC Characteristics

8.1.1 Display Module

(Unless otherwise noted, Ta=25 °C, VCC=2.9V, IOVCC=1.8V, VSS=0V)

			(Offices officewise floted, 14-25 0, 400-2.94, 10400-1.04, 400-04)					
Item	Symbol	Condition		Rating	Unit	Applicable		
			MIN	TYP	MAX		terminal	
Input Signal	VIH	IOVCC=1.7-2.0V	0.7×IOVCC	-	IOVCC	V	RESX	
Voltage	VIL		0	1	0.3×IOVCC	V		
Operating	ICC	Still Image*	-	29.5	59	mA	VCC	
Current	ICCIO		-	17.5	35	mA	IOVCC	
Sleep_mode	ICCS	Clock/Data=LP11	-	50	100	uA	VCC	
Current	ICCIOS	Sleep_mode	-	75	200	uA	IOVCC	

 $^{^{\}star}$ A still image (color bar) on display , when accessing to the driver by MIPI DSI 4Lane.

The details of the drive condition of MIPI DSI are referring to the following.

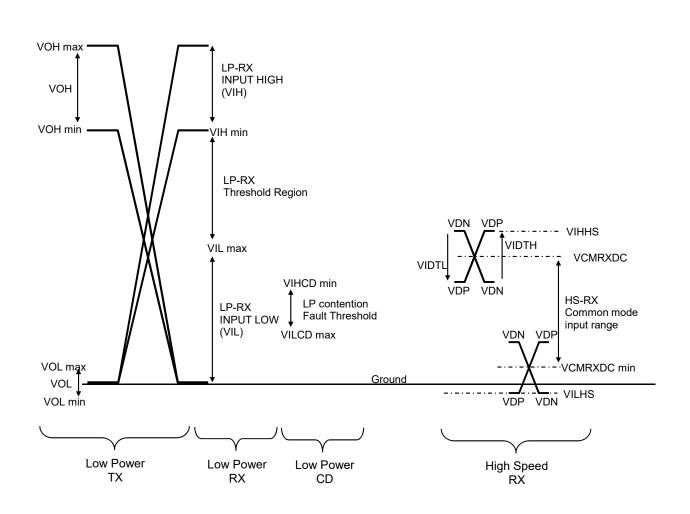
Vsync pulse width	3	line
V-Back porch	3	line
V-Front porch	6	line
V-Line	1280	line

Hsync pulse width	15	clk
H-Back porch	70	clk
H-Front porch	91	clk
H-Line	720	clk

Frame Rate	58.1	Hz
Data Rate	403.2	Mbps
CLK frequency	201.6	MHz

8.1.2 MIPI DSI Interface

	Item	Symbol	Condition		Rating			
				MIN	TYP	MAX		
HS-RX	Differential input high threshold	VIDTH		-	-	70	mV	
	Differential input low threshold	VIDTL		-70	-	-	mV	
	Single-ended input high voltage	VIHHS		-	-	460	mV	
	Single-ended input low voltage	VILHS		-40	-	-	mV	
	Common-mode voltage HS receive mode	VCMRXD C		70	-	330	mV	
	Differential input impedance	ZID		80	100	125	Ohm	
LP-RX	Logic 1 input voltage	VIH		880	-	-	mV	
	Logic 0 input voltage	VIL		-	-	550	mV	
LP-TX	Thevenin output low level	VOL		-50	-	50	mV	
	Thevenin output high level	VOH		1.1	1.2	1.3	V	
	Output impedance of LP transmitter	ZOLP		110	-	-	Ohm	
CD-RX	Logic 1 contention threshold	VIHCD		450	-	-	mV	
	Logic 0 contention threshold	VILCD		-	-	200	mV	



The signal levels for the LP and HS.

8.1.3 Backlight

Item	Symbol	Condition		Rating		Unit	Applicable terminal
			MIN	TYP	MAX		
Forward current	IL25	Ta=25 ℃		10	35	mA	LED+ - LED-
	IL70	Ta=70 °C			15	mA	
Forward voltage	VL	Ta=25 ℃		24.8*	27.5*	V	
(*Reference only)		IL=10mA					
Estimated Life	LL	Ta=25 ℃		20000		hrs	
of LED		IL=10mA					
		Note					

Note: - The lifetime of the LED is defined as a period till the brightness of the LED decreases to the half of its initial value.

- This figure is given as a reference purpose only, and not as a guarantee.
- This figure is estimated for an LED operating alone.
 As the performance of an LED may differ when assembled as a monitor together with a TFT panel due to different environmental temperature.
- Estimated lifetime could vary on a different temperature and usually higher temperature could reduce the life significantly.

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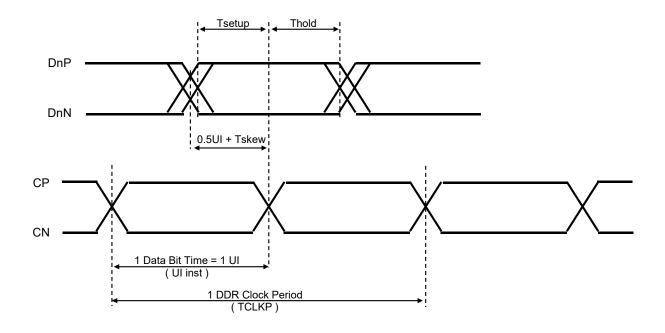


8.2 AC Characteristics

8.2.1 HS-RX Specifications

(Unless otherwise noted, Ta=25 °C, VCC=2.9V, IOVCC=1.8V, VSS=0V)

(emess suisimes nates, ra 25 s, vee 2.5v, reves 1.5v, vee sv)								
Item	Symbol	Condition	Rating			Unit	Applicable terminal	
			MIN	TYP	MAX			
DSICLK Frequency	f DSICLK		(40)	-	(250)	MHz	CP/N	
DSICLK Cycle time	TCLKP		4.0	-	25.0	ns]	
DSI Data Transfer Rate	UI		2.0	-	12.5	ns	D0P/N,D1P/N	
	t DSIR		80	-	500	Mbps	D2P/N,D3P/N	
Data to Clock Setup Time	Tsetup		0.15	-	-	UI	D0P/N,D1P/N	
Clock to Data Hold Time	Thold		0.15	-	-	UI	D2P/N,D3P/N	
							CP/N	



Data to Clock Timing Definitions

8.2.2 LP-TX Specifications

(Unless otherwise noted, Ta=25 °C,VCC=2.9V,IOVCC=1.8V,VSS=0V)

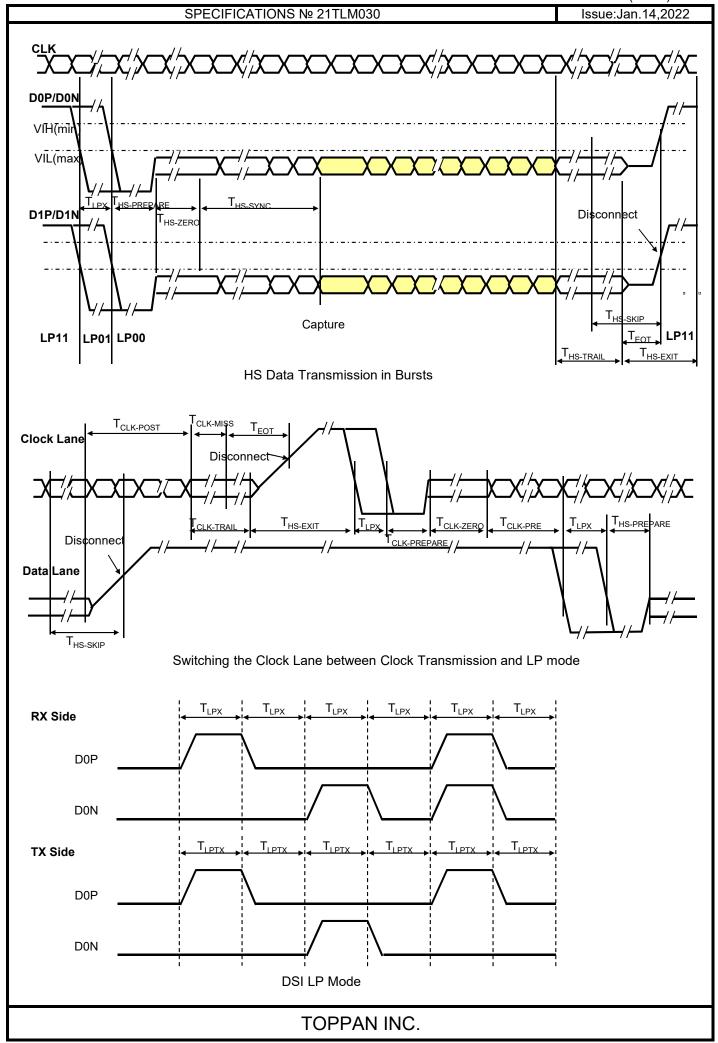
Item	Symbol	Condition	Rating			Unit	Applicable terminal
			MIN	TYP	MAX		
15% - 85% rise time	TRLP		-	-	25	ns	D0P/N
85% - 15% fall time	TFLP		-	-	25	ns	
Slew rate	ΔV / ⊿tSR	Cload = 70pF	-	-	150	mV/ns	



8.2.3 RX/TX Specifications

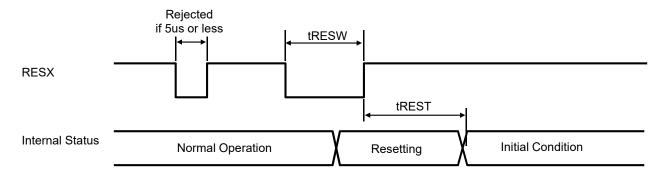
(Unless otherwise noted, Ta=25 $^{\circ}$ C,VCC=2.9V,IOVCC=1.8V,VSS=0V)

Item	Description	Rating				
		MIN	TYP	MAX		
T _{HS-PREPARE}	Time to drive LP00 to prepare for HS transmission	40 ns + 4UI	-	85 ns + 6UI		
T _{HS-PREPARE} + T _{HS-ZERO}	T _{HS-PREPARE} + Time to drive HS0 before the SYNC sequence	145 ns +10UI	-	-		
T _{HS-TRAIL}	Time to drive flipped differential state after last payload data bit of a HS transmission burst	max(n*8UI,60 ns+n*4UI)	-	-		
T _{HS-EXIT}	Time to drive LP11 after HS burst	100	-	-	ns	
T _{TA-GO}	Time to drive LP00 after Turnaround Request		4*T _{LPTX}			
T _{TA-SURE}	Time out before new TX side starts driving	TLPTX	-	2*T _{LPTX}		
T _{TA-GET}	Time to drive LP00 by new TX		5*T _{LPTX}			
T_{LPX}	Length of any Low Power state period	50	-	-	ns	
Ratio T _{LPX}	Ratio of T _{LPX} (MASTER)/T _{LPX} (SLAVE) between Master and Slave side	2/3	-	3/2		
T _{CLK-POST}	Time that the transmitter shall continue sending HS clock after the last associated Data Lane has transitioned to LP mode	60 ns + 52UI	-	-		
T _{CLK-PREPARE} + T _{CLK-} ZERO	T _{CLK-PREPARE} +time for lead HS-0 drive period before starting Clock	300	-	-	ns	
T _{CLK-PRE}	Time that the HS clock shall be driven prior to any associated Data Lane beginning the transition from LP to HS mode	8	-	-	UI	
T _{CLK-PREPARE}	Time to drive LP-00 to prepare for HS clock transmission	38	-	95	ns	
T _{CLK-TRAIL}	Time to drive HS differential state after last payload clock bit of a HS transmission burst	60	-	-	ns	
T _{EOT}	Time from start of THS-TRAIL period to start of LP- 11 state	-	-	105 ns + n*12UI		
T _{LPTX1}	Length of Low-Power TX state period in case of using DSI clock	-	n*DSITX	-	UI	
T _{LPTX2}	Length of Low-Power TX state period in case of using internal OSC clock	-	1/fosc	-	ns	



8.2.4 Reset input timing

Item	Symbol	Condition	Rating		Unit	Remark	
			MIN	TYP	MAX		
Reset Low Pulse width	tRESW		20	-	-	us	RESX
Reset Complete time	tREST	during Sleep In	10	-	-	ms	RESX
Neset Complete time	lixE31	during Sleep Out	120	-	-	ms	

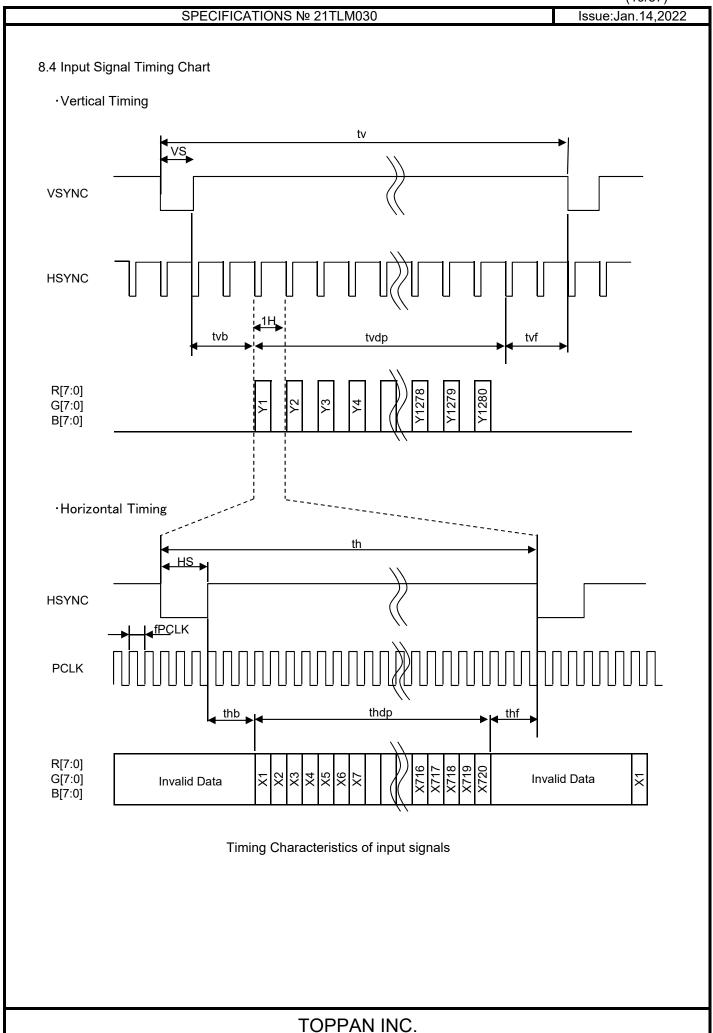


RESX input timing

8.3 Input Timing Characteristics

Item	Symbol	Condition	Rating		Unit	Remark	
			MIN	TYP	MAX		
CLK Frequency	fPCLK		62.5	67.3	-	MHz	PCLK
VSYNC Frequency Note	fVSYNC		54	58.1	66	Hz	VSYNC
VSYNC Cycle	tv		1292	1292	-	Н	
VSYNC Pulse Width	VS		3	3	-	Н	
Vertical Back Porch	tvb		3	3	-	Н	
Vertical Front Porch	tvf		6	6	-	Н	
Vertical data start Point	VS+tvb		6	6	-	Н	
Vertical Blanking Period	VS+tvb+tvf		12	12	-	Н	
Vertical Display Period	tvdp		-	1280	-	Н	
HSYNC frequency	fHSYNC		69.8	75.1	-	kHz	HSYNC
HSYNC Cycle	th		-	896	-	CLK	
HSYNC Pulse Width	HS		-	15	-	CLK	
Horizontal Back Porch	thb		-	70	-	CLK	
Horizontal Front Porch	thf		-	91	-	CLK	
Horizontal data start Point	HS+thb		-	85	-	CLK	
Horizontal Blanking Period	HS+thb+thf		-	176	-	CLK]
Horizontal Display Period	thdp		-	720	-	CLK	

Note: This is recommended spec to get high quality picture on display. It is customer's risk to use out of this frequency.



9. About MIPI Interface

9.1 Version

The DSI incorporated in the LCD-Driver complies with the following standards.

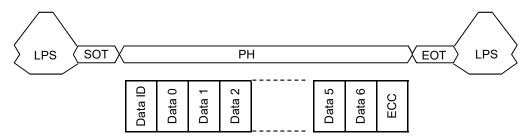
MIPI DSI : Version 1.1 MIPI D-PHY : Version 1.1

Data transfer mode: Video mode only. Virtual Channel: Set to VC[1:0]=00.

It is recommended to turn DSI CLK off (LP mode) during the blanking period.

9.2 DSI protocol

- Short packets specify the payload length using the Data Type field and are from 2 to 9 bytes in length. Short packets is used for most Command Mode commands and associated parameters.

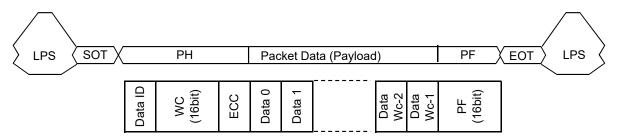


LPS: Low power state SOT: Start of Transmission PH: Packet Header

DI(Data ID): Contain Virtual Channel Identifier and Data Type

ECC: Error Correction Code

- Long packets specify the payload length using a two-byte Word Count field and then the payload maybe 0 to 65,541 bytes in length. Long packets permit transmission of large blocks of pixel or other data.



LPS: Low power state SOT: Start of Transmission PH: Packet Header

DI(Data ID): Contain Virtual Channel Identifier and Data Type WC(Word Count): The receiver use WC to define packet end.

ECC: Error Correction Code

PF(Packet Footer): Mean 16-bit Checksum.

9.3 Packet data types

LCD-Driver has the following restriction.

Generic short / Long Write Cmd is not supported.

It is only DCS Short / Long Write Cmd.

Processor to peripheral direction

Data Type Hex	Description	Size
01 h	Sync Event , V Sync Start (01,00,00,07)	Short
11 h	Sync Event , V Sync End (11,00,00,14)	Short
21 h	Sync Event , H Sync Start (21,00,00,12)	Short
31 h	Sync Event , H Sync End (31,00,00,01)	Short
22 h	Shut Down Peripheral Command (22,00,00,1E)	Short
32 h	Turn On Peripheral Command (32,00,00,0D)	Short
05 h	DCS WRITE , no parameters	Short
15 h	DCS WRITE , one parameters	Short
06 h	DCS READ , no parameters	Short
37 h	Set Maximum Return Packet Size	Short
08 h	End of Transmission Packet (08,0F,0F,01)	Short
09 h	Null Packet , no data	Long
19 h	Blanking Packet , no data	Long
39 h	DCS Long Write Command Packet	Long
0E h	Packed Pixel Stream , 16-bit RGB , 5-6-5 Format	Long
1E h	Packed Pixel Stream , 18-bit RGB , 6-6-6 Format	Long
2E h	Loosely Packed Pixel Stream , 18-bit RGB ,6-6-6 Format	Long
3E h	Packed Pixel Stream , 24-bit RGB , 8-8-8 Format	Long

Peripheral to processor direction

Data Type	Description	Size
Hex		
02 h	Acknowledge with Error Report	Short
1C h	DCS Long READ Response	Long

9.4 Packet Footer on the long packet

After Packet Data, Packet Footer is added in Long packet. Packet Footer adds CRC calculated from Packet Data as Checksum.

Checksum(2byte) = CRC(Packet Data) CRC = X^16 + X^12 + X^5 + X^0

10.Sequence

10.1 Power-ON Sequence

PH :Packet Header

10.1	Power-On Sequ	ience	Ph .Packet header				
No.		Function	DNC	Command /Parameter	Type	Command/Parameter	
	Power on	VCC, IOVCC On					
	MIPI signal state	Keep CN/P, D0N/P, D1N/P D2N/P,D3N/P in STOP state LP-11 (Must be set to LP-11 is all lanes of MIPI before rising edge of RESX.)					
	Wait	wait 1 msec or more					
	H/W RESET	RESX=1					
	Wait	wait 10 msec or more					
1	SLEEP OUT	Sleep out	0	11h	PH	[05 11 00 36]	
	Wait	wait 200 msec or more					
2	DISPON	Display on	0	29h	PH	[05 29 00 1C]	
	Wait	wait 50 msec or more					
3	Video stream						

10.2 Power-OFF Sequence

No.		Function	DNC	Command /Parameter	Туре	Command/Parameter
1	DISPOFF	Display off	0	28h	PH	[05 28 00 06]
2	SLPIN	Sleep in	0	10h	PH	[05 10 00 2C]
	Wait	wait 120 msec or more				
3	Video signals	Video signals stop				
4	RESET	RESX off(low)				

▼ Power off

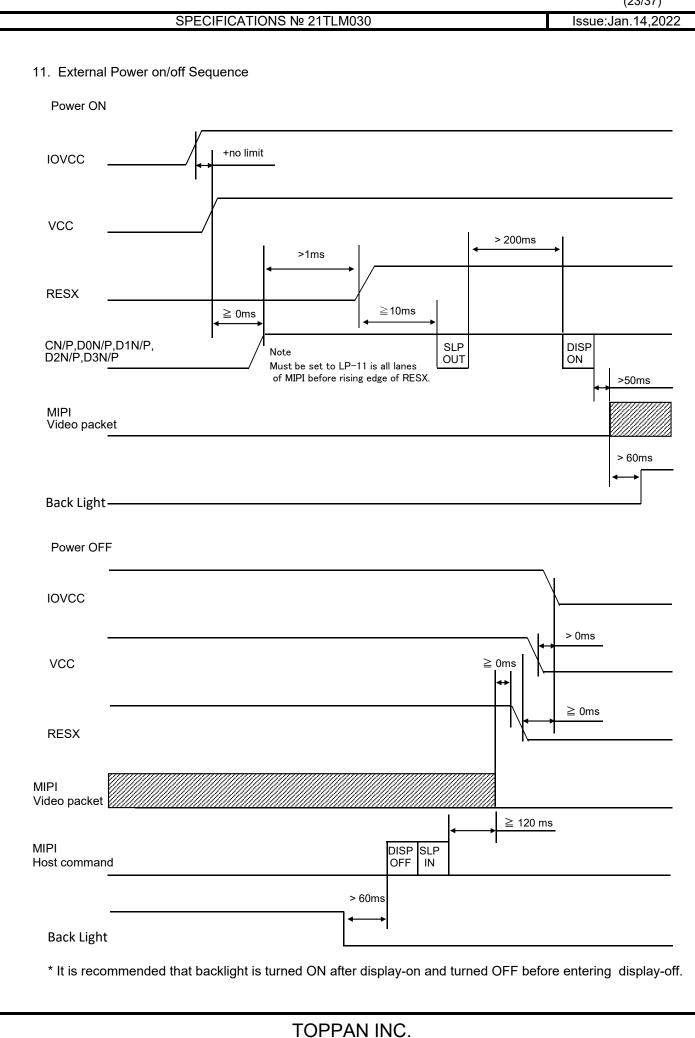
10.3 Sleep Sequence

No.	·	Function	DNC	Command /Parameter	Туре	Command/Parameter
1	SLPIN	Sleep in	0	10h	PH	[05 10 00 2C]
	Wait	wait 120 msec or more				
2	Video signals	Video signals stop				

10.4 Sleep Release Sequence

No.		Function	DNC	Command /Parameter	Type	Command/Parameter
1	Video signals	Video signals start				
	Wait	wait 120 msec or more]	
2	SLPOUT	Sleep out	0	11h	PH	[05 11 00 36]

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12. Characteristics

12.1 Optical Characteristics

(Measurement Condition)

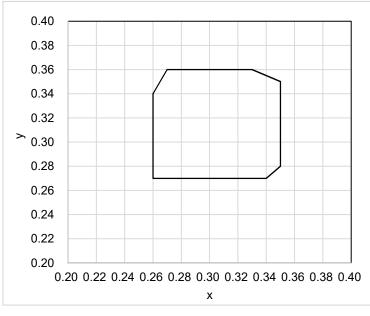
Measuring instruments: CS2000 (KONICA MINOLTA), LCD7200 (OTSUKA ELECTRONICS), EZcontrastXL88 (ELDIM)

Driving condition: VCC=2.9V, IOVCC=1.8V, VSS=0V, Optimized VCOMDC

Backlight: IL= 10.0 mA Measured temperature: Ta = 25° C

	Item	Symbol	Condition	MIN	TYP	MAX	Unit	Note №	Remark
Response time	Rise time + Fall time	TON + TOFF	[Data]= 00h → FFh →00h	-	-	100	ms	1	
	Backlight ON	CR	[Data]= FFh / 00h	350	700	-		2	
Contrast ratio	Backlight OFF			-	3.0	-			
	Left	θL	[Data]=	80	-	-	deg	3	
Viewing angle	Right	θR	FFh / 00h	80	-	-	deg		
/ie/	Up	φU	CR ≧10	80	-	-	deg		
	Down	φD		80	-	-	deg		
White	e Chromaticity	Х	[Data]= FFh	White chromaticity range				4	
		У							
Cent	er Brightness		[Data]= FFh	210	300	-	cd/m²	5	
Brigh	tness distribution		[Data]= FFh	70	-	-	%	6	
Burn	-in			No noticeable burn-in image shall be observed after 2 hours of window pattern display.			of	7	Defenses !!

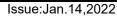
^{*} Note number 1 to 7: Refer to the APPENDIX of "Reference Method for Measuring Optical Characteristics and Performance".



White Chromaticity Range

(White Chromaticity Range)

Χ	у
0.26	0.34
0.26	0.27
0.34	0.27
0.35	0.28
0.35	0.35
0.33	0.36
0.27	0.36





12.2 Temperature Characteristics

(Measurement Condition)

Measuring instruments: CS2000 (KONICA MINOLTA), LCD7200 (OTSUKA ELECTRONICS)

Driving condition: VCC=2.9V, IOVCC=1.8V, VSS=0V, Optimized VCOMDC

Backlight: IL= 10.0 mA

Item		Symbol	Specif	Remark	
			Ta = -10 ℃	Ta = 70 ℃	
Response time	Rise time + Fall time	TON + TOFF	600 msec or less	80 msec or less	
Contrast ratio		CR	40 or more	40 or more	Backlight ON
Display Quality			No noticeable display d should be observed.	lefect or ununiformity	

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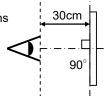
13. Criteria of Judgment

13.1 Defective Display and Screen Quality

Test Condition: Observed TFT-LCD monitor from front during operation with the following conditions

Driving Signal: Raster Patter (RGB, white, black) Signal condition: [Data]:00h, A8h, FFh (3steps)

Observation distance: 30 cm
Illuminance: 200 to 350 lx
Backlight: IL=10.0mA



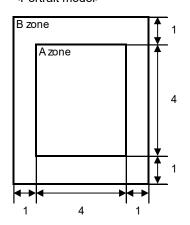
De	efect item	Defect content		Criteria
	Line defect	Black, white or color lir	e, 3 or more neighboring defective dots	Not exists
lity	Dot	Uneven brightness on	dot-by-dot base due to defective	Refer to table 1
)ua	Dot defect	TFT or CF, or dust is c	ounted as dot defect	
Š		(brighter dot, darker do	t)	
Display		High bright dot: Visible	through 2% ND filter at [Data]=00h	
Dis		Low bright dot: Visible	through 5% ND filter at [Data]=00h	
		Dark dot: Appear dark	through white display at [Data]=A8h	
		Invisible through 5% N	D filter at [Data]=00h	Acceptable
	Stain	Uneven brightness (wh	ite stain, black stain etc)	Invisible through 5% ND filter at Black screen. Invisible through 1% ND filter at other screen.
lity	Foreign	Point-like	0.25mm< φ	N=0
)ua	Foreign particle		0.20mm< φ ≦0.25mm	N≦2
n G			φ ≦0.20mm	Acceptable
Screen		Liner	3.0mm < length and 0.08mm < width	N=0
Sc			length \leq 3.0mm or width \leq 0.08mm	Acceptable
	Others			Use boundary sample
				for judgment when necessary

φ(mm): Average diameter = (major axis + minor axis)/2
Permissible number: N

Table1

Area	High bright dot	Low bright dot	Dark dot	Total	Criteria
А	0	2	2	3	Permissible distance between same color bright dots (includes neighboring dots): 3 mm or more
В	2	4	4	6	Permissible distance between same color high bright dots (includes neighboring dots): 5 mm or more
Total	2	4	4	7	

<Portrait model>



Division of A and B areas

B area: Active area

Dimensional ratio between A and B areas: 1: 4: 1

(Refer to the left figure)

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13.2 Screen and Other Appearance

Testing conditions

Observation distance: 30 cm

Illuminance: 1200 \sim 2000 lx

	Item	Criteria	Remark
	Flaw	Ignore invisible defect when the backlight is on.	Applicable area: Active area only
۳	Stain		(Refer to the section 3.2 Outward Form)
rize	Dirt		
ola	Dirt Bubble		
╚	Dust		
	Dent		
S	case	No functional defect occurs	
FF	PC	No functional defect occurs	

Item	Appearance	Criteria
Glass chipping	Corner area	Unit : mm $a \leq 3$ $b \leq 3$ $c \leq t \qquad (t: glass thickness)$ $a,b \leq 0.5 \text{ is acceptable}$ $n \leq 2$
	Others Progressive crack	$\begin{array}{c} \text{Unit:mm} \\ a \leqq 5 \\ b \leqq 1 \\ c \leqq t \qquad (\text{ t: glass thickness}) \\ a,b \leqq 0.5 \text{ is acceptable} \\ \text{Maximum permissible number} \\ \text{of chipping off on a side is 5.} \\ \\ \text{None} \end{array}$

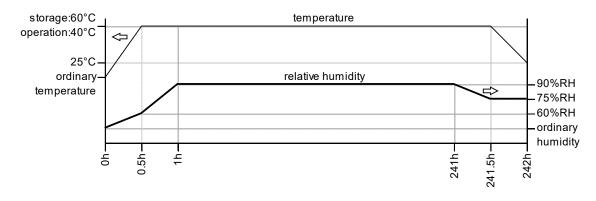
14. Reliability Test

Test item		Test condition		number of failures /
				number of examinations
Durability test	High temperature storage	Ta = 80°C	240hrs	0/3
	Low temperature storage	Ta = -30°C	240hrs	0/3
	High temperature &	Ta = 60℃, RH = 90%,	240hrs	0/3
	high humidity storage	non condensing	*	
	High temperature operation	Tp = 70°C	240hrs	0/3
ırak	Low temperature operation	Tp = -20°C	240hrs	0/3
۵	High temperature &	Tp = 40℃, RH = 90%,	240hrs	0/3
	high humidity operation	non condensing	*	
	Thermal shock storage	-30°C ↔ 80°C (30min / 30min)	100cycles	0/3
	Electrostatic discharge test	Confirms to EIAJ ED-4701/300, C=200pF,R=0Ω,V=±200V		0/3
şşt	(Non operation)	Each 3 times of discharge on and power supply		
al te		and other terminals.		
ents	Surface discharge test	C=250pF, R=100Ω, V=±8kV		0/3
J J	(Non operation)	Each 5 times of discharge in both polarities		
jo j		on the center of screen with the case		
env	Vibration test	Total amplitude 1.5mm, f=10∼55Hz,		0/3
<u>8</u>		X,Y,Z directions for each 2 hours		
Mechanical environmental test	Impact test	Use TOPPAN original jig (see next page) and		0/3
		make an impact with peak acceleration of 1000m/s ² for 6 msec		
		with half sine-curve at 3 times to each X, Y, Z directions		
		in conformance with JIS C 60068-2-27-2011.		
Packing test	Packing vibration-proof test	Acceleration of 19.6m/s ² with frequency of 10→55→10Hz,		0 / 1 packing
		X,Y, Zdirection for each 30 minutes.		
acl	Packing drop test	Drop from 75cm high.		0 / 1 packing
14		1 time to each 6 surfaces, 3 edges, 1 corner		

Note:Ta=ambient temperature

Tp=Panel temperature

% The profile of high temperature/humidity storage and High Temperature/humidity operation (Pure water of over 10M Ω ·cm shall be used.)



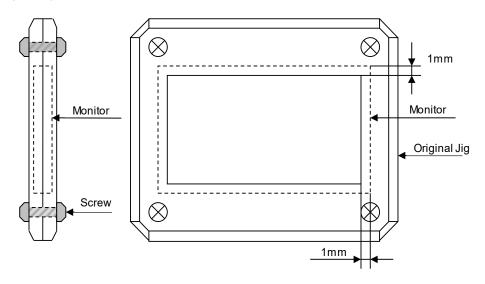
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Table2. Reliability Criteria

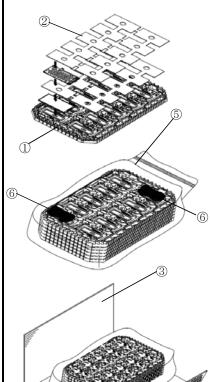
The parameters should be measured after leaving the monitor at the ordinary temperature for 24 hours or more after the test completion.

Item	Standard	Remark
Display quality	No visible abnormality shall be seen.	
	(Except for unevenness by Pol deterioration.)	
Contrast ratio	40 or more	Backlight ON

TOPPAN Original Jig



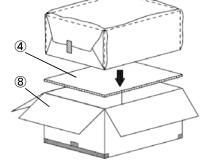
15. Packing specifications



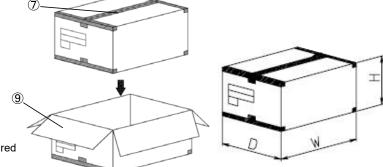
Step 1. Each product is to be placed in one of the cut-outs of the tray with its the LCD display side facing upward, and foam-sheet is put on products. (10 pieces / 1 step) Each product is to be placed on the FOAM SHEET with its the LCD display side facing downward, and foam-sheet is put on products. (20 pieces / tray)

- Step 2. Each tray filled with products is to be piled up in stack of 5. One empty tray is to be put on the stack of 5 trays.
- Step 3. 2 packs of moisture absorbers are to be placed on the top tray as shown in the drawing. Put piled trays into a sealing bag.
- Step 4. Vacuum and seal the sealing bag with the vacuum sealing machine.
- Step 5. The pilled trays are to be wrapped with a bubble cushioning sheet, and to be fixed with adhesive tape.
- Step 6. A corrugated board is to be placed in the bottom of an outer carton. The wrapped trays are to be put on the corrugated board in the outer carton. Another corrugated board is to be placed on the top of the inserted carton box.
- Step 7. The outer carton is to be sealed in H-shape with packing tape as shown in the drawing. The model number, quantity of products, and shipping date are to be printed on the 2 opposite sides of the outer carton with black ink. If necessary, shipping labels or impression markings are to be put on the outer carton.
- Step 8. The outer carton is to be inserted into an extra outer carton with same orientation.
- Step 9. The extra outer carton is to be sealed with in H-shape with packing tape as shown in the drawing.

The model number, quantity of products, and shipping date are to be printed on the 2 opposite sides of the extra outer carton with black ink.



If necessary, shipping labels or impression markings are to be put on the extra outer carton.



* Maximum piling up: 8 cartons Remark: The return of packing materials is not required

Packing item name		Specs., Material	
1	TRAY	A-PET (Antistatic)	
2	FOAM SHEET	Antistatic Polyethylene	
3	B SHEET A	Antistatic air babble sheet	
4	INNER BOARD	Corrugated cardboard	
(5)	SEALING BAG		
6	DRIER	Moisture absorber	
7	PACKING TAPE		
8	OUTER CARTON	Corrugated cardboard	
9	EXTRA OUTER CARTON	Corrugated cardboard	

Dimension of extra outer carton				
D : Approx.	(337mm)			
W : Approx.	(618mm)			
H : Approx.	(179mm)			
Quantity of products packed in one carton:100				
Gross weight : Approx	5.7 kg			

16. Handling Instruction

16.1 Cautions for Handling LCD panels



Caution

- (1) Do not make an impact on the LCD panel glass because it may break and you may get injured from it.
- (2) If the glass breaks, do not touch it with bare hands.
 (Fragment of broken glass may stick you or you cut yourself on it.
- (3) If you get injured, receive adequate first aid and consult a medial doctor.
- (4) Do not let liquid crystal get into your mouth. (If the LCD panel glass breaks, try not let liquid crystal get into your mouth even toxic property of liquid crystal has not been confirmed.)
- (5) If liquid crystal adheres, rinse it out thoroughly.(If liquid crystal adheres to your cloth or skin, wipe it off with rubbing alcohol or wash it thoroughly with soap.If liquid crystal gets into eyes, rinse it with clean water for at least 15 minutes and consult an eye doctor.
- (6) If you scrap this products, follow a disposal standard of industrial waste that is legally valid in the community, country or territory where you reside.
- (7) Do not connect or disconnect this product while its application products is powered on.
- (8) Do not attempt to disassemble or modify this product as it is precision component.
- (9) If a part of soldering part has been exposed, and avoid contact (short-circuit) with a metallic part of the case etc. about FPC of this model, please. Please insulate it with the insulating tape etc. if necessary. The defective operation is caused, and there is a possibility to generation of heat and the ignition.
- (10) Since excess current protection circuit is not built in this TFT module, there is the possibility that LCD module or peripheral circuit become feverish and burned in case abnormal operation is generated. We recommend you to add excess current protection circuit to power supply.
- (11) The devices on the FPC are damageable to electrostatic discharge, because the terminals of the devices are exposed. Wear grounded wrist-straps and use electrostatic neutralization blowers to prevent static charge and discharge when handling the TFT monitors. Designate an appropriate operating area, and set equipment, tools, and machines properly when handling this product.

$\underline{\Lambda}$

Caution

This mark is used to indicate a precaution or an instruction which, if not correctly observed, may result in bodily injury, or material damages alone.

16.2 Precautions for Handling

- Wear finger tips at incoming inspection and for handling the TFT monitors to keep display quality and keep the working area clean.
 Do not touch the surface of the monitor as it is easily scratched.
- Wear grounded wrist-straps and use electrostatic neutralization blowers to prevent static charge and discharge when handling the TFT monitors as the LED in this TFT monitors is damageable to electrostatic discharge.
 Designate an appropriate operating area, and set equipment, tools, and machines properly when handling this product.
- 3) Avoid strong mechanical shock including knocking, hitting or dropping to the TFT monitors for protecting their glass parts. Do not use the TFT monitors that have been experienced dropping or strong mechanical shock.
- 4) Do not use or storage the TFT monitors at high temperature and high humidity environment.

 Particularly, never use or storage the TFT monitors at a location where condensation builds up.
- 5) Avoid using and storing TFT monitors at a location where they are exposed to direct sunlight or ultraviolet rays to prevent the LCD panels from deterioration by ultraviolet rays.
- 6) Do not stain or damage the contacts of the FPC cable . FPC cable needs to be inserted until it can reach to the end of connector slot. During insertion, make sure to keep the cable in a horizontal position to avoid an oblique insertion. Otherwise, it may cause poor contact or deteriorate reliability of the FPC cable.
- 7) The FPC cable is a design very weak to the bend and the pull as it is fixed with the tape. Do not bend or pull the FPC cable or carry the TFT monitor by holding the FPC cable.
- Peel off the protective film on the TFT monitors during mounting process.
 Refer to the section 16.5 on how to peel off the protective film.
 We are not responsible for electrostatic discharge failures or other defects occur when peeling off the protective film.
- 9) It is recommended to employ the structure of which polarizer peripheral area of LCD panel being pressed by cushioning materials, in order to prevent a cause of display brightness unevenness.

16.3 Precautions for Operation

- 1) Since this TFT monitors are not equipped with light shielding for the driver IC, do not expose the driver IC to strong lights during operation as it may cause functional failures.
- 2) In case of powering up or powering off this LCD module, be sure to comply the sequence as instructed in this specification.
- 3) Do not plug in or out the FPC cable while power supply is switch on. Plug the FPC cable in and out while power supply is switched off.
- 4) Do not operate the TFT monitors in the strong magnetic field. It may break the TFT monitors.
- 5) Do not display a fixed image on the screen for a long time. Use a screen-saver or other measures to avoid a fixed image displayed on the screen for a long time. Otherwise, it may cause burn-in image on the screen due the characteristics of liquid crystal.

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16.4 Storage Condition for Shipping Cartons

(Storage environment)

Temperature 0 to 40°C
 Humidity 60%RH or less

No-condensing occurs under low temperature with high humidity condition.

Atmosphere No poisonous gas that can erode electronic components and/or

wiring materials should be detected.

Time period 1 year

Unpacking To prevent damages caused by static electricity, anti-static precautionary measures

(e.g. earthing, anti-static mat) should be implemented.

After unpack, keep product in the appropriate condition,

otherwise bubble seal of Protective film may be printed on Polarizer.

Maximum piling up 7 cartons

*Conditions to storage after unpacking

(Storage environment)

Temperature 0 to 40°CHumidity 60%RH or less

No-condensing occurs under low temperature with high humidity condition.

Atmosphere No poisonous gas that can erode electronic components and/or

wiring materials should be detected.

Time period 1 year (Shelf life)

Others Keep/ store away from direct sunlight

Storage goods on original tray made by TOPPAN.

16.5 Precautions for Peeling off the Protective film

The followings work environment and work method are recommended to prevent the TFT monitors from static damage or adhesion of dust when peeling off the protective films.

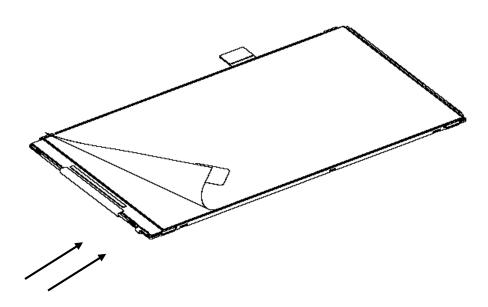
A) Work Environment

- a) Humidity: 50 to 70 %RH, Temperature15 to 27 °C
- b) Operators should wear conductive shoes, conductive clothes, conductive finger tips and grounded wrist-straps. Use an electrostatic neutralization blower.
- c) Anti-static treatment should be implemented to work area's floor.
 Use a room shielded against outside dust with sticky floor mat laid at the entrance to eliminate dirt.

B) Work Method

The following procedures should taken to prevent the driver ICs from charging and discharging.

- a) Use an electrostatic neutralization blower to blow air on the TFT monitors to its lower right when FPC is placed at the bottom.
 Optimize direction of the blowing air and the distance between the TFT monitors and the electrostatic neutralization blower.
- b) Pull the R tape slowly (taking more than 2 seconds to complete) towards the operator to remove the protection film.



Blower wind direction (Set an ion blower with its adequate conditions.)

16.6 Warranty

TOPPAN is only liable to defective goods which is stored and used under the condition complying with this specifications and returned within 1 (one) year.

Warranty caused by manufacturing defect shall be conducted by replacement of goods or refundment at unit price.

APPENDIX

Reference Method for Measuring Optical Characteristics and Performance

1. Measurement Condition (Backlight ON)

Measuring instruments: CS2000 (KONICA MINOLTA), LCD7200(OTSUKA ELECTRONICS), EZcontrastXL88 (ELDIM)

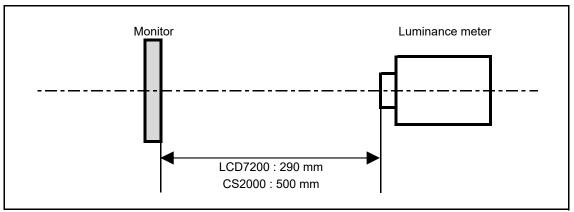
Driving condition: Refer to the section "Optical Characteristics"

Measured temperature: 25°C unless specified

Measurement system: See the chart below. The luminance meter is placed on the normal line of measurement system.

Measurement point: At the center of the screen unless otherwise specified

Dark box at constant temperature

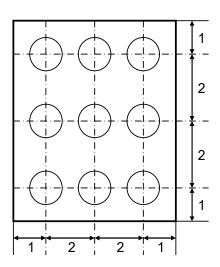


^{*}Measurement is made after 30 minutes of lighting of the backlight.

Measurement point: At the center point of the screen

Brightness distribution: 9 points shown in the following drawing.

<Portrait model>



Dimensional ratio of active area

Backlight IL=10.0mA

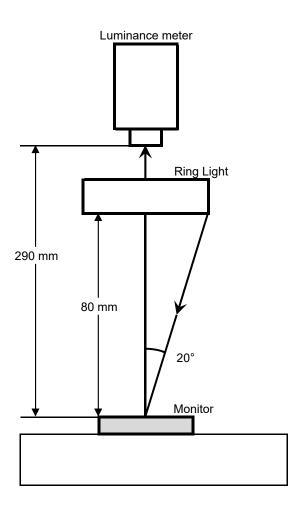
Measurement Condition (Contrast ratio Backlight OFF only)

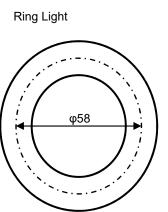
Measuring instruments: LCD7200(OTSUKA ELECTRONICS), Ring Light (40,000 lx, ϕ 58)

Driving condition: Refer to the section "Optical Characteristics"

Measured temperature: 25°C unless specified Measurement system: See the chart below.

Measurement point: At the center of the screen unless otherwise specified





SPECIFICATIONS № 21TLM030				Issue:Jan.14,2022	
2. Test	Method				
lotice	Item	Test method	Measuring	Remark	
			instrument		
1	Response time	Measure output signal waveform by the luminance meter when raster of window pattern is changed from white to black and from black to white. Black 100% 90%	LCD7200	Black display [Data]=00h White display [Data]=FFh TON Rise time TOFF Fall time	
2	Contrast ratio	Measure maximum luminance Y1([Data]=FFh) and minimum luminance Y2([Data]=00h) at the center of the screen by displaying raster or window pattern. Then calculate the ratio between these two values. Contrast ratio = Y1/Y2 Diameter of measuring point: 7.8mmφ(CS2000)	CS2000 LCD7200	Backlight ON Backlight OFF	
3	Viewing angle Horizontalθ Verticalφ	Diameter of measuring point: 3mmφ(LCD7200) Move the luminance meter from right to left and up and down and determine the angles where contrast ratio is 10.	EZcontrastXL88		
4	White chromaticity	Measure chromaticity coordinates x and y of CIE1931 colorimetric system at [Data] = FFh Color matching function: 2°view measurement angle: 1°	CS2000		
5	Center brightness	Measure the brightness at the center of the screen.	CS2000		
6	Brightness distribution	(Brightness distribution) = 100 x B/A % A: max. brightness of the 9 points B: min. brightness of the 9 points	CS2000		
7	Burn-in	Visually check burn-in image on the screen after 2 hours of "window display" ([Data]=00h/FFh).		At optimized VCOMDC	





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DATA MODUL AG Landsberger Straße 322 DE-80687 Munich Phone: +49-89-56017-0 DATA MODUL WEIKERSHEIM GMBH Lindenstraße 8 DE-97990 Weikersheim Phone: +49-7934-101-0



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