



# SPECIFICATION



EL133UR1 13.3", 1600x1200, TTL

Version: 1.0 Date: 27.10.2020

Note: This specification is subject to change without prior notice

www.data-modul.com



Version: 1.0

# **Technical Specification**

# MODEL NO: EA2220-NCC (EL133UR1)

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Customer's Confirmation

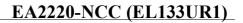
Customer

Date

Ву

E Ink's Confirmation

Approve By	Sean Chin
Confirmed By	Miso sai
Prepared By	for Li





# **Revision History**

Rev.	Issued Date	Revised Contents			
0.1	2020.09.28	Tentative			
1.0	2020.10.27	Update 3. Mechanical Specifications			
		Update 5. Input/Output Interface			
		Update 6. Power Sequence			
		Jpdate 7. Electrical Characteristics			
		Jpdate 8. Optical Characteristics			

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# **TECHNICAL SPECIFICATION**

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## EA2220-NCC (EL133UR1)

## Application

EA2220-NCC is a reflective electrophoretic E Ink<sup>®</sup> technology display module based on active matrix TFT substrate. The diagonal length of active area is 13.3" and contains 1600 x 1200 pixels. The display is capable to display 1-bit black, white and red images depending on the display controller and the associated lookup table used.

#### 2. Features

Highlight Red

1.

- High contrast
- ➢ High reflectance
- Ultra wide viewing angle
- Pure reflective mode
- ➢ Bi-stable
- Commercial temperature range
- Landscape, portrait mode
- Antiglare hard-coated front-surface

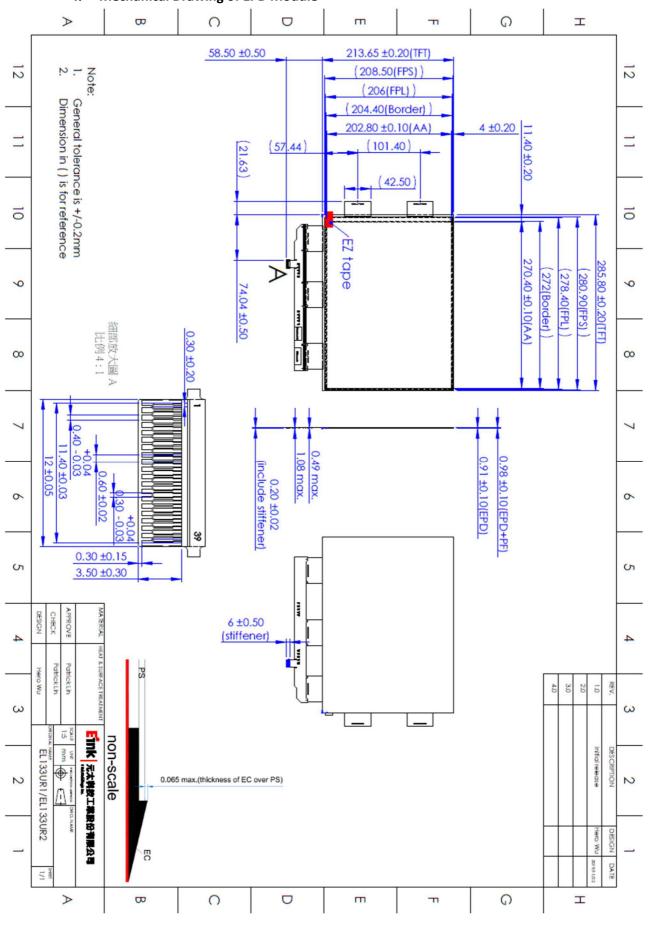
#### 3. Mechanical Specifications

Parameter	Specifications		Remark
Screen Size	13.3	Inch	
Display Resolution	1600 (H)×1200(V)	Pixel	DPi: 150
Active Area	270.4 (H)×202.8 (V)	mm	
Pixel Pitch	0.169 (H)×0.169 (V)	mm	Square
Outline Dimension	285.8(W)×213.65(H)×0.91(D)	mm	Without masking film
Module Weight	110 ± 5.5	g	

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4. Mechanical Drawing of EPD Module





Input/Output Interface 5.

5.1 Pin Assignment

35

36

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VSS

VSS

VSS

VSS

VSS

Р

Р

Р

Р

Р

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Pin	Signal         I/O         Description			Remark
1	VNEG	Р	Negative power supply source driver	
2	VPOS	Р	Positive power supply source driver	
3	VSS	Р	Ground	
4	VDD	Р	Digital power supply drivers	
5	XCL	Ι	Clock source driver	
6	XLE	Ι	Latch enable source driver	
7	XOE	Ι	Output enable source driver	
8	VSS	Р	Ground	
9	VSS	Р	Ground	
10	NC	-	No Connection	
11	XSTL	Ι	Start pulse source driver	
12	D0	Ι	Data signal source driver	
13	D1	Ι	Data signal source driver	
14	D2	Ι	Data signal source driver	
15	D3	Ι	Data signal source driver	
16	D4	Ι	Data signal source driver	
17	D5	Ι	Data signal source driver	
18	D6	Ι	Data signal source driver	
19	D7	Ι	Data signal source driver	
20	VSS	Р	Ground	
21	NC	-	No Connection	
22	VCOM	Р	Common connection	
23	VGH	Р	Positive power supply gate driver	
24	VGL	Р	Negative power supply gate driver	
25	NC	-	No Connection	
26	NC	-	No Connection	
27	NC	-	No Connection	
28	MODE1	Ι	Output mode selection gate driver	
29	VSS	Р	Ground	
30	VSS	Р	Ground	
31	VSS	Р	Ground	
32	SPV	Ι	Start pulse gate driver	
33	CKV	Ι	Clock gate driver	
34	BORDER	Ι	Border connection	

Ground

Ground

Ground

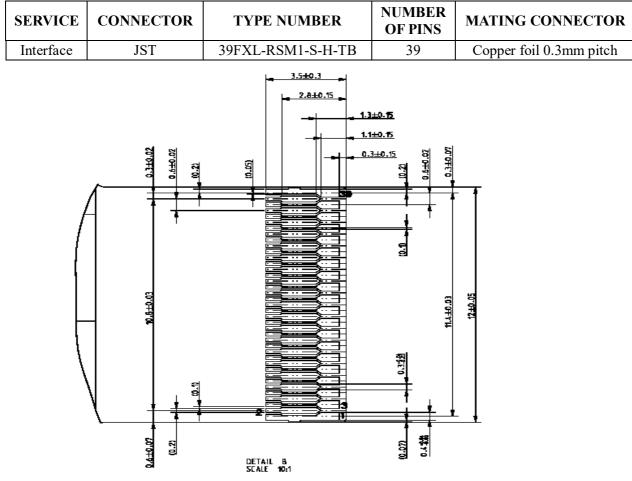
Ground

Ground

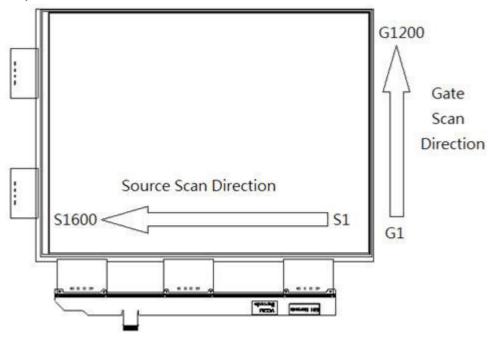
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#### 5-2) Panels Electrical Connection



#### 5-3) Panel Scan Direction



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5-4) The relationship of input data and output

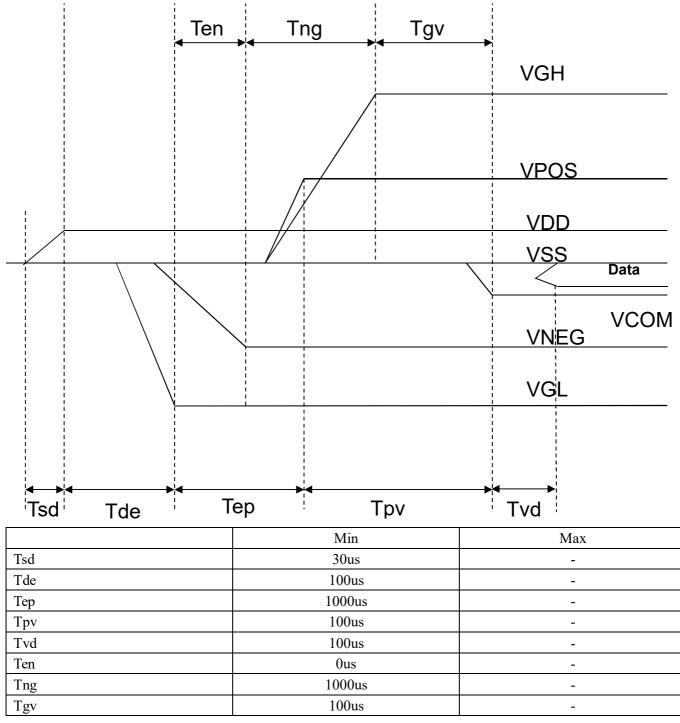
Output	S1	S2	S3	S4
	D7	D5	D3	D1
Data	D6	D4	D2	D0

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6. Power Sequence
Power Rails must be sequenced in the following order :
1. VSS → VDD → VNEG → VPOS (Source driver) → VCOM

2. VSS  $\rightarrow$  VDD  $\rightarrow$  VGL  $\rightarrow$  VGH (Gate driver)

### POWER ON



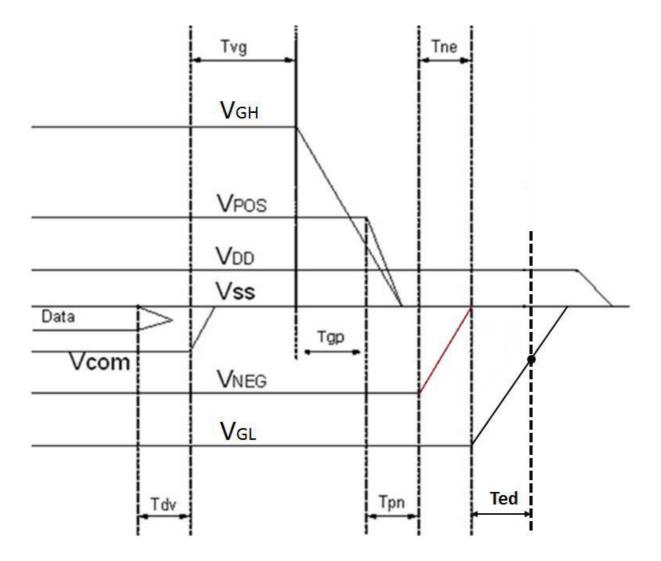
Note : Voltage scales here are just for reference. For valid voltage scales, please refer to the section of Panel DC characteristics.

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**POWER OFF** 

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	Min	Max	Remark
Tdv	100µs	-	-
Tvg	0µs	-	-
Tgp	0µs	-	-
Tpn	0µs	-	-
Tne	0µs	-	-
Ted	0.5s	-	Discharged point @ -7.4 Volt

Note : Voltage scales here are just for reference. For valid voltage scales, please refer to the section of Panel DC characteristics.



#### 7. Electrical Characteristics

<b>-</b> 1)	. 1 1 .		. •
(/-I)	Absolute	maximum	rating
• - /			

Parameter	Symbol	Rating	Unit
Logic Supply Voltage	VDD	-0.3 to +7	V
Positive Supply Voltage	V <sub>POS</sub>	-0.3 to +18	V
Negative Supply Voltage	V <sub>NEG</sub>	+0.3 to -18	V
Max .Drive Voltage Range	$V_{POS}$ - $V_{NEG}$	36	V
Supply Voltage	VGH	-0.3 to +55	V
Supply Voltage	VGL	-32 to +0.3	V
Supply Range	VGH-VGL	-0.3 to +55	V
Operating Temp. Range	TOTR	0 to +40	°C
Storage Temperature	TSTG	-25 to +60	°C

Note: Maximum ratings are those values beyond which damages to the device may occur.
Functional operation should be restricted to the limits in the Electrical Characteristics chapter

#### 7-2) Panel DC characteristics

The following specifications apply for: VDD = 3.3V,  $TA = 25^{\circ}C$ 

Parameter	symbol	conditions	Min	Тур	Max	Unit
Signal ground	Vss			0		V
Legie veltege europy	Vdd		2.75	3.3	3.6	V
Logic voltage supply	lvdd	Vdd=3.3V		3.4	8	mA
Cata pagativa aupply	GVgl		-19	-20	-21	V
Gate negative supply	Glgl	Gvgl=-20V		1.5	4	mA
Cata Dagitiya gumphy	GVgh		26	27	28	V
Gate Positive supply	Glgh	GVgh=27V		1.5	4	mA
Course perstive cumply	Vneg		-15.4	-15	-14.6	V
Source negative supply	Ineg	Vneg=-15V		6.5	135	mA
	Vpos	Vpos1=15V	14.6	15	15.4	V
Source Positive supply		Vpos2		Adjusted		V
	lpos	Vpos=15V		6.3	150	mA
Border supply	Vcom		-3.5	Adjusted	-0.3	V
Asymmetry source	Vasm	Vpos+Vneg	-800		800	mV
	Vcom		-3.5	Adjusted	-0.3	V
Common voltage(DC)	lcom			0.8	1	mA
Maxmum Power panel	Pmax				4508	mW
Typcal power panel	Ptyp			275		mW
Standby power panel	Pstby				0.4	mW

> Note: The above data is measured by using volt-ohm meter, and extract the max value.

> Note: The VPOS is composed of VPOS 1 & VPOS2.

VPOS2 must follow E Ink waveform definition of each FPL batch by the tolerance of  $\pm$  0.2V.

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## EA2220-NCC (EL133UR1)

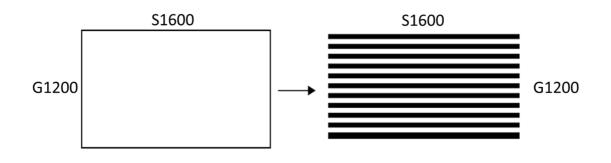
- The Maximum power consumption is measured using associated 25C waveform with following pattern transition: from full white pattern to pattern of repeated 1 consecutive white scan lines followed by 1 consecutive black scan lines. (Note 7-1)
- The Typical power consumption is measured using associated 25C waveform with following pattern transition: from full white pattern to black, white and red stripe pattern.(Note 7-2)
- > The standby power is the consumed power when the panel controller is in standby mode. The standby power is the consumed power when the panel controller is in standby mode.
- The listed electrical/optical characteristics are only guaranteed under the controller & waveform provided by E Ink.
- > Vcom is recommended to be set in the range of assigned value  $\pm 0.1V$

### Note 7-1

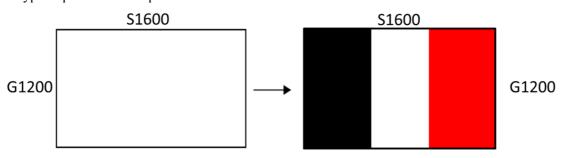
The maximum power consumption

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Note 7-2 The Typical power consumption



#### 7-3) Refresh Rate

The module EL133UR1 is applied at maximum screen refresh rate of 75Hz.

	Min	Max
Refresh Rate	-	75Hz

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## EA2220-NCC (EL133UR1)

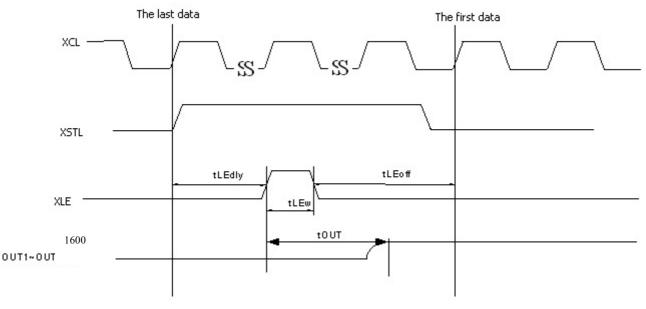
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#### 7-4 ) Panel AC characteristics

VDD=2.73V to 3.6V, unless otherwise specified.

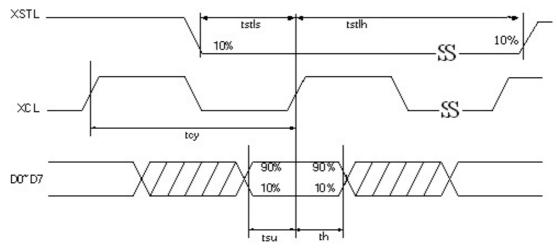
8. Parameter	Symbol	Min.	Тур.	Max.	Unit
Clock frequency	fckv	-	-	200	kHz
Minimum "L" clock pulse width	twL	0.5	-	-	us
Minimum "H" clock pulse width	twH	0.5	-	-	us
Clock rise time	trckv	-	-	100	ns
Clock fall time	tfckv	-	-	100	ns
SPV setup time	tSU	100	-	twH-100	ns
SPV hold time	tH	100	-	twH-100	ns
Pulse rise time	trspv	-	-	100	ns
Pulse fall time	tfspv	-	-	100	ns
Clock XCL cycle time	tcy	16.7	-	-	ns
D0 D7 setup time	tsu	8	-	-	ns
D0 D7 hold time	th	8	-	-	ns
XSTL setup time	tstls	8.35	-	-	ns
XSTL hold time	tstlh	8.35	-	-	ns
XLE on delay time	tLEdly	40	-	-	ns
XLE high-level pulse width (When VDD=2.73V to 3.6V)	tLEw	40	-	-	ns
XLE off delay time	tLEoff	200	-	-	ns
Output setting time to +/- 30mV(C <sub>load</sub> =200pF)	tout	-	-	12	us
Frame Sync Length (Mode 1)	t1	1			1 line

#### OUTPUT LATCH CONTROL SIGNALS

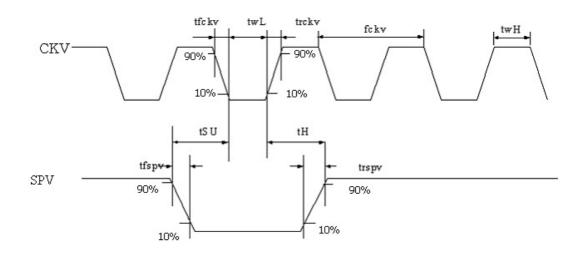


CLOCK & DATA TIMING

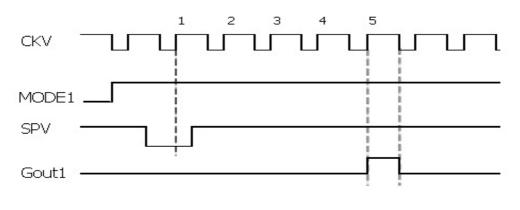




CKV & SPV TIMING



GATE OUTPUT TIMING

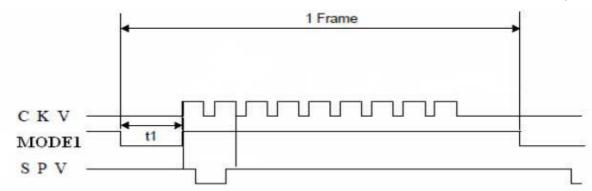


Frame Sync Length

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### EA2220-NCC (EL133UR1)



### Note: First gate line on timing

After 5CKV , gate line is on .





#### 8. **Optical Characteristics**

#### 8-1) Specification

Measurements are made with that the illumination is under an angle of 45 degrees, the detector is perpendicular unless otherwise specified.

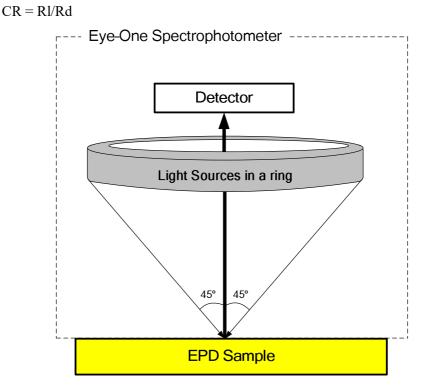
Symbol	Parameter	Conditions	Temperature	Min	Тур.	Max	Unit	Note
R	Reflectance	White	25°C	30	33	-	%	Note 8-1
CR	Contrast Ratio	-	25°C	10	15	-		-
RS_L*	Red State L*value	Red	25°C	24	26			Note 8-1
RS_a	Red State a* value	Red	25°C	34	39	-		Note 8-1
Tupdate_RS	Update time	Red	25°C		18		sec	
RS_a	Red State a* value	Red	0 °C	30	35			Note 8-1

WS: White state, DS: Dark state, RS: Red state

Note 8-1 : Luminance meter : Eye – One Pro Spectrophotometer

#### 8-2) Definition of contrast ratio

The contrast ratio (CR) is the ratio between the reflectance in a full white area (Rl) and the reflectance in a dark area (Rd) :



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#### 8-3) Reflection Ratio

The reflection ratio is expressed as :

 $R = Reflectance Factor_{white board} \quad x \quad (L_{center} / L_{white board})$ 

 $L_{center}$  is the luminance measured at center in a white area.  $L_{white board}$  is the luminance of a standard white board. Both are measured with equivalent illumination source. The viewing angle shall be no more than 2 degrees.

### EA2220-NCC (EL133UR1)

#### 9. Handling, Safety and Environmental Requirements and Remark

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#### WARNING

The display glass may break when it is dropped or bumped on a hard surface. Handle with care. Should the display break, do not touch the electrophoretic material. In case of contact with electrophoretic material, wash with water and soap.

#### CAUTION

The display module should not be exposed to harmful gases, such as acid and alkali gases, which corrode electronic components.

Disassembling the display module can cause permanent damage and invalidate the warranty agreements.

IPA solvent can only be applied on active area and the back of a glass. For the rest part, it is not allowed.

#### **Mounting Precautions**

(1) It's recommended that you consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module.

(2) It's recommended that you attach a transparent protective plate to the surface in order to protect the EPD.

Transparent protective plate should have sufficient strength in order to resist external force.

(3) You should adopt radiation structure to satisfy the temperature specification.

(4) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the PS at high temperature and the latter causes circuit break by electro-chemical reaction.

(5) Do not touch, push or rub the exposed PS with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of PS for bare hand or greasy cloth. (Some cosmetics deteriorate the PS)

(6) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach the PS. Do not use acetone, toluene and alcohol because they cause chemical damage to the PS.

(7) Wipe off saliva or water drops as soon as possible. Their long time contact with PS causes deformations and color fading.

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Data sheet status						
Product specification	This data sheet contains final product specifications subjected to changes without notice.					
Limiting values						
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.						
Application information						

Where application information is given, it is advisory and does not form part of the specification.



#### 10. Reliability Test

	TEST	CONDITION	REMARK
1	High Temperature Storage	Ta= 60°C 40% RH, 240Hrs	(Test in White pattern)
2	Low Temperature Storage	Ta= -25°C, 240Hrs	(Test in White pattern)
3	High Temperature Operation	Ta= 40°C 35% RH, 240Hrs	
4	Low Temperature Operation	Ta= 0°C, 240Hrs, 240Hrs	
5	High-Temperature, High- Humidity Operation	T = +40°C, RH = 80%, 168Hrs	
6	High-Temperature, High- Humidity Storage	T = +50°C, RH = 80%, 240Hrs	(Test in White pattern)
7	Temperature Cycle	-25°C(30 min) ~60°C(30 min), 50 cycle, 1Hr/cycle	(Test in White pattern )
8	Electrostatic Discharge	(Machine model)+/- 200V 0Ω, 200pF	Non-operation

Actual EMC level to be measured on customer application.

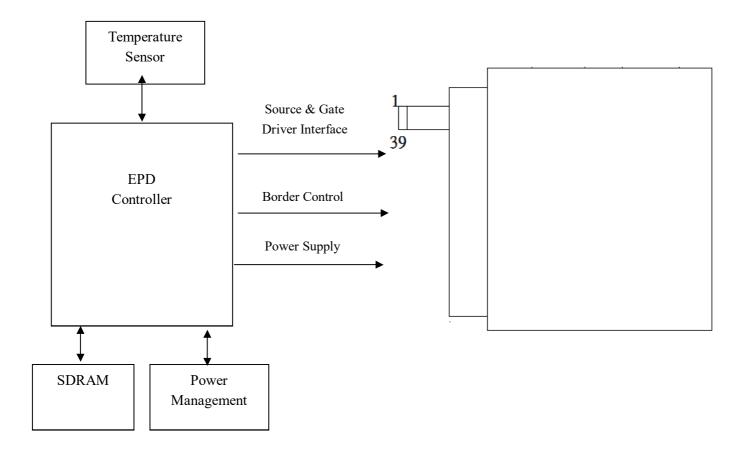
Note: The protective film must be removed before temperature test.

### < Criteria >

In the standard conditions, there is not display function NG issue occurred. All the cosmetic specification is judged before the reliability stress.



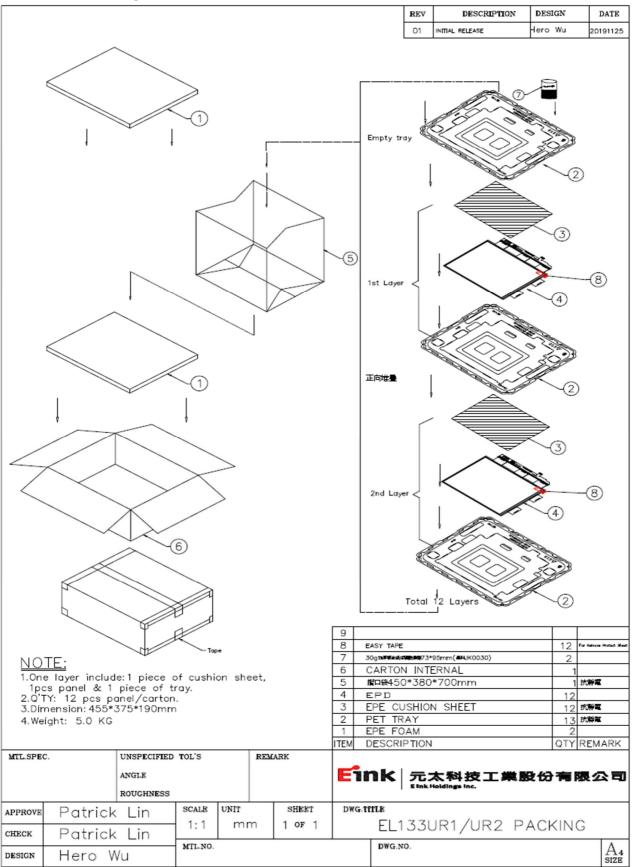
#### 11. Block Diagram



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