



SPECIFICATION



P270HVN01.0

27" – FHD – LVDS

Version: 4.4.2

Date: 11.08.2021

Note: This specification is subject to change without prior notice

Model Name: P270HVN01.0

Issue Date : 2021/08/11

() Preliminary Specifications

(*) Final Specifications

Customer Signature	Date	AUO Display Plus	Date
Approved By <hr/>		Approval by PM Director CT Wu <hr/>	
Note		Reviewed by RD Director Lamy Chen <hr/>	
		Reviewed by Project Leader Wallace Ting <hr/>	
		Prepared by PM Sophie Teng <hr/>	

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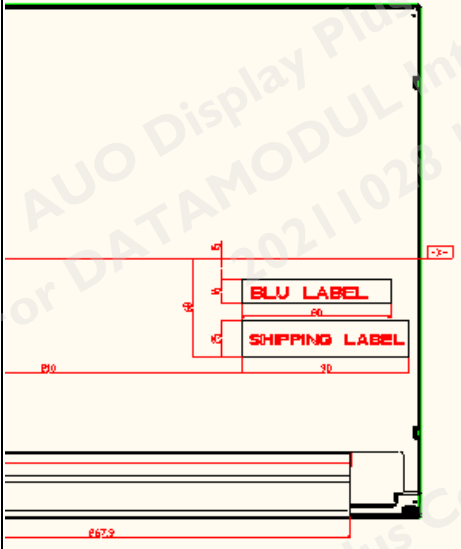
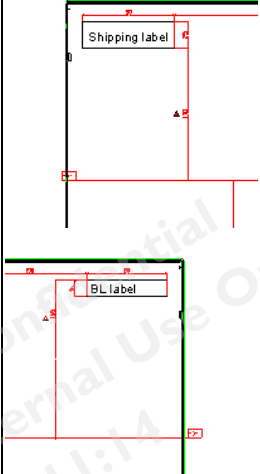
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Content

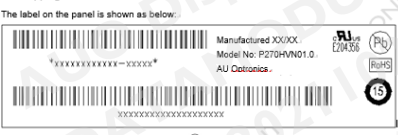







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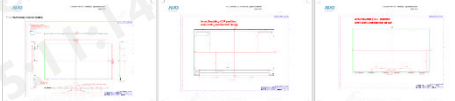
Records of Revision

Version and Date	Page	Old description	New Description	Remark
0.1 2012/05/10		First Version		
0.2 2012/06/15	14,18		To Define TFT LCD power spec To Define BL Power spec	
0.3 2012/06/20	6		To Define Panel Weight	
0.3 2012/06/20	31		<p>To Change the Label position. BLU Label is at the upper right (corner). Shipping label is at the the upper left (corner).</p> 	
0.4 2012/06/29	14	Verison 0.3 , the absolute VDD maximum is 5.5V of the module	Verison 0.4 , Update the absolute VDD maximum is 6.0V of the module	
0.5 2012/07/12	6, 7, 14,	In Verison 0.4 , Surface Treatment , Color/Chromaticity , absolute rating as TBD °	To Define the Surface Treatment To Define Color / Chromaticity (should be confirm again) To Define the Absolute Maximum Ratings of BL unit	
0.5 2012/07/12	23,19	In Verison 0.4 , the Max clock is 85MHz.In Verison 0.4 , the VLB minmun doesn't defined.	To upgrade the Max clock frequency as 90MHz. To define the VLB minmum voltage .	
0.6 2012/07/25	6		To correct the model name as P270HVN01.0	

0.7 2012/08/20	6	<table border="1"> <tr> <td>[Watt]</td> <td>29.34 watt VDD line : PDD (typ), All white pattern at 60Hz = 5.94 W LED line : PBLU (typ) = 23.4 W</td> </tr> </table>	[Watt]	29.34 watt VDD line : PDD (typ), All white pattern at 60Hz = 5.94 W LED line : PBLU (typ) = 23.4 W	<p>To modify the Panel Power Consumption</p> <table border="1"> <tr> <td>[Watt]</td> <td>27.72 watt VDD line : PDD (typ), All white pattern at 60Hz = 5.94 W LED line : PBLU (typ) = 21.78 W</td> </tr> </table>	[Watt]	27.72 watt VDD line : PDD (typ), All white pattern at 60Hz = 5.94 W LED line : PBLU (typ) = 21.78 W																																																					
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0.7 2012/08/20	7	<table border="1"> <tr><td>Red x</td><td>0.612</td><td>0.642</td><td>0.672</td></tr> <tr><td>Red y</td><td>0.304</td><td>0.334</td><td>0.364</td></tr> <tr><td>Green x</td><td>0.292</td><td>0.322</td><td>0.352</td></tr> <tr><td>Green y</td><td>0.594</td><td>0.624</td><td>0.654</td></tr> <tr><td>Blue x</td><td>0.126</td><td>0.156</td><td>0.186</td></tr> <tr><td>Blue y</td><td>0.030</td><td>0.060</td><td>0.090</td></tr> </table>	Red x	0.612	0.642	0.672	Red y	0.304	0.334	0.364	Green x	0.292	0.322	0.352	Green y	0.594	0.624	0.654	Blue x	0.126	0.156	0.186	Blue y	0.030	0.060	0.090	<p>To modify the Color / Chromaticity Coordinates (CIE)</p> <table border="1"> <tr><td>Red x</td><td>0.615</td><td>0.645</td><td>0.675</td></tr> <tr><td>Red y</td><td>0.300</td><td>0.330</td><td>0.360</td></tr> <tr><td>Green x</td><td>0.285</td><td>0.315</td><td>0.345</td></tr> <tr><td>Green y</td><td>0.590</td><td>0.620</td><td>0.650</td></tr> <tr><td>Blue x</td><td>0.124</td><td>0.154</td><td>0.184</td></tr> <tr><td>Blue y</td><td>0.034</td><td>0.064</td><td>0.094</td></tr> </table>	Red x	0.615	0.645	0.675	Red y	0.300	0.330	0.360	Green x	0.285	0.315	0.345	Green y	0.590	0.620	0.650	Blue x	0.124	0.154	0.184	Blue y	0.034	0.064	0.094									
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0.7 2012/08/20	13	<p>VDD minimum is 0V LED forward Voltage variation(ΔV_f) is 6V</p>	<p>To modify the VDD minimum as - 0.3V LED forward Voltage variation(ΔV_f) as 3V</p>																																																									
1.0 2012/10/08	6	<p>The 0.7 version power consumption is 27.72W</p>	<p>Update the power consumption 26.88W</p>																																																									
	6		<p>TCO Compliance : TCO 6.0</p>																																																									
	14		<p>To Define Max. Value IDD1 and PDD1</p>																																																									
	22		<p>Add Hsync Frequency</p>																																																									
	30	<p>Note 3: TORQUE OF M2 USER HOLE SHOULD BE WITHIN 4kgf-cm</p>	<p>Update Note3: TORQUE OF M2 USER HOLE SHOULD BE WITHIN 3kgf-cm</p>																																																									
1.1 2012/12/4	34		<p>To Modify the Ch 11-2 Panel dimension with 613.6(H)mm x 356.85(V)mm x 10(D)mm</p>																																																									
	18		<p>To Correct the Note3 , VLED changed VLB</p>																																																									

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1.3 2013/2/26	7		To define the maximum value of response time																																																																															
1.4 2013/4/12	20/25	Pin25/Pin 27 is ground pin	Re-define the Pin assignment Pin25/Pin27 as NC																																																																															
1.5 2013/5/6	31/32 /33	Without shading mylar on front bezel surface	Adding mylar on front bezel surface																																																																															
	19	Maximum of Power is 24.95	Recommendation for LED driver power design: To Modify the Maximum of Power is 23.76																																																																															
	34/35	To modify the pallet and shipment information, before the packing box is 9pcs/Box and the weight of pallet of packing is 216kg.	After modifying , the packing box is 10pcs/Box and the weight of pallet of packing is 229.94kg.																																																																															
2.0 2014/8/4	7	White Luminance (Center) 350 cd/m2 (Typ.) @ 110mA 300 cd/m2 (Typ.) @ 100mA	White Luminance (Center)=300nits																																																																															
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LTLED	LED Life Time (Typical)	30,000	-	-	[Hour]	Note 4																																																																												
3.0 2014/8/5	all	Change chapter order																																																																																
	31		Remark width of bezel(upper and down)																																																																															
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4.0 2016/12/23	all	Change to new format																									
	7	2.1 Display Characteristics 350cd/m2@110mA / 300cd/m2@100mA	2.1 Display Characteristics 350 cd/m2 (Typ.)																								
	8		2.3 Optical Characteristics																								
	13		4.2 Backlight Unit LED Pulse Current: 210→300mA																								
	15		5.1.1 Power Specification PDD1: 7.26W→7.3W																								
	19	5.2 Backlight Unit Each module consists of 40 pcs LED (4 strings x 10pcs / string)	5.2 Backlight Unit LED Forward Current: 110mA→90mA PBLU: 21.78W→16.7W Each module consists of 60 pcs LED (4 strings x 15pcs / string)																								
	25		6.6 Power ON/OFF Sequence																								
	26		7.1 TFT LCD Module																								
4.1 2017/08/17	13		Adding the definition of suitable operation time: 20 hours a day.																								
4.2 2019/06/25	31		Adding the width from center to edge of panel on 2D drawing																								
4.3 2020/10/30	30	<p>9 Shipping Label</p> <p>The label on the panel is shown as below:</p>  <p>Note 1: For Pb Free products, AUO will add  for identification. Note 2: For RoHS compatible products, AUO will add  for identification. Note 3: For China RoHS compatible products, AUO will add  for identification. Note 4: The Green Mark will be presented only when the green documents have been ready by AUO Internal Green Team.</p>	<p>Update packing information based on Label EC</p> <p>The label on the panel is shown as below:</p>  <p>Note 5-1: For Pb Free products, AUP will add  for identification. Note 5-2: For RoHS compatible products, ADP will add  for identification. Note 5-3: For China RoHS compatible products, AUP will add  for identification. Note 5-4: The Green Mark will be presented only when the green documents have been ready by</p>																								
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	29	<p>4.2 Interface Connection-</p> <p>4.2.1 Connector Type:</p> <table border="1"> <tr> <td>Backlight Connector-</td> <td>Manufacturer:-</td> <td>ENTERY-</td> </tr> <tr> <td></td> <td>Part Number:-</td> <td>3707K-S06N-21R-</td> </tr> <tr> <td>Mating Connector-</td> <td>Manufacturer:-</td> <td>Cvilux-</td> </tr> <tr> <td></td> <td>Part Number:-</td> <td>C11406SL000-NH (Locking type)-</td> </tr> </table>	Backlight Connector-	Manufacturer:-	ENTERY-		Part Number:-	3707K-S06N-21R-	Mating Connector-	Manufacturer:-	Cvilux-		Part Number:-	C11406SL000-NH (Locking type)-	<p>Update Connector Type</p> <table border="1"> <tr> <td>Backlight Connector-</td> <td>Manufacturer:-</td> <td>CVILUX-</td> </tr> <tr> <td></td> <td>Part Number:-</td> <td>C11406M1HRN-NH1-</td> </tr> <tr> <td>Mating Connector-</td> <td>Manufacturer:-</td> <td>CVILUX or Compatible-</td> </tr> <tr> <td></td> <td>Part Number:-</td> <td>C11406SL000-NH (Lock)-</td> </tr> </table>	Backlight Connector-	Manufacturer:-	CVILUX-		Part Number:-	C11406M1HRN-NH1-	Mating Connector-	Manufacturer:-	CVILUX or Compatible-		Part Number:-	C11406SL000-NH (Lock)-
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	<p>32 33 34</p>		<p>Update module drawing which add ADP logo</p> 	
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1 Handling Precautions

- 1) Since front polarizer is easily damaged, pay attention not to scratch it.
- 2) Be sure to turn off power supply when inserting or disconnecting from input connector.
- 3) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
- 4) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
- 5) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface.
- 6) Since CMOS LSI is used in this module, take care of static electricity and insure human earth when handling.
- 7) Do not open or modify the Module Assembly.
- 8) Do not press the reflector sheet at the back of the module to any directions.
- 9) In case if a Module has to be put back into the packing container slot after once it was taken out from the container, do not press the center of LED light bar edge. Instead, press at the far ends of the LED light bar edge softly. Otherwise the TFT Module may be damaged.
- 10) At the insertion or removal of the Signal Interface Connector, be sure not to rotate nor tilt the Interface Connector of the TFT Module.
- 11) After installation of the TFT Module into an enclosure, do not twist nor bend the TFT Module even momentary. At designing the enclosure, it should be taken into consideration that no bending/twisting forces are applied to the TFT Module from outside. Otherwise the TFT Module may be damaged.
- 12) Small amount of materials having no flammability grade is used in the LCD module. The LCD module should be supplied by power complied with requirements of Limited Power Source (IEC60950 or UL1950), or be applied exemption.
- 13) Avoid touching COF position while doing mechanical design.
- 14) While storing modules as spares for a long time, the following precautions are necessary:
 - Store modules in a dark place. Do not expose them to sunlight or fluorescent light.
 - Keep the temperature between 5°C and 35°C at normal humidity.

2 General Description

This specification applies to the 27 inch-FHD color a-Si TFT-LCD Module P270HVN01.0. The display supports the FHD - 1920(H) x 1080(V) screen format and 16.7M colors (RGB 8-bit data). The light source of this TFT-LCD module is W-LED. All input signals are 2-channel LVDS interface and this module doesn't contain a driver for backlight.

2.1 Display Characteristics

The following items are characteristics summary on the table under 25°C condition:

Items	Unit	Specification
Screen Diagonal	[mm]	685.65(27.0")
Active Area	[mm]	597.6 (H) x 336.15 (V)
Pixels H x V		1920(x3) x 1080
Pixel Pitch	[um]	311.25 (per one triad) ×311.25
Pixel Arrangement		R.G.B. Vertical island
Display Mode		VA Mode, Normally Black
White Luminance (Center)	[cd/m ²]	350 cd/m ² (Typ.)
Contrast Ratio		3000 (Typ.)
Optical Response Time	[msec]	12ms (Typ., on/off)
Nominal Input Voltage	[Volt]	5 V (Typ)
Power Consumption (VDD line + LED line)	[Watt]	21.39 watt(4.65W+16.74W=21.39W) VDD line : PDD (typ),All white pattern at 60Hz = 4.65 W LED line : PBLU (typ) = 16.74 W(@90mA)
Weight	[g]	2180 gram (Typ.)
Physical Size	[mm]	613.6(H)x356.85(V)x10.0(D) (Typ.)
Electrical Interface		Dual channel LVDS
Support Color		16.7M colors (RGB 8-bit)
Surface Treatment		Anti-Glare 3H
Temperature Range		
Operating	[°C]	0 to +50
Storage (Shipping)	[°C]	-20 to +60
RoHS Compliance		RoHS Compliance
TCO Compliance		TCO 7.0 Compliance

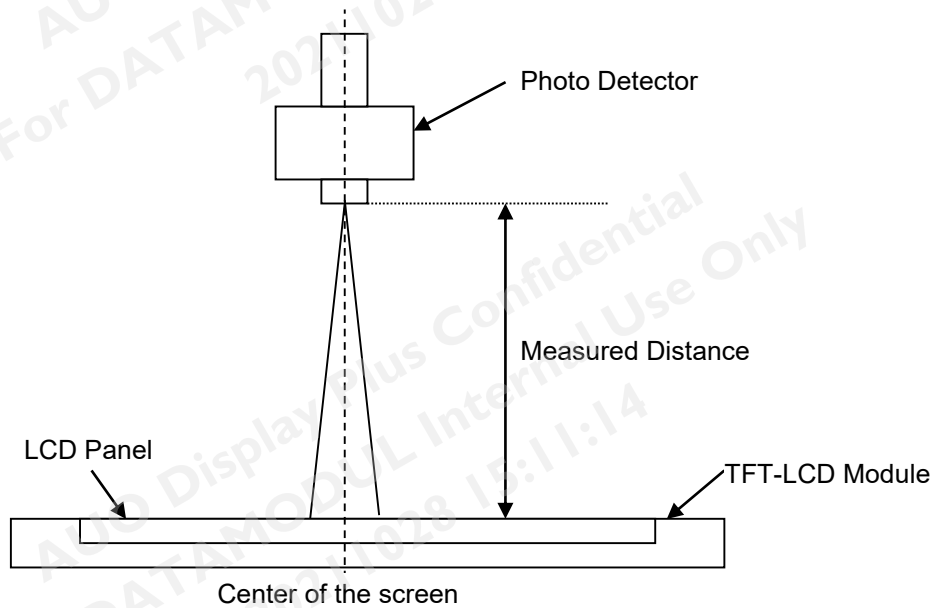
2.2 Optical Characteristics

The optical characteristics are measured under stable conditions at 25°C:

Item	Unit	Conditions	Min.	Typ.	Max.	Note
Viewing Angle	[degree]	Horizontal (Right) CR = 10 (Left)	150	178	-	2
		Vertical (Up) CR = 10 (Down)	150	178	-	
Contrast ratio		Normal Direction	1800	3000	-	3
Response Time	[msec]	Raising Time (T _{IR})	-	7	17	4
		Falling Time (T _{IF})	-	5	7	
		Raising + Falling	-	12	24	
Color / Chromaticity Coordinates (CIE)		Red x	0.610	0.640	0.670	5
		Red y	0.303	0.333	0.363	
		Green x	0.290	0.320	0.350	
		Green y	0.590	0.620	0.650	
		Blue x	0.126	0.156	0.186	
		Blue y	0.025	0.055	0.085	
Color Coordinates (CIE) White		White x	0.283	0.313	0.343	
		White y	0.299	0.329	0.359	
Central Luminance	[cd/m ²]		280	350	-	6
Luminance Uniformity	[%]		75	80	-	7
Crosstalk (in 60Hz)	[%]				1.5	8
Flicker	dB				-20	9

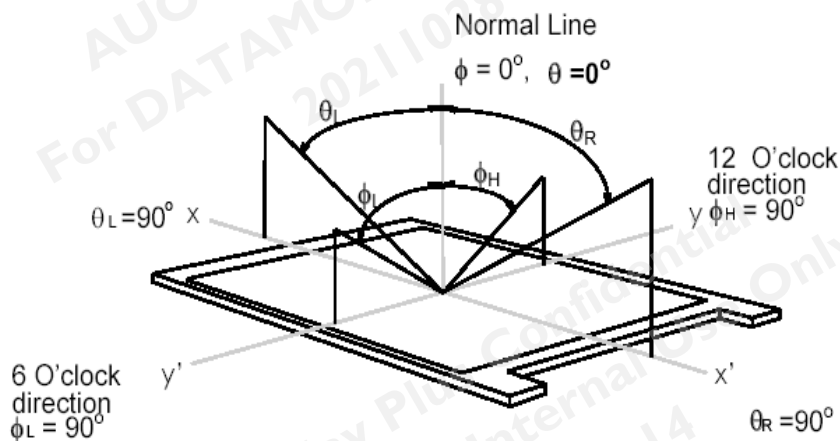
Note 1: Measurement Method

The LCD module should be stabilized at given temperature for 30 minutes to avoid abrupt temperature change during measuring (at surface 35°C). In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 30 minutes in a stable, windless and dark room.



Note 2: Definition of Viewing Angle measured by ELDIM (EZContrast 88)

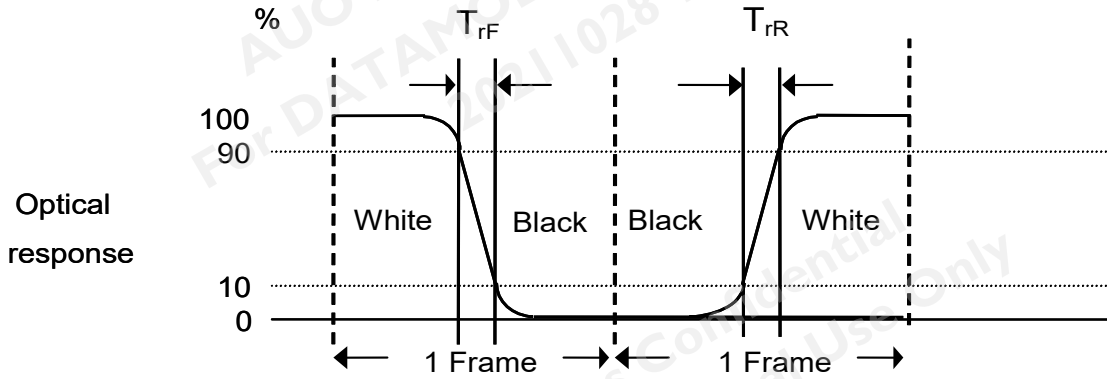
Viewing angle is the measurement of contrast ratio ≥ 10 , at the screen center, over a 180° horizontal and 180° vertical range (off-normal viewing angles). The 180° viewing angle range is broken down as follows; 90° (θ) horizontal left and right and 90° (ϕ) vertical, high (up) and low (down). The measurement direction is typically perpendicular to the display surface with the screen rotated about its center to develop the desired measurement viewing angle.



Note 3: Contrast Ratio measured by TOPCON SR-3

Note 4: Definition of Response time measured by Westar TRD-100A

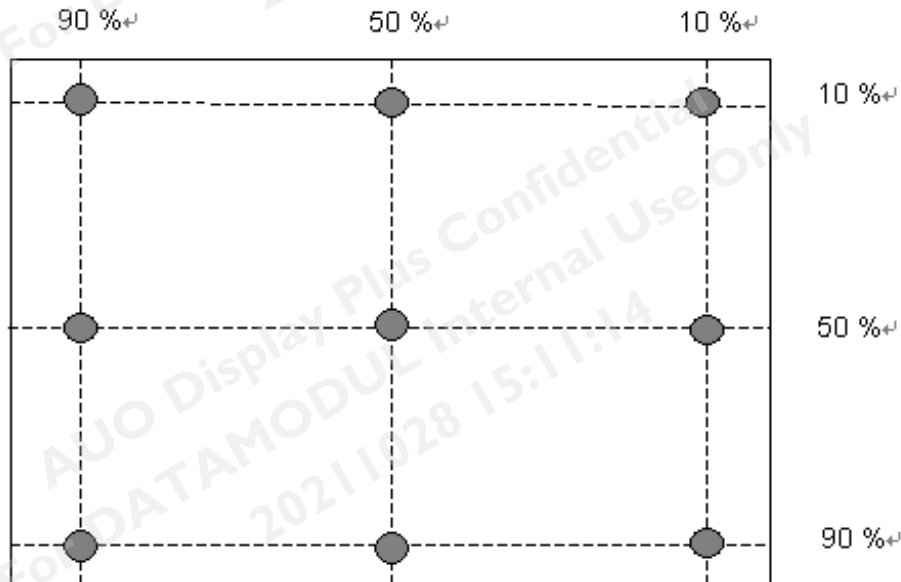
The output signals of photo detector are measured when the input signals are changed from “Full Black” to “Full White” (rising time, T_{rR}), and from “Full White” to “Full Black” (falling time, T_{rF}), respectively. The response time is interval between the 10% and 90% (1 frame at 60 Hz) of amplitudes. $T_{rR} + T_{rF} = 12$ msec (typ.).



Note 5: Color Chromaticity and Coordinates (CIE) measured by TOPCON SR-3

Note 6: Central Luminance measured by TOPCON SR-3

Note 7: Luminance Uniformity of these 9 points defined as below and measured by TOPCON SR-3



$$\text{Uniformity} = \frac{\text{Minimum Luminance in 9 points (1-9)}}{\text{Maximum Luminance in 9 Points (1-9)}}$$

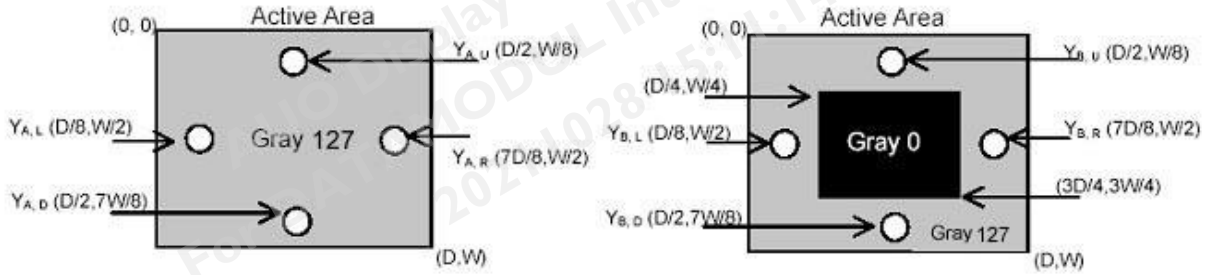
Note 8: Crosstalk defined as below and measured by TOPCON SR-3

$$CT = |YB - YA| / YA \times 100 (\%)$$

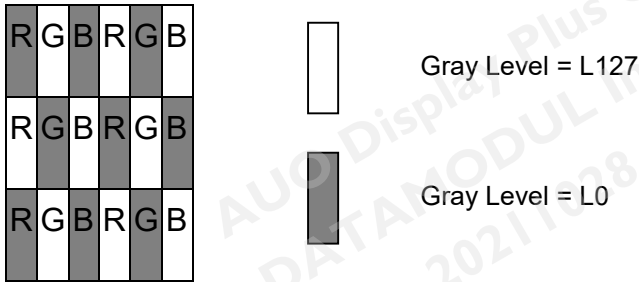
Where

YA = Luminance of measured location without gray level 0 pattern (cd/m²)

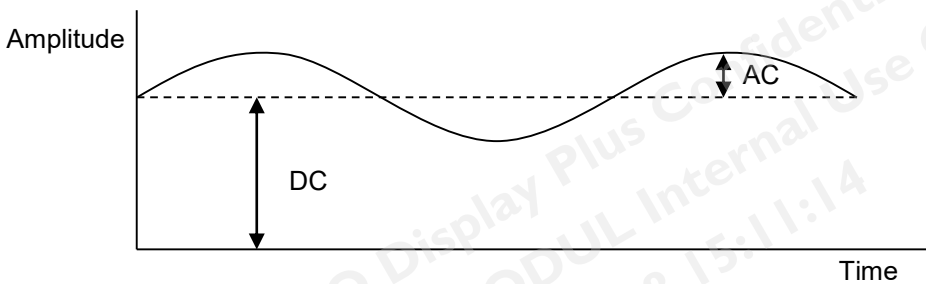
YB = Luminance of measured location with gray level 0 pattern (cd/m²)



Note 9: Test Pattern Sub-checker Pattern measured by TOPCON SR-3



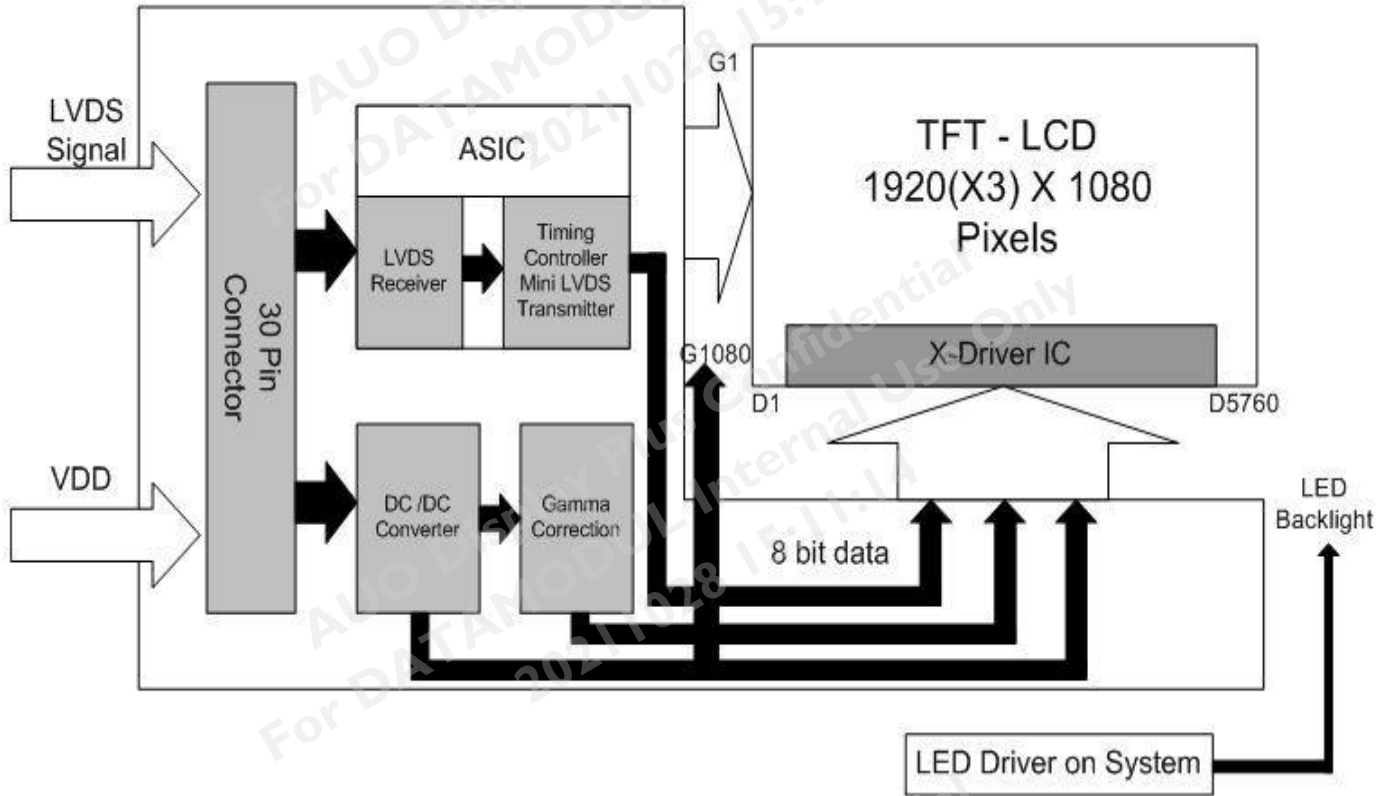
Method: Record dBV & DC value with TRD-100



$$\text{Flicker (dB)} = 20 \log \frac{\text{AC Level (at 30 Hz)}}{\text{DC Level}}$$

3 Functional Block Diagram

The following diagram shows the functional block of the 27.0 inch Color TFT-LCD Module:



4 Absolute Maximum Ratings

Absolute maximum ratings of the module are listed as follows:

4.1 TFT LCD Module

Item	Symbol	Min	Max	Unit	Conditions
Logic/LCD Drive	VDD	-0.3	6.0	[Volt]	Note 1, 2

4.2 Backlight Unit

Item	Symbol	Min	Max	Unit	Conditions
LED Forward Current	IFLED1	0	150	[mA]	Note 1,2,5 100% duty
	IFLED2				
	IFLED3				
	IFLED4				
LED Pulse Forward Current	IPLIED1	-	300	[mA]	Note 1,2,5 10% duty @100Hz
	IPLIED2				
	IPLIED3				
	IPLIED4				
LED forward Voltage variation (per string variation)	ΔV_f	-	3	[Volt]	Note 1, 2

4.3 Absolute Ratings of Environment

Item	Symbol	Min.	Max.	Unit	Conditions
Operating Temperature	TOP	0	+50	[°C]	Note 3
Glass Surface Temperature (Operation)	TGS	0	+65	[°C]	Note3 Note4
Operation Humidity	HOP	5	90	[%RH]	Note 3
Storage Temperature	TST	-20	+60	[°C]	
Storage Humidity	HST	5	90	[%RH]	
Suitable operating time	SOT		20	Hrs/day	Note 6

Note 1: With in Ta (25 °C)

Note 2: Permanent damage to the device may occur if exceeding maximum values

Note 3: Temperature and relative humidity range are shown as the below figure.

- 90% RH Max (Ta ≤ 39°C)
- Max wet-bulb temperature at 39°C or less. (Ta ≤ 39°C)
- No condensation

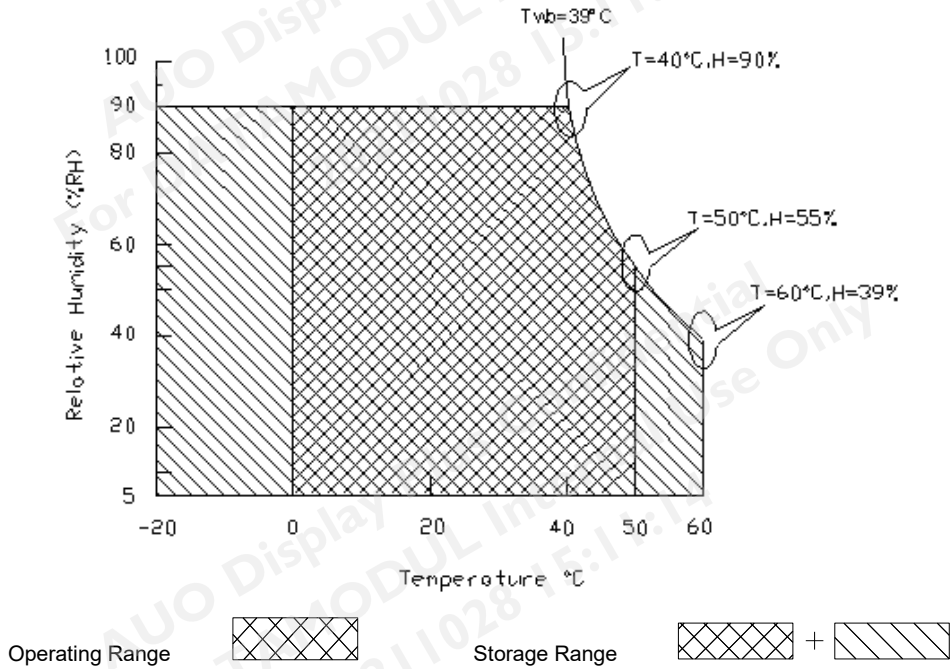
Note 4: Function Judged only

Note 5: IFLED1,2,3,4 and IPLIED1,2,3,4 define as per strings LED current.

Note 6: Operation usage to protect against image sticking due to long-term static display.

- Suitable operating time: 20 hours a day or less.
- Liquid Crystal refresh time is required. Cycling display between 5 minutes' information (static) display and 10 seconds' moving image.

3. Periodically change background and character (image) color.
4. Avoid combination of background and character with large different luminance.



5 Electrical Characteristics

5.1 TFT LCD Module

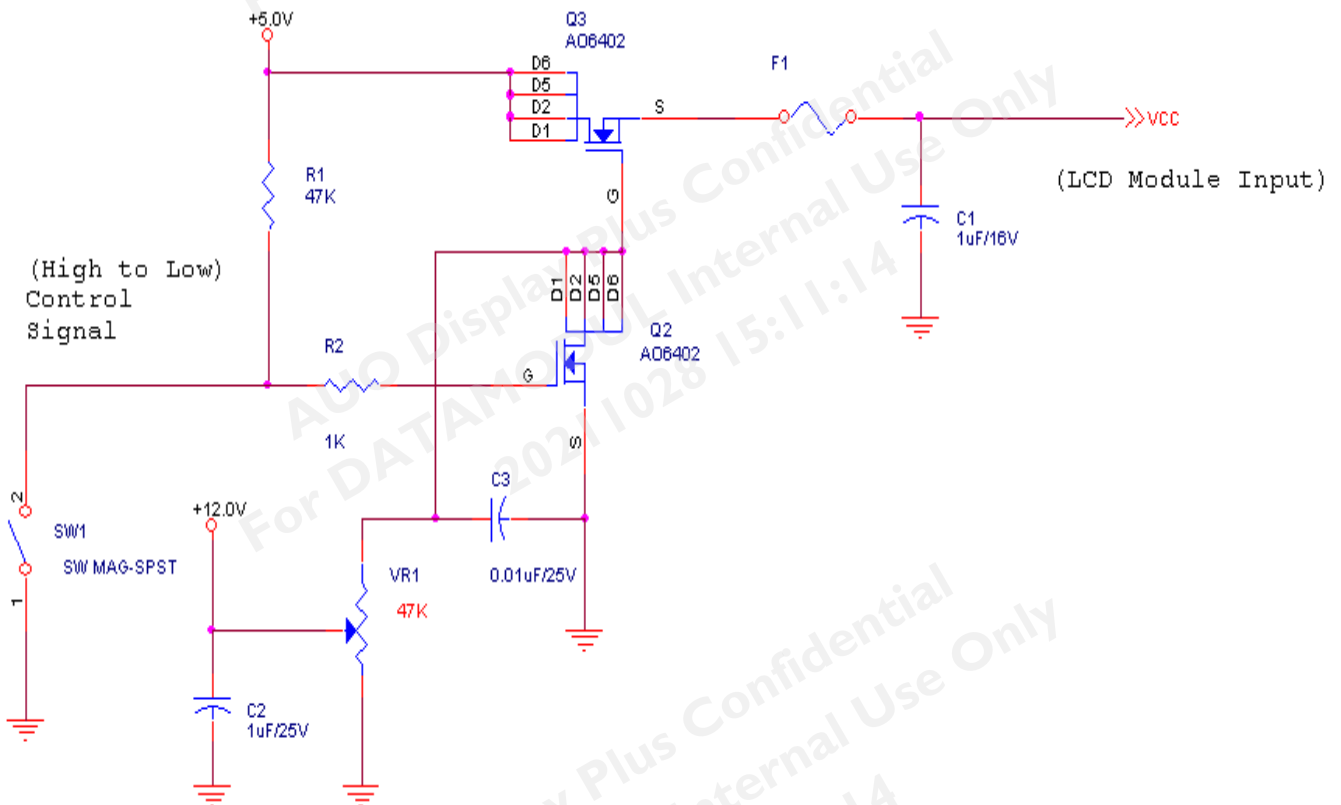
5.1.1 Power Specification

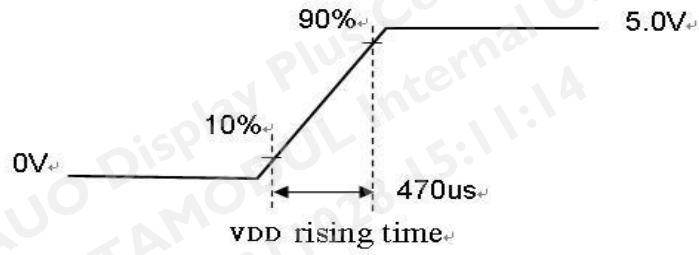
Input power specifications are listed as follows:

Symbol	Description	Min	Typ.	Max	Unit	Conditions
VDD	Logic/LCD Drive Voltage	4.5	5.0	5.5	[Volt]	+/-10%
IDD1	Input Current	-	0.93	1.22	[A]	VDD= 5.0V, All white Pattern at 60 Hz
		-	1.22	1.46	[A]	VDD= 5.0V, All white Pattern at 75 Hz
PDD1	VDD Power	-	4.65	6.10	[Watt]	VDD= 5.0V, All white Pattern at 60 Hz
		-	6.10	7.30	[Watt]	VDD= 5.0V, All white Pattern at 75 Hz
IRush	Inrush Current	-	-	3	[A]	Note 1
VDDrp	Allowable Logic/LCD Drive Ripple Voltage	-	-	500	[mV] p-p	VDD= 5.0V, All white Pattern at 75 Hz

Note 1: Measurement Conditions:

The duration of rising time of power input is 470 μ s.





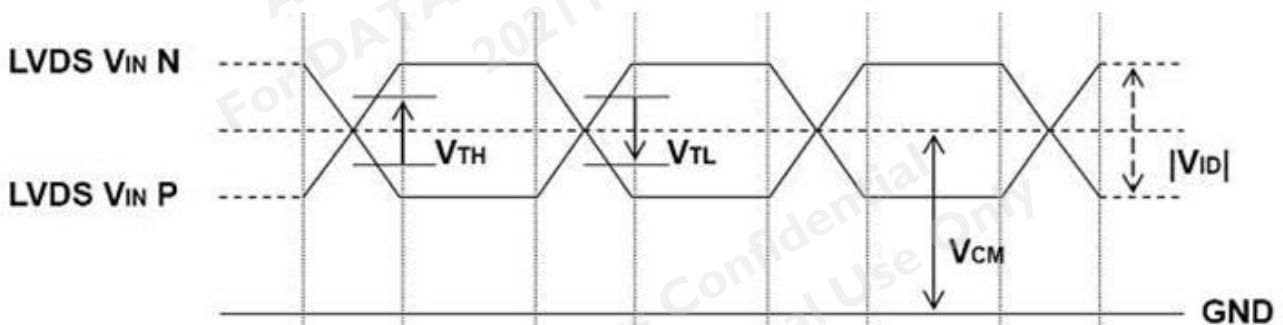
5.1.2 Signal Electrical Characteristics

(1) DC Characteristics of each signal are as following:

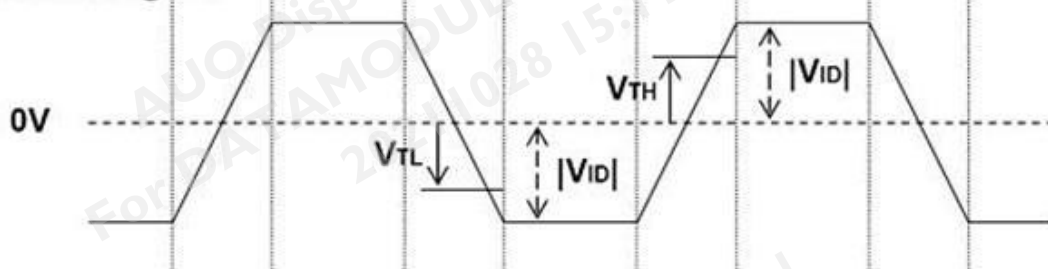
Symbol	Description	Min	Typ	Max	Units	Conditions
V_{TH}	Differential Input High Threshold	-	-	+100	[mV]	$V_{CM} = 1.2V$ Note 1
V_{TL}	Differential Input Low Threshold	-100	-	-	[mV]	$V_{CM} = 1.2V$ Note 1
$ V_{ID} $	Input Differential Voltage	100	-	600	[mV]	Note 1
V_{CM}	Differential Input Common Mode Voltage	+1.0	+1.2	+1.5	[V]	$V_{TH}-V_{TL} = 200MV$ (max) Note 1

Note 1: LVDS Signal Waveform

Single-End

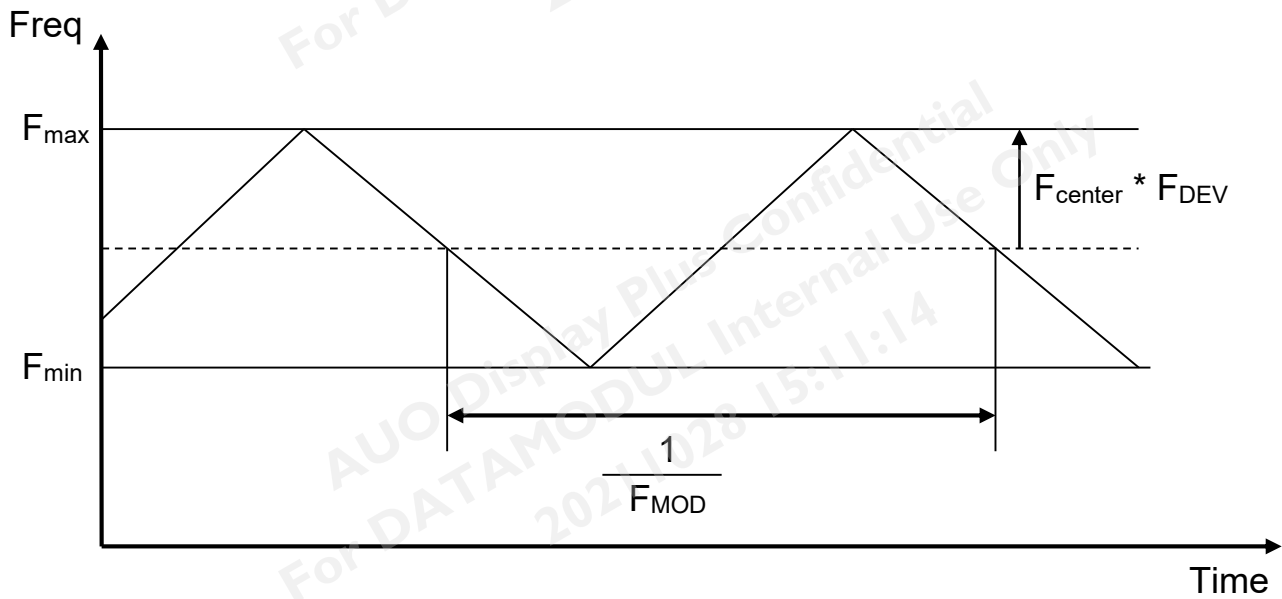


Differential Signal



(2) AC Characteristics

Symbol	Description	Min	Max	Units	Conditions
F_{DEV}	Maximum deviation of input clock frequency during SSC	-	± 3	%	
F_{MOD}	Maximum modulation frequency of input clock during SSC	-	200	KHz	



< Spread Spectrum >

5.2 Backlight Unit

Parameter guideline for LED driving is under stable conditions at 25 °C (Room Temperature):

Symbol	Description	Min.	Typ.	Max.	Unit	Note
IFLED1	LED Forward Current	-	90	99	[mA]	Note 1
IFLED2						
IFLED3						
IFLED4						
VSLED	Light Bar Operation Voltage (for reference)	42	46.5	51	[Volt]	Note 2
PBLU	BLU Power Consumption (for reference)	-	16.7	18.4	[Watt]	Note 3
LTLED	LED Life Time (Typical)	30,000	-	-	[Hour]	Note 4

Each module consists of 60 pcs LED (4 strings x 15pcs / string)

Note 1: The specified current is 100% duty of LED chip input current, IRLED1,2,3,4 define as per strings LED current.

Note 2: The value showed is one string operation voltage.

Note 3: $PBLU = VSLED * (IFLED1 + IFLED2 + IFLED3 + IFLED4)$

Note 4: Definition of life time: Brightness becomes to 50% of its original value. The minimum life time of LED unit is on the condition of IFLED = 90mA and 25±2°C (Room Temperature).

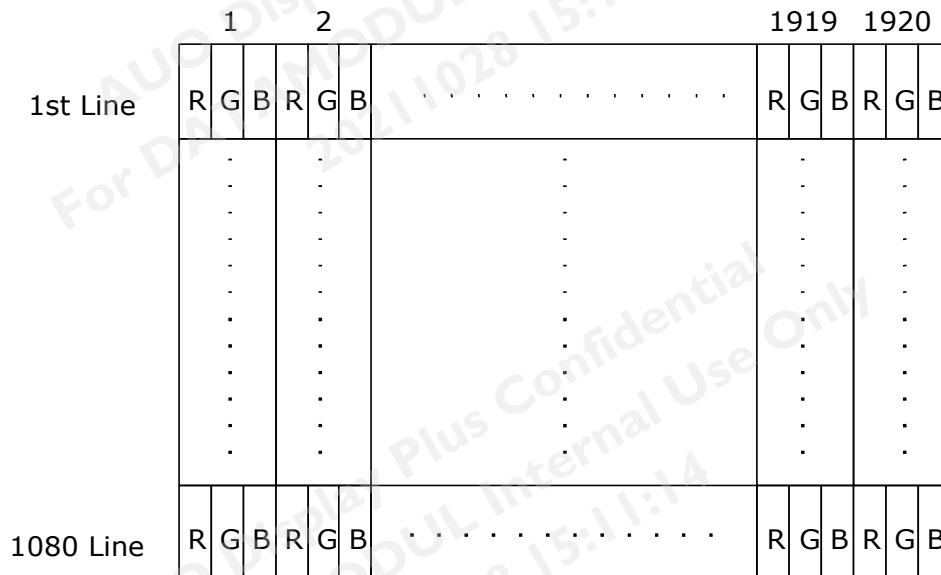
Note 5: Recommendation for LED driver power design:

Due to there are electrical property deviation in LED & monitor set system component after long time operation. ADP strongly recommend the design value of LED driver board OVP (over voltage protection) should be 10% higher than max. value of LED string voltage (Vs) at least.

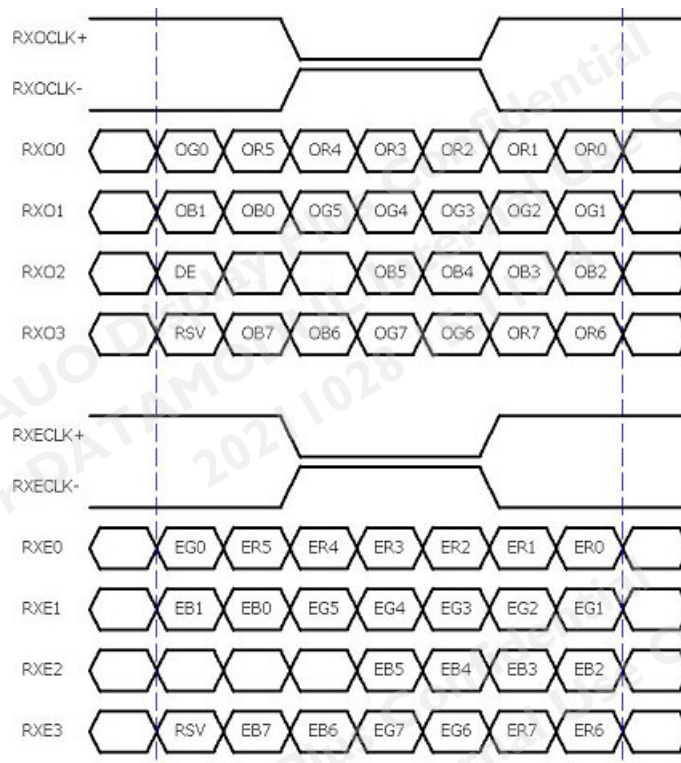
6 Signal Characteristics

6.1 Pixel Format Definition

Following figure shows the relationship between the input signals and LCD pixel format.



6.2 Input Data Format Definition



Note1: Normally, DE, VS, HS on EVEN channel are not used.

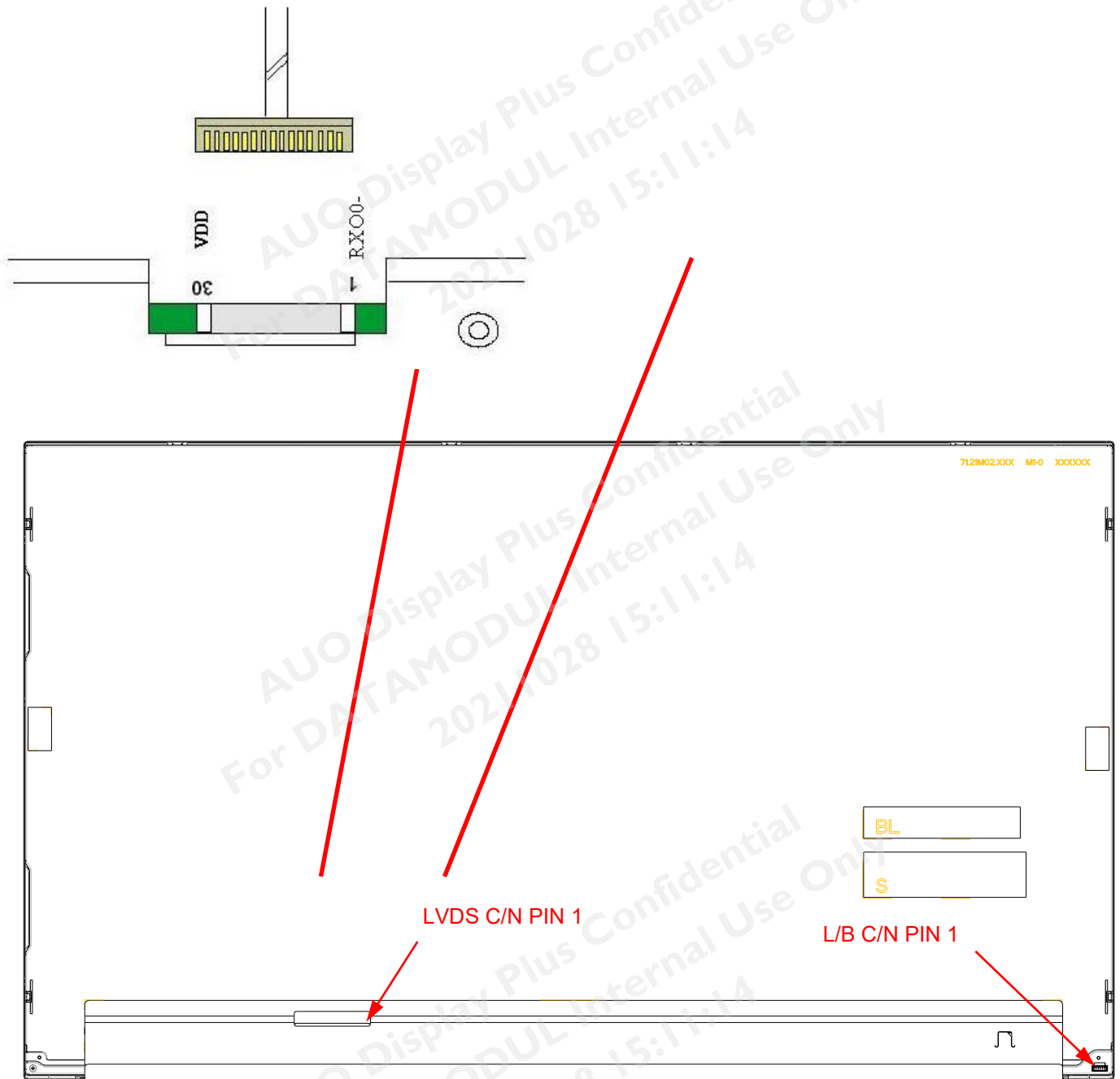
Note2: Please follow PSWG.

Note3: 8-bit in

6.3 Signal Description

PIN #	SIGNAL NAME	DESCRIPTION
1	RXO0-	Negative LVDS differential data input (Odd data)
2	RXO0+	Positive LVDS differential data input (Odd data)
3	RXO1-	Negative LVDS differential data input (Odd data)
4	RXO1+	Positive LVDS differential data input (Odd data)
5	RXO2-	Negative LVDS differential data input (Odd data)
6	RXO2+	Positive LVDS differential data input (Odd data)
7	GND	Power Ground
8	RXOCLK-	Negative LVDS differential clock input (Odd clock)
9	RXOCLK+	Positive LVDS differential clock input (Odd clock)
10	RXO3-	Negative LVDS differential data input (Odd data)
11	RXO3+	Positive LVDS differential data input (Odd data)
12	RXE0-	Negative LVDS differential data input (Even data)
13	RXE0+	Positive LVDS differential data input (Even data)
14	GND	Power Ground
15	RXE1-	Negative LVDS differential data input (Even data)
16	RXE1+	Positive LVDS differential data input (Even data)
17	GND	Power Ground
18	RXE2-	Negative LVDS differential data input (Even data)
19	RXE2+	Positive LVDS differential data input (Even data)
20	RXECLK-	Negative LVDS differential clock input (Even clock)
21	RXECLK+	Positive LVDS differential clock input (Even clock)
22	RXE3-	Negative LVDS differential data input (Even data)
23	RXE3+	Positive LVDS differential data input (Even data)
24	GND	Power Ground
25	NC	No contact
26	NC	No contact
27	NC	No contact
28	VDD	+5.0V Power Supply
29	VDD	+5.0V Power Supply
30	VDD	+5.0V Power Supply

Note 1: Input signals of odd and even clock shall be the same timing.



6.4 Timing Characteristics

It only support DE mode, and the input timing are shown as the following table.

Symbol	Description	Min.	Typ.	Max.	Unit	Remark	
Tv	Vertical Section	Period	1092	1130	1793	Th	
Tdisp (v)		Active	1080	1080	1080	Th	
Tblk (v)		Blanking	12	50	713	Th	
Fv		Frequency	50	60	76	Hz	
Th	Horizontal Section	Period	1004	1050	1100	Tclk	
Tdisp (h)		Active	960	960	960	Tclk	
Tblk (h)		Blanking	44	90	140	Tclk	
Fh		Frequency	55	68	90	KHz	Note 1
Tclk	LVDS Clock	Period	11.1	14.0	18.2	ns	1/Fclk
Fclk		Frequency	54.8	71.2	90.0	MHz	Note 2

Note 1: The equation is listed as following. Please don't exceed the above recommended value.

$$Fh (\text{Min.}) = Fclk (\text{Min.}) / Th (\text{Min.});$$

$$Fh (\text{Typ.}) = Fclk (\text{Typ.}) / Th (\text{Typ.});$$

$$Fh (\text{Max.}) = Fclk (\text{Max.}) / Th (\text{Min.});$$

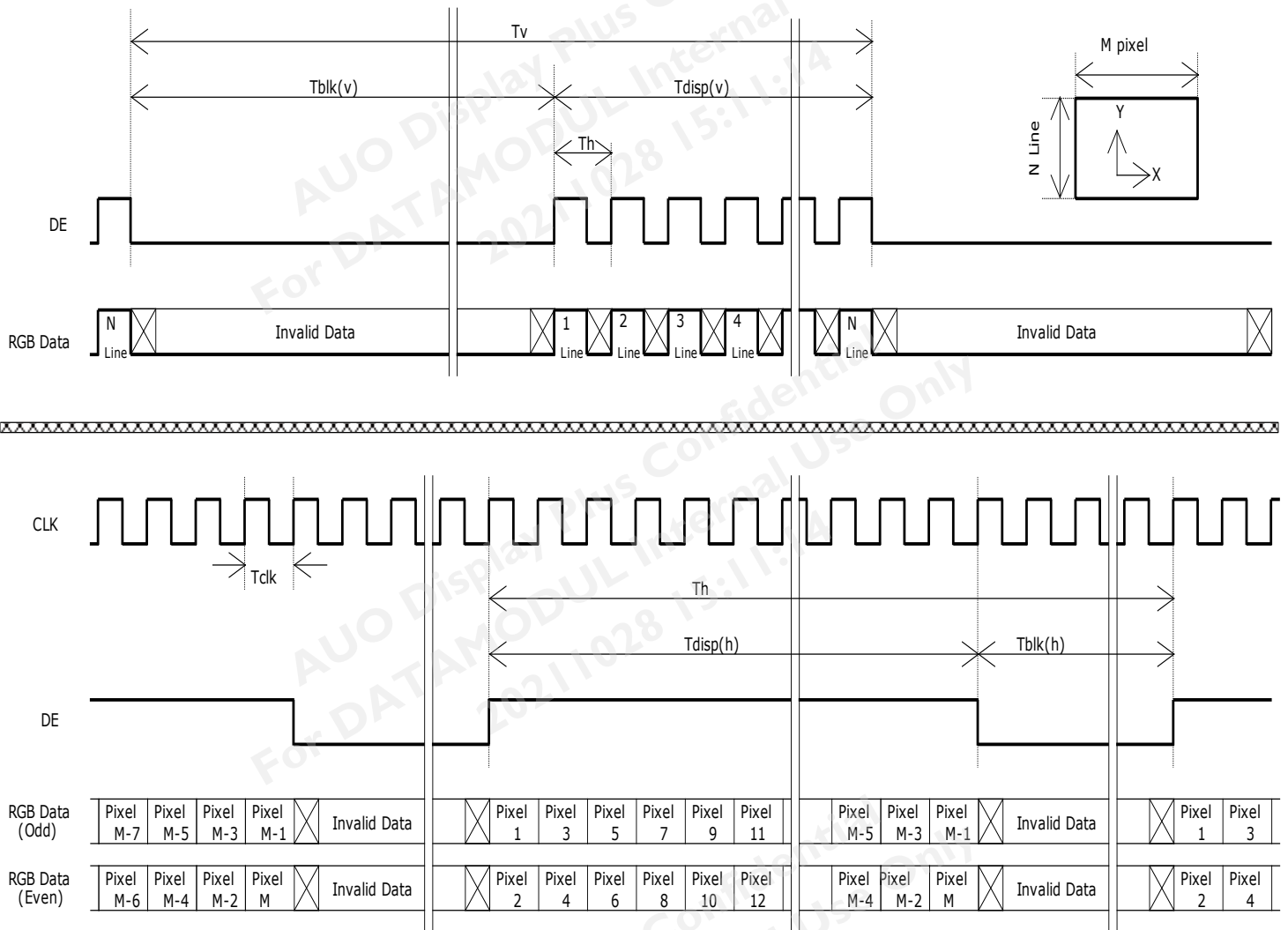
Note 2: The equation is listed as following. Please don't exceed the above recommended value.

$$Fclk (\text{Min.}) = Fv (\text{Min.}) \times Th (\text{Min.}) \times Tv (\text{Min.});$$

$$Fclk (\text{Typ.}) = Fv (\text{Typ.}) \times Th (\text{Typ.}) \times Tv (\text{Typ.});$$

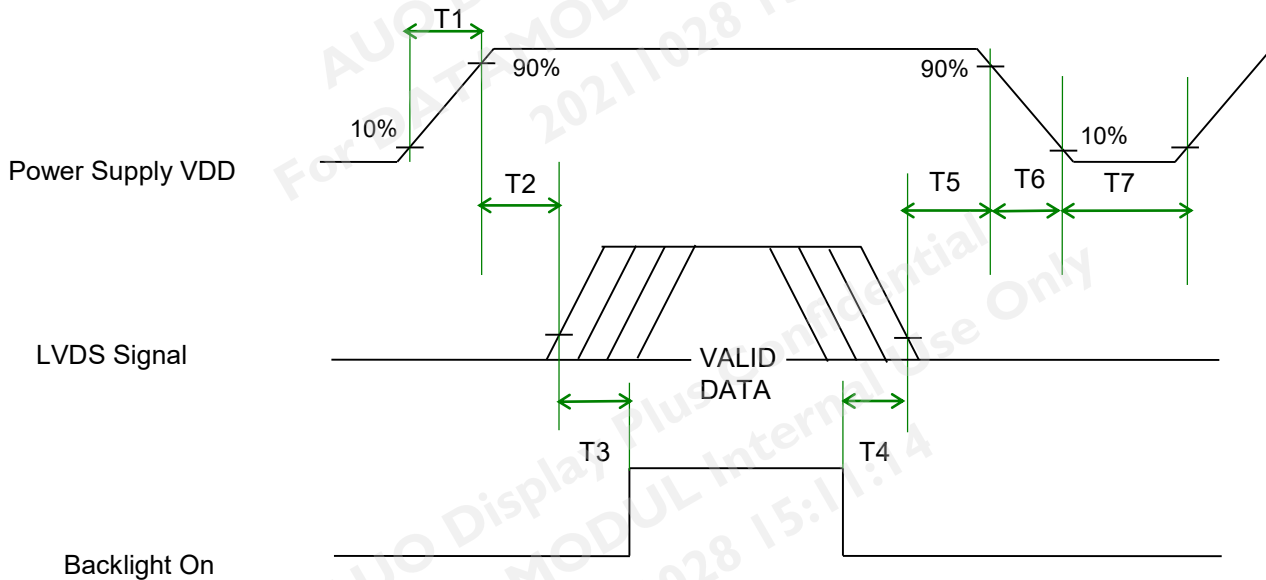
$$Fclk (\text{Max.}) = Fv (\text{Max.}) \times Th (\text{Typ.}) \times Tv (\text{Typ.});$$

Timing Diagram



6.5 Power ON/OFF Sequence

VDD power and lamp on/off sequence are as follows. Interface signals are also shown in the chart. Signals from any system shall be Hi-Z state when VDD is off.



Power Sequence Timing

Parameter	Value			Unit
	Min.	Typ.	Max.	
T1	0.5	-	10	[ms]
T2	0	-	50	[ms]
T3	500	-	-	[ms]
T4	100	-	-	[ms]
T5	0	-	50	[ms] Note1,2
T6	0	-	200	[ms] Note2,3
T7	1000	-	-	[ms]

Note1 : Recommend setting T5 = 0ms to avoid electronic noise when VDD is off.

Note2 : During T5 and T6 period , please keep the level of input LVDS signals with Hi-Z state.

Note3: Voltage of VDD must decay smoothly after power-off. (customer system decide this value)

7 Connector and Pin Assignment

Physical interface is described as for the connector on module. These connectors are capable of accommodating the following signals and will be following components.

7.1 TFT LCD Module

Connector Name / Designation	Interface Connector / Interface Card
Manufacturer	STM P-TWO STARCONN
Type Part Number	MSCKT2407P30HB AL230F-A0G1D-P 093G30-02001A-M4
Mating Housing Part Number	FI-X30HL (Locked Type)

7.1.1 Pin Assignment

Pin#	Signal Name	Pin#	Signal Name
1	RX00-	2	RX00+
3	RX01-	4	RX01+
5	RX02-	6	RX02+
7	GND	8	RXOCLK-
9	RXOCLK+	10	RX03-
11	RX03+	12	RXE0-
13	RXE0+	14	GND
15	RXE1-	16	RXE1+
17	GND	18	RXE2-
19	RXE2+	20	RXECLK-
21	RXECLK+	22	RXE3-
23	RXE3+	24	GND
25	NC	26	NC
27	NC	28	VDD
29	VDD	30	VDD

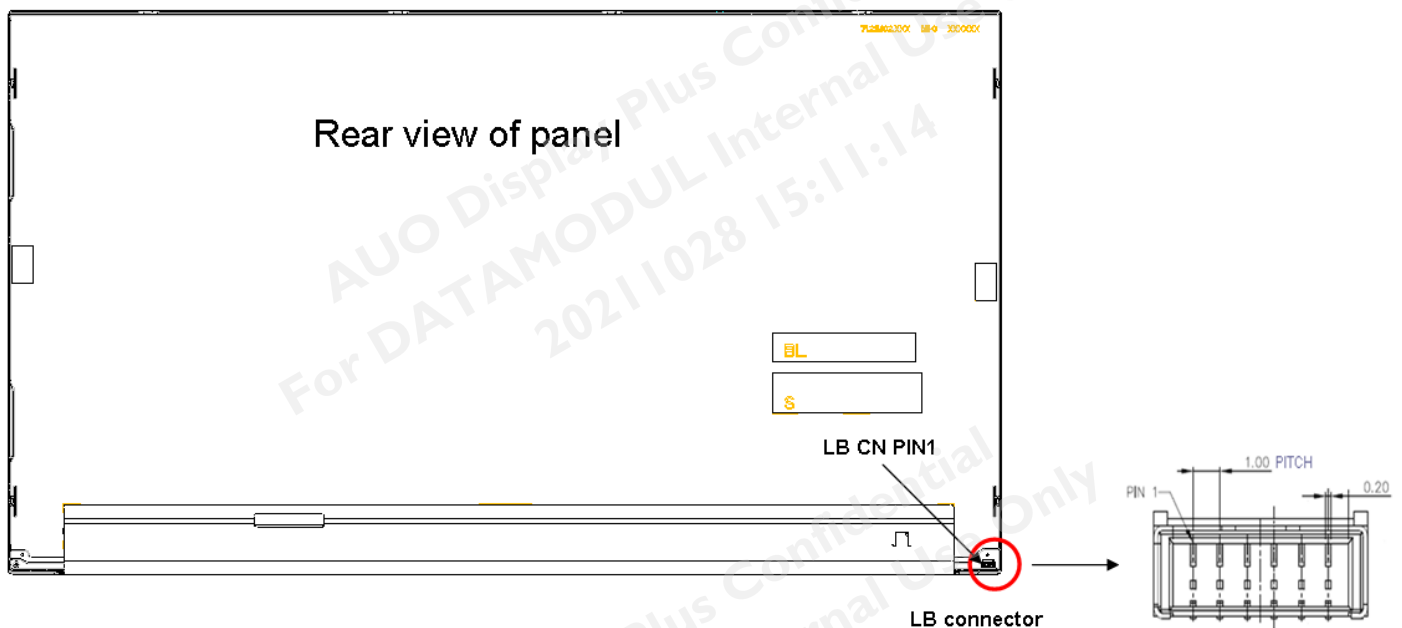
7.2 LED Connector on Backlight Unit

This connector is mounted on LED light bar.

Backlight Connector	Manufacturer	CVILUX
	Part Number	CI1406M1HRN-NH1
Mating Connector	Manufacturer	CVILUX or Compatible
	Part Number	CI1406SL000-NH (Lock)

7.2.1 LED Pin assignment

Pin#	Signal Name
1	IFLED (current return)
2	IFLED (current return)
3	VSLED (voltage in)
4	VSLED (voltage in)
5	IFLED (current return)
6	IFLED (current return)



8 Reliability Test

Environment test conditions are listed as following table.

Items	Required Condition	Note
Temperature Humidity Bias (THB)	Ta= 50°C, 80%RH, 300 hours	
High Temperature Operation (HTO)	Ta= 50°C, 50%RH, 300 hours	
Low Temperature Operation (LTO)	Ta= 0°C, 300 hours	
High Temperature Storage (HTS)	Ta= 60°C, 300 hours	
Low Temperature Storage (LTS)	Ta= -20°C, 300 hours	
Vibration Test (Non-operation)	Acceleration: 1.5 Grms Wave: Random Frequency: 10 - 200 Hz Duration: 30 Minutes each Axis (X, Y, Z)	
Shock Test (Non-operation)	Acceleration: 50 G Wave: Half-sine Active Time: 20 ms Direction: ±X, ±Y, ±Z (one time for each Axis)	
Drop Test	Height: 46 cm, package test	
Thermal Shock Test (TST)	-20°C/30min, 60°C/30min, 100 cycles	Note 1
On/Off Test	On/10sec, Off/10sec, 30,000 cycles	
ESD (Electro Static Discharge)	Contact Discharge: ± 15KV, 150pF(330Ω) 1sec, 15 points, 25 times/ point	Note 2
	Air Discharge: ± 15KV, 150pF(330Ω) 1sec 15 points, 25 times/ point	
Altitude Test	Operation: 18,000 ft Non-Operation: 40,000 ft	Note 3

Note 1: The TFT-LCD module will not sustain damage after being subjected to 100 cycles of rapid temperature change. A cycle of rapid temperature change consists of varying the temperature from -20°C to 60°C, and back again. Power is not applied during the test. After temperature cycling, the unit is placed in normal room ambient for at least 4 hours before power on.

Note 2: EN61000-4-2, ESD class B: Certain performance degradation allowed:

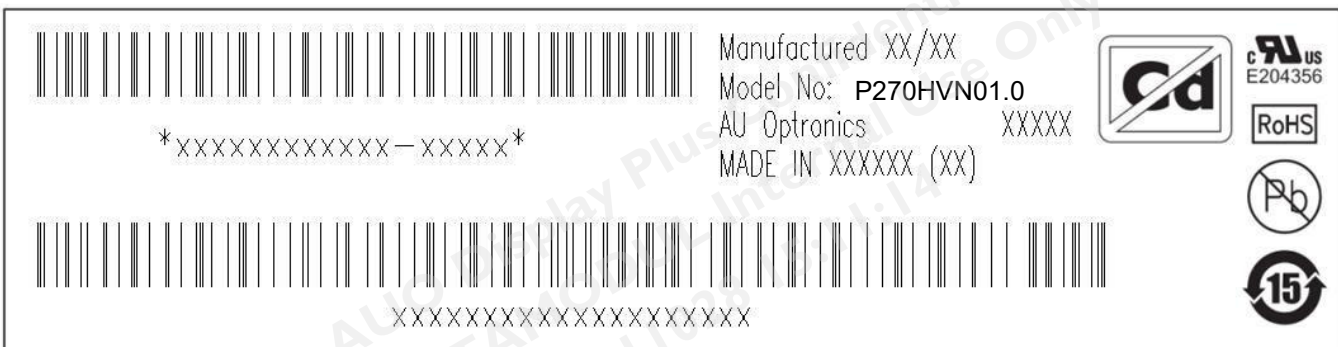
- No data lost
- Self-recoverable
- No hardware failures

Note 3: Result Evaluation Criteria:

TFT-LCD panels test should take place after gradually cooling enough at room temperature. In the normal application, there should be no particular problems that may affect the display function.

9 Shipping Label


The label on the panel is shown as below:



Green mark description

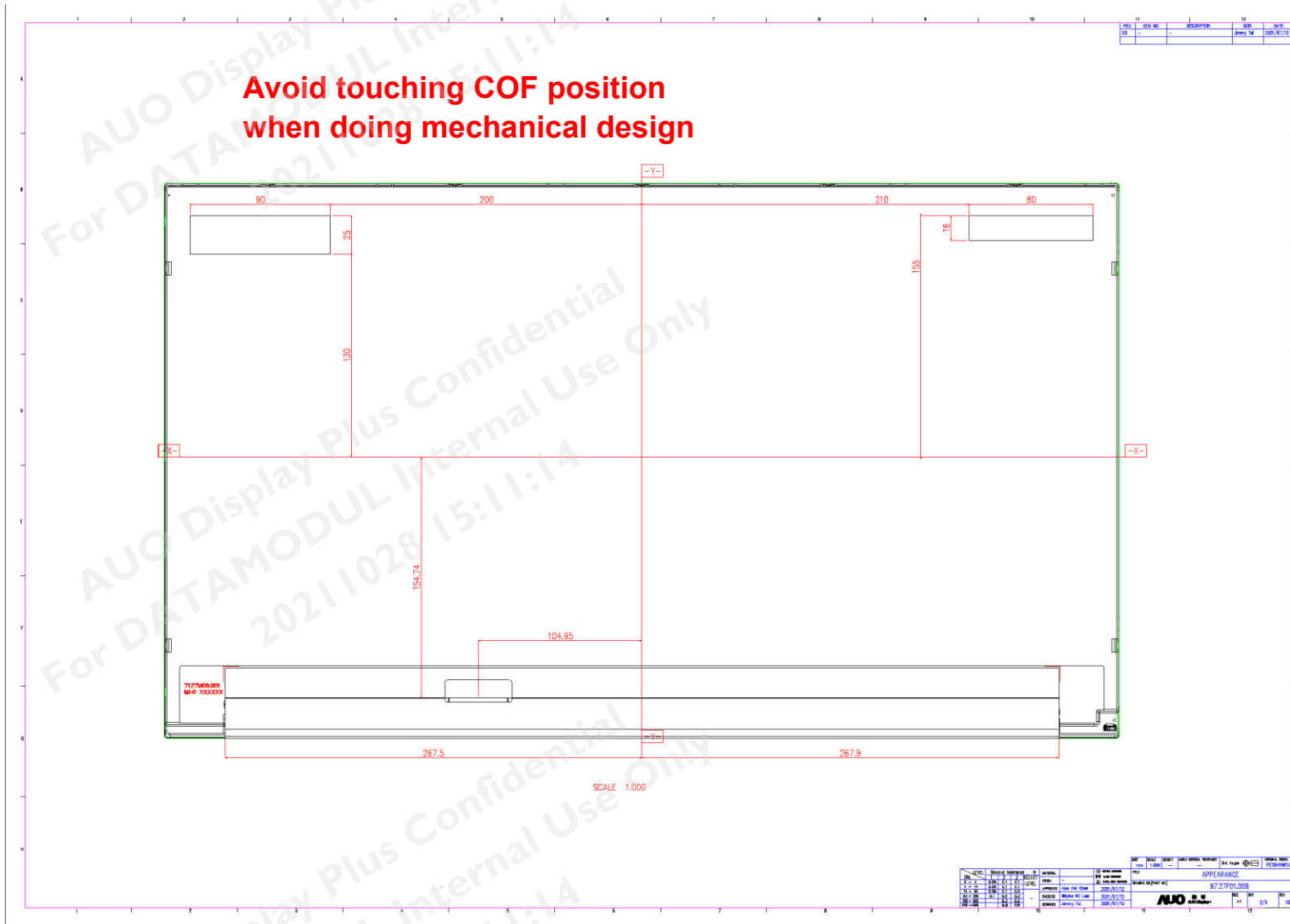
Note 1: For Pb Free products, ADP will add  for identification.

Note 2: For RoHS compatible products, ADP will add  for identification.

Note 3: For China RoHS compatible products, ADP will add  for identification.

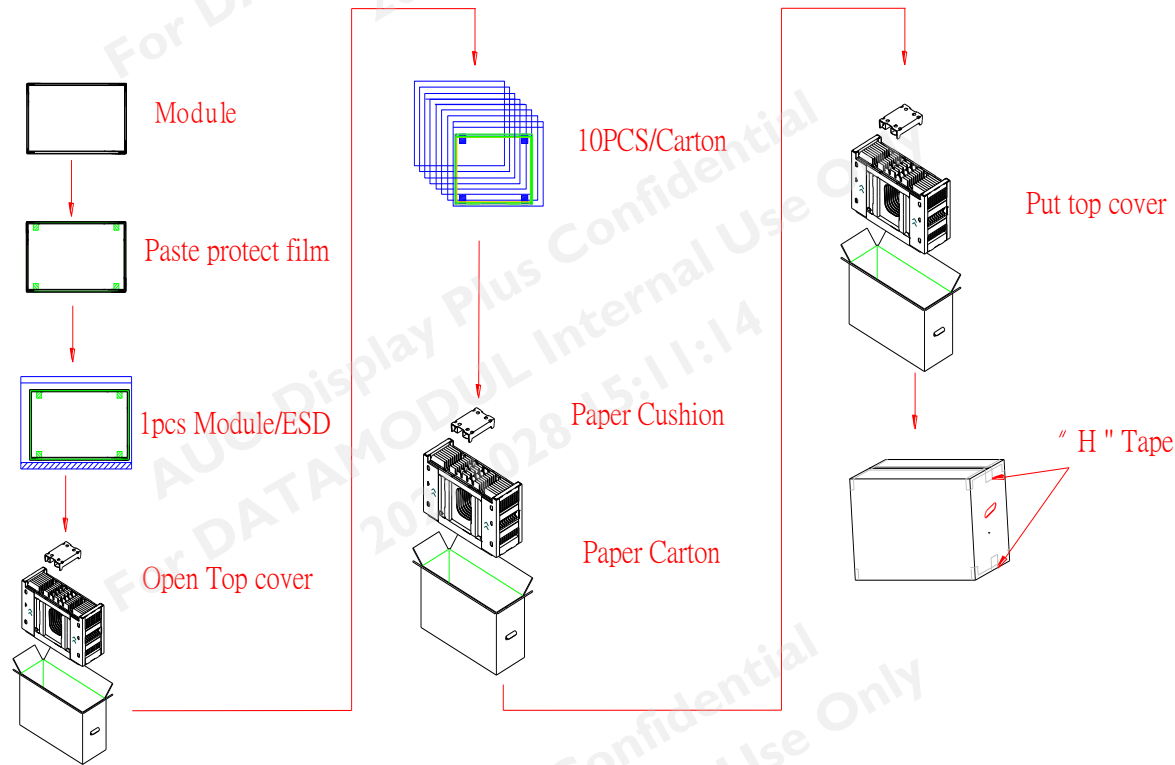
Note 4: The Green Mark will be presented only when the green documents have been ready by ADP Internal Green Team.

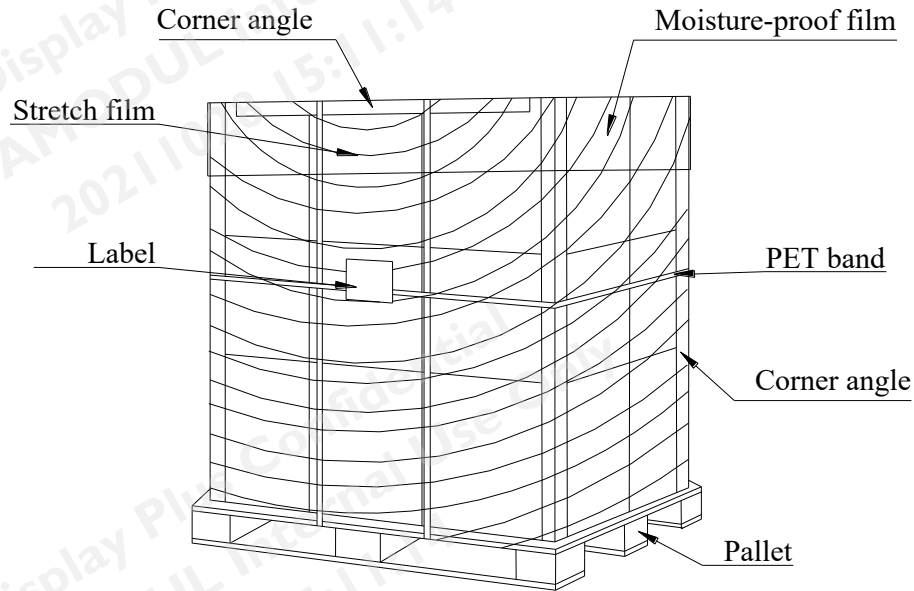
Note 5: For "Cadmium Free product, ADP will add  for identification.



11 Packing Specification

11.1 Packing Flow





11-2 Pallet and shipment information

	Item	Specification			Remark
		Q'ty	Dimension	Weight(kg)	
1	Panel	1	613.6(H)mm x 356.85(V)mm x 10(D)mm	2.180	
2	Cushion	1	-	4.41	
3	Box	1	708(L)mm x 261(W)mm x 473(H)mm	1.38	without Panel & cushion
4	Packing Box	10 pcs/Box	708(L)mm x 261(W)mm x 473(H)mm	27.13	with panel & cushion
5	Pallet	1	1070(L)mm x 740(W)mm x 138(H)mm	12.9	
6	Pallet after Packing	8 boxes/pallet	1070(L)mm x 740(W)mm x 1084(H)mm	229.94	



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