



# SPECIFICATION

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**PH102600T013-ZFA**

7.0" - WSVGA - LVDS

Version: 1.1

Date: 02.08.2018

Note: This specification is subject to change without prior notice

## History of Version

Date (mm / dd / yyyy)	Ver.	Edi.	Description	Page	Design by
08/02/2018	01	001	New Drawing	-	Yuan

Total: 27 Page

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## 1. SPECIFICATIONS

### 1.1 Features

Item	Standard Value
Display Type	1024 * 3 (RGB) * 600 Dots
LCD Type	IPS , Normally Black , Transmissive type
Screen size(inch)	7.0 inch
Color configuration	RGB-Strip
Backlight Type	LED B/L
Interface	LVDS Interface
Other(controller/driver IC)	EK79001EB+EK73215BCGB (Or Compatible IC)
ROHS	THIS PRODUCT CONFORMS THE ROHS OF PTC Detail information please refer website : <a href="http://www.powertip.com.tw/news_detail.php?Key=1&amp;cID=1">http://www.powertip.com.tw/news_detail.php?Key=1&amp;cID=1</a>

### 1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	165.75(W) * 105.4(L) * 2.75(H)	mm

#### LCD panel

Item	Standard Value	Unit
Viewing Area	155.21(W) x 86.92 (L)	mm
Active Area	154.21 (W) * 85.92 (L)	mm

Note : For detailed information please refer to LCM drawing

### 1.3 Absolute Maximum Ratings

#### Module

Item	Symbol	Condition	Min.	Max.	Unit
Power Voltage	V <sub>DD</sub>	-	-0.5	5	V
	AV <sub>DD</sub>	-	-0.5	15	V
	V <sub>GH</sub>	-	-0.3	40	V
	V <sub>GL</sub>	-	-20	0.3	V
Operating Temperature	T <sub>OP</sub>	-	-20	70	°C
Storage Temperature	T <sub>ST</sub>	-	-30	70	°C

### 1.4 DC Electrical Characteristics

#### Module

GND = 0V, Ta = 25 °C

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
	V <sub>DD</sub>	-	2.5	3.3	3.6	V	
	AV <sub>DD</sub>	-	9.4	9.6	9.8	V	
	V <sub>GH</sub>	-	17.0	18.0	19.0	V	
	V <sub>GL</sub>	-	-6.6	-6.0	-5.4	V	
Input signal Voltage	V <sub>COM</sub>	-	2.83	3.15	3.45	V	
Input Signal Voltage	V <sub>IH</sub>	-	0.7V <sub>DD</sub>	-	V <sub>DD</sub>	V	
	V <sub>IL</sub>	-	0	-	0.3 V <sub>DD</sub>	V	
Supply Current	I <sub>DD</sub>	V <sub>DD</sub> = 3.3 V Pattern= Red	-	-	-	mA	Note1
	I <sub>ADD</sub>	AV <sub>DD</sub> =9.6V Pattern= Red	-	-	-	mA	
	I <sub>GH</sub>	V <sub>GH</sub> =18.0V Pattern= Red	-	-	-	mA	
	I <sub>GL</sub>	V <sub>GL</sub> =-6.0V Pattern= Red	-	-	-	mA	
	I <sub>Vcom</sub>	V <sub>COM</sub> =3.15V Pattern= Red	-	-	-	uA	

Note1:Maximum current display

## 1.5 Optical Characteristics

### TFT LCD Module

VDD = 3.3 V, Ta=25°C

Item	Symbol	Condition	Min.	Typ.	Max.	unit		
Response time	Tr + Tf	Ta = 25°C θX, θY = 0°	-	30	40	ms	Note 2	
Viewing angle	Top	θY+	CR ≥ 10	-	80	-	Deg.	Note 4
	Bottom	θY-		-	80	-		
	Left	θX-		-	80	-		
	Right	θX+		-	80	-		
Contrast ratio	CR		500	600	-	-	Note 3	
Color of CIE Coordinate ( With B/L )	White	X	Ta = 25°C θX , θY = 0°	-	-	-	-	Note1
		Y		-	-	-		
	Red	X		-	-	-		
		Y		-	-	-		
	Green	X		-	-	-		
		Y		-	-	-		
	Blue	X		-	-	-		
		Y		-	-	-		
Average Brightness Pattern=white display (With LCD)*1	IV	IF=200 mA	-	(400)	-	cd/m <sup>2</sup>	Note1	
Uniformity (With LCD)*2	△B	IF=200 mA	70	-	-	%	Note1	

Note 1:

\*1 :  $\Delta B = B(\text{min}) / B(\text{max}) * 100\%$

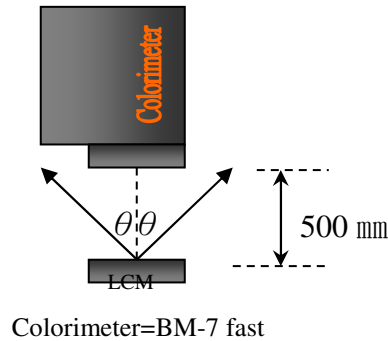
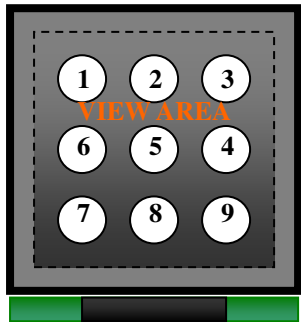
\*2 : Measurement Condition for Optical Characteristics:

a : Environment:  $25^{\circ}\text{C} \pm 5^{\circ}\text{C}$  /  $60 \pm 20\%$  R.H , no wind , dark room below 10 Lux at typical lamp current and typical operating frequency.

b : Measurement Distance:  $500 \pm 50$  mm , ( $\theta = 0^{\circ}$ )

c : Equipment: TOPCON BM-7 fast , (field  $1^{\circ}$ ) , after 10 minutes operation.

d : The uncertainty of the C.I.E coordinate measurement  $\pm 0.01$  , Average Brightness  $\pm 4\%$



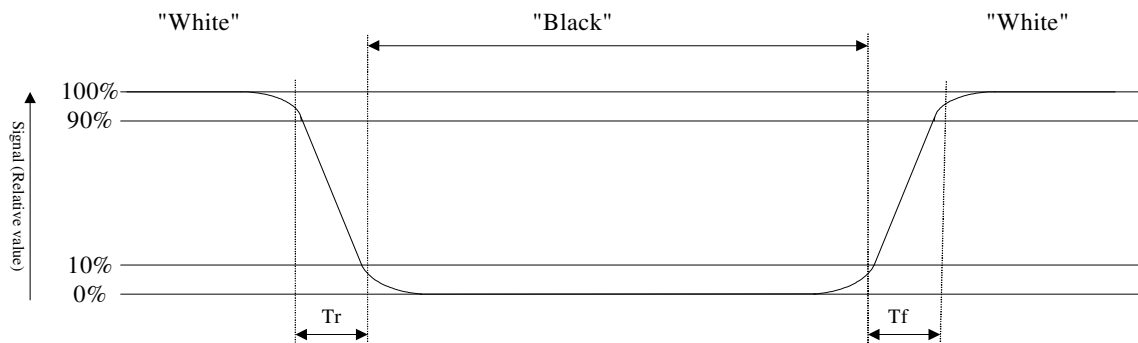
To be measured at the center area of panel with a viewing cone of  $1^{\circ}$  by Topcon luminance meter BM-7, after 10 minutes operation (module)

Note2: Definition of response time:

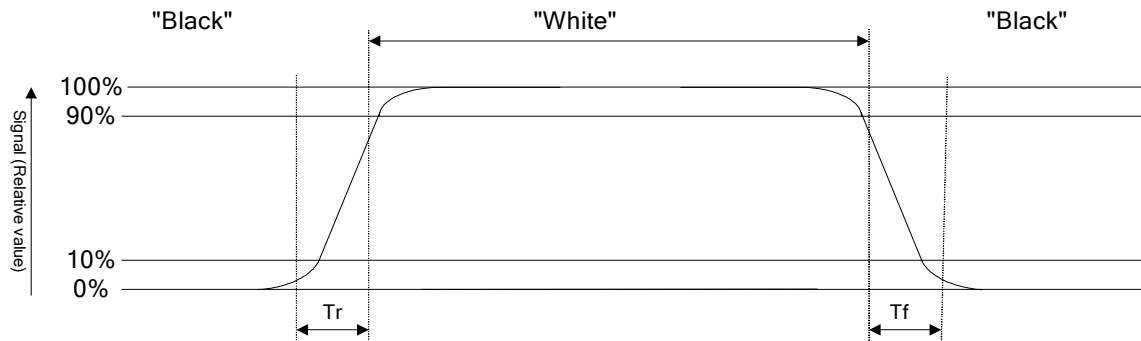
The output signals of photo detector are measured when the input signals are changed from "black" to "white"(falling time) and from "white" to "black"(rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

Refer to figure as below:

Normally White



### Normally Black



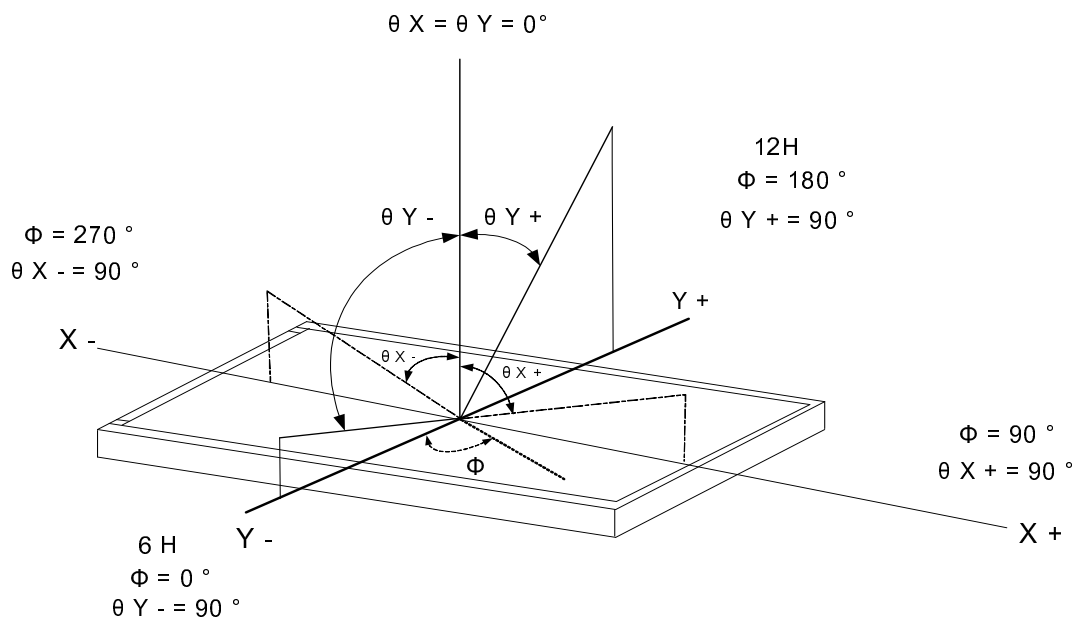
Note3: Definition of contrast ratio:

Contrast ratio is calculated with the following formula

$$\text{Contrast ratio (CR)} = \frac{\text{Photo detector output when LCD is at "White" state}}{\text{Photo detector output when LCD is at "Black" state}}$$

Note4: Definition of viewing angle:

Refer to figure as below:



Note5: Applying with spectrophotometer in the condition of 400 to 700nm, 10nm/each; in accordance with JIS Z 8701 2 degree viewing XYZ system, measuring the reflective rate of 5 degree



## 1.6 Backlight Characteristics

### Maximum Ratings

Item	Symbol	Conditions	Min.	Max.	Unit
LED Forward Current	IF	Ta =25°C	-	-	mA
LED Reverse Voltage	VR	Ta =25°C	-	-	V
Power Dissipation	PD	Ta =25°C	-	-	W

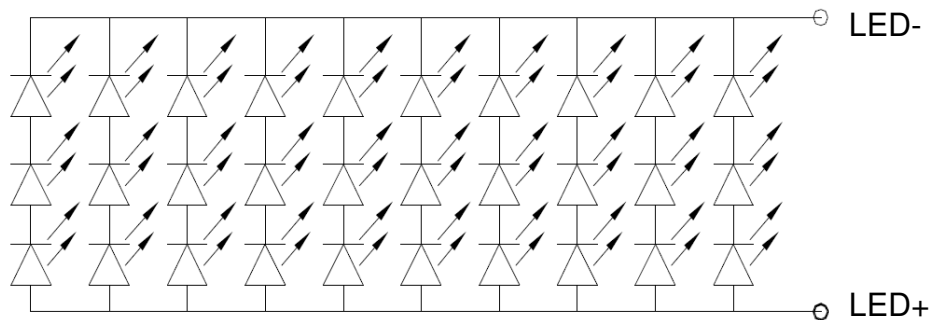
### Backlight Characteristics

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Forward Voltage	VF	IF=200mA	-	(9.6)	-	V
Average Brightness (Without LCD )	IV		(13200)	-	cd/m <sup>2</sup>	
CIE Color Coordinate (Without LCD )	X		-	(0.28)	-	-
	Y		-	(0.31)	-	
Uniformity *1	△B		80	-	-	*2
Color	White					

\*1 : This value will be changed while mass production.

\*2 :  $\Delta B = B(\text{min}) / B(\text{max})\%$

B/L Internal Circuit Diagram



### Other Description

Item	Conditions	Description
Life Time	Ta =25°C IF=200mA	20000 hrs

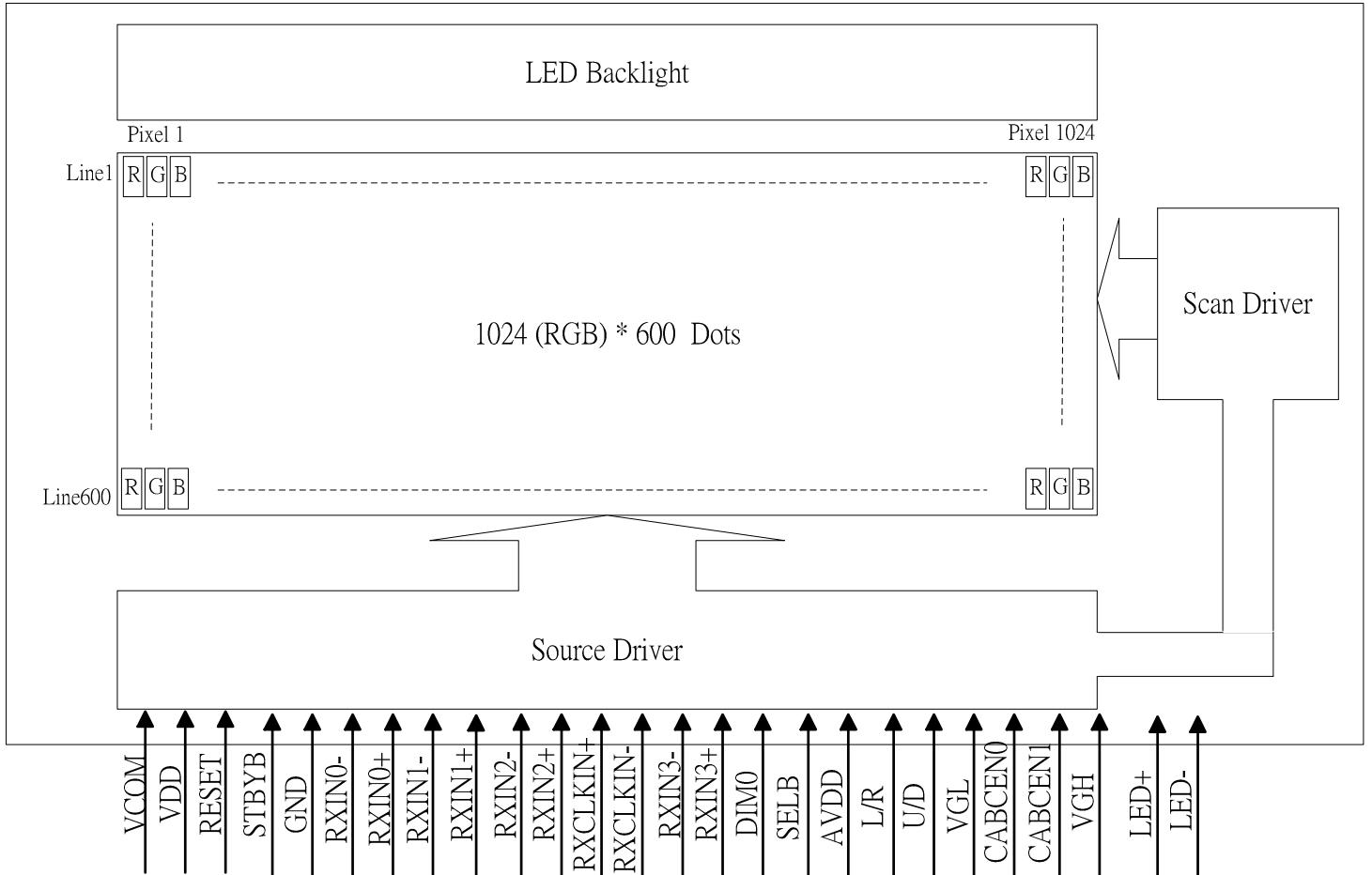
## 2. MODULE STRUCTURE

### 2.1 Counter Drawing

#### 2.1.1 LCM Mechanical Diagram

\* See Appendix

#### 2.1.2 Block Diagram



## 2.2 Interface Pin Description

Pin No.	Symbol	Function
1	VCOM	Common Voltage
2	VDD	Power Voltage for digital circuit
3	VDD	Power Voltage for digital circuit
4	NC	No connection
5	Reset	Global reset pin
6	STBYB	Standby mode, Normally pulled high STBYB = “1” , normal operation STBYB = “0” , timing controller, source driver will turn off, all output are High-Z
7	GND	Ground
8	RXIN0-	- LVDS differential data input
9	RXIN0+	+ LVDS differential data input
10	GND	Ground
11	RXIN1-	- LVDS differential data input
12	RXIN1+	+ LVDS differential data input
13	GND	Ground
14	RXIN2-	- LVDS differential data input
15	RXIN2+	+ LVDS differential data input
16	GND	Ground
17	RXCLKIN-	- LVDS differential clock input
18	RXCLKIN+	+ LVDS differential clock input
19	GND	Ground
20	RXIN3-	- LVDS differential data input
21	RXIN3+	+ LVDS differential data input
22	GND	Ground
23	NC	No Connection
24	NC	No Connection
25	GND	Ground

Pin No.	Symbol	Function
26	NC	No Connection
27	DIM0	Backlight CABC controller signal output DIM0=L Turn off external backlight controller DIM0=H Logical control signal to turn on external backlight controller
28	SELB	6bit/8bit mode select If LVDS input data is 6 bits ,SELB must be set to High; If LVDS input data is 8 bits ,SELB must be set to Low.
29	AVDD	Power for Analog Circuit
30	GND	Ground
31	LED-	LED Cathode
32	LED-	LED Cathode
33	L/R	Horizontal inversion When L/R="0", set right to left scan direction. When L/R="1", set left to right scan direction.
34	U/D	Vertical inversion When U/D="0", set top to bottom scan direction. When U/D="1", set bottom to top scan direction.
35	VGL	Gate OFF Voltage
36	CABCEN1	CABC H/W enable Note:1
37	CABCEN0	CABC H/W enable Note:1
38	VGH	Gate ON Voltage
39	LED+	LED Anode
40	LED+	LED Anode

Note1:

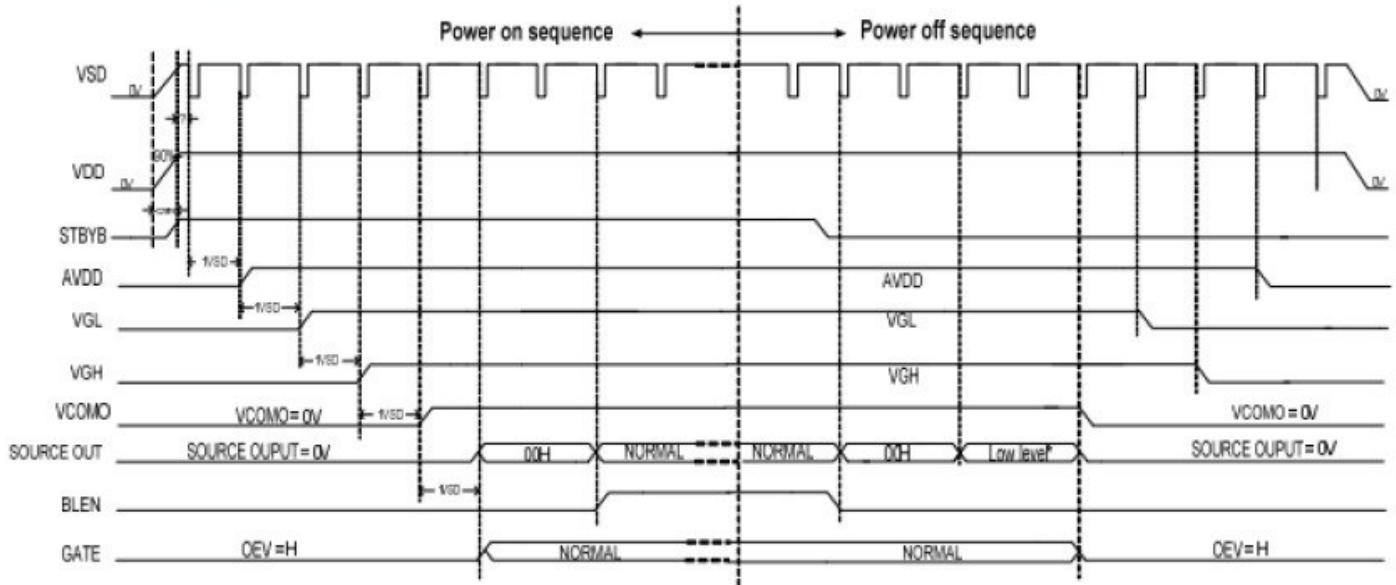
CABCEN1	CABCEN0	DESCRIPTION
L	L	CABC OFF
L	H	User interface Image
H	L	Still Picture
H	H	Moving Image

## 2.3 Timing Characteristics

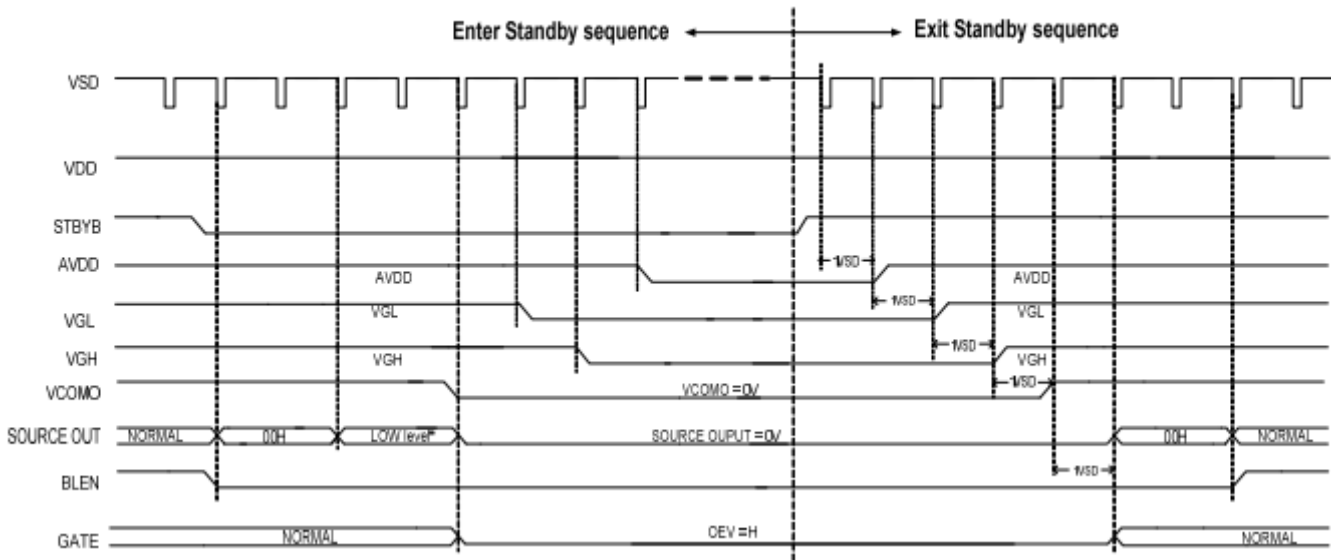
### 2.3.1 Power Sequence

In order to prevent IC from power on reset fail, the rising time ( $T_{POR}$ ) of the digital power supply VDD should be maintained within the given specifications. Refer to "AC Characteristics" for more detail on timing.

#### Power-On/Off Timing Sequence:

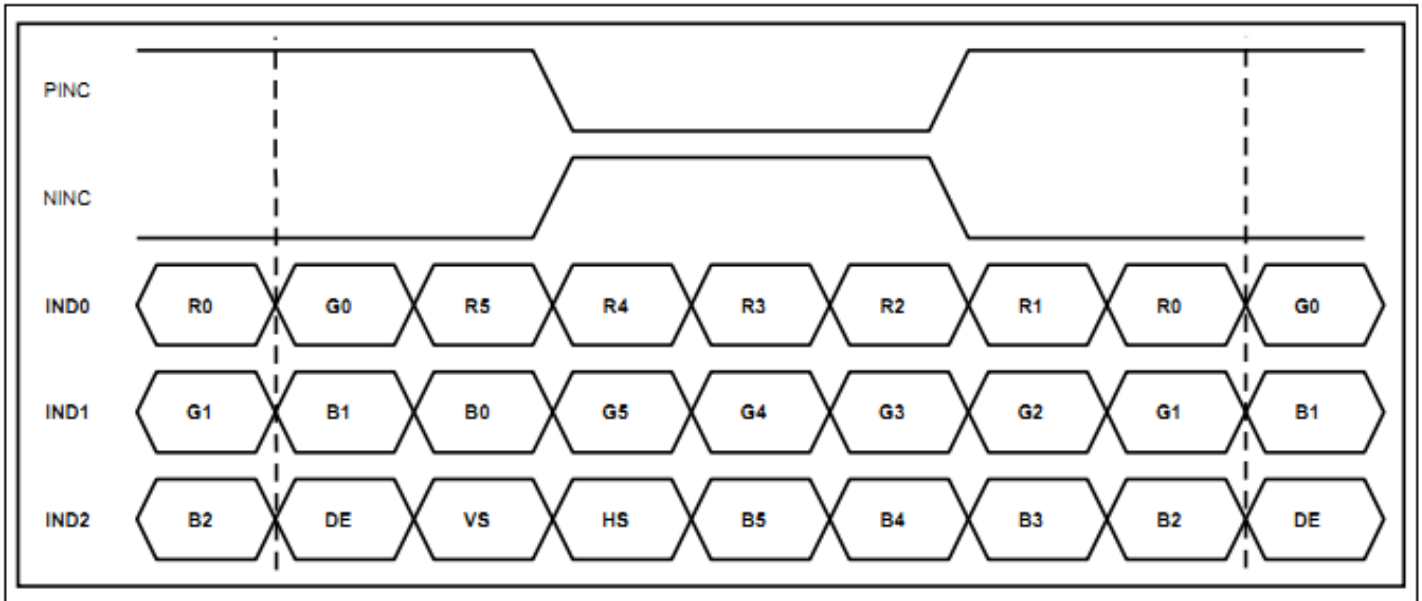


#### Enter and Exit Standby Mode Sequence:

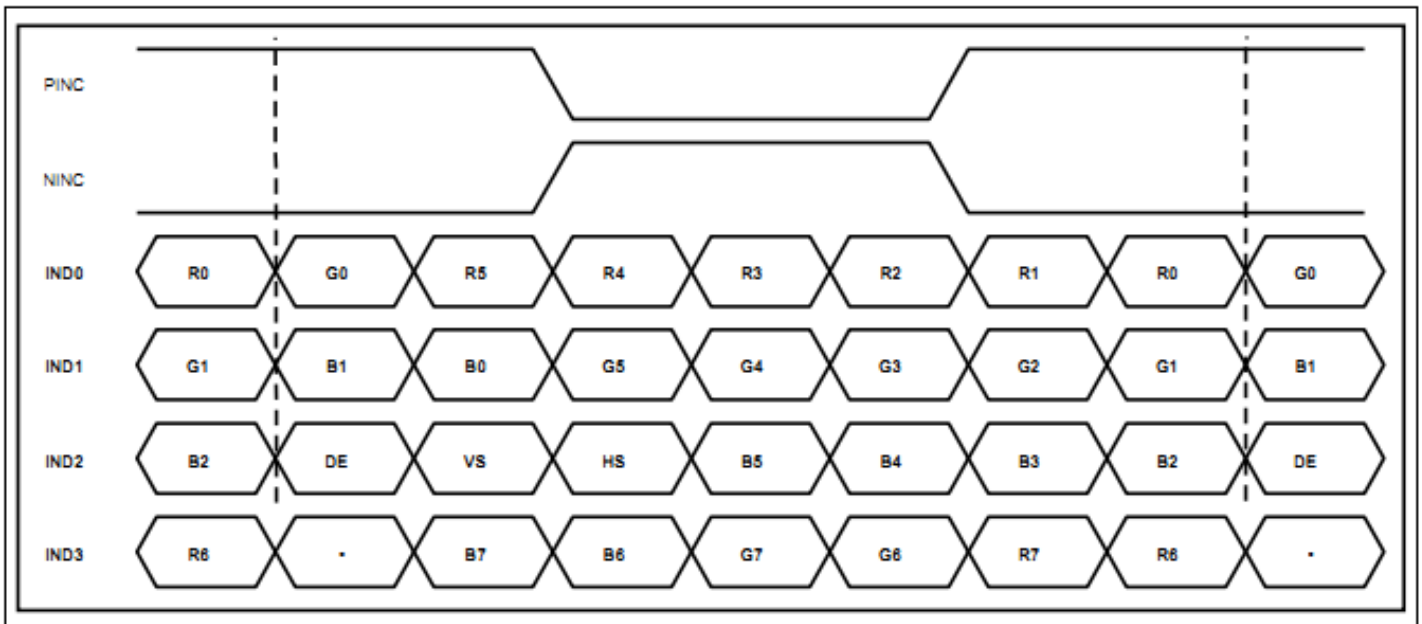


### 2.3.2 Data Input Format for LVDS

#### 6bit LVDS input



#### 8-bit LVDS input (HSD='L')



## 2.4 AC Electrical Characteristics

DE mode

DE mode					
Parameter	Symbol	Value			Unit
		Min.	Typ.	Max.	
DCLK frequency @Frame rate=60hz	fclk	40.8	51.2	67.2	Mhz
Horizontal display area	thd	1024			DCLK
HSYNC period time	th	1114	1344	1400	DCLK
HSYNC blanking	thb+thfp	90	320	376	DCLK
Vertical display area	tvd	600			H
VSYNC period time	tv	610	635	800	H
VSYNC blanking	tvb+tvfp	10	35	200	H

HV mode(1)

HV mode					
Horizontal input timing					
Parameter	Symbol	Value			Unit
Horizontal display area	thd	1024			DCLK
DCLK frequency@ Frame rate=60hz	fclk	Min. 44.9	Typ. 51.2	Max. 63	Mhz
1 Horizontal Line	th	1200	1344	1400	DCLK
HSYNC pulse width	thpw	Min.	1		
		Typ.	—		
		Max.	140		
HSYNC back porch	thbp	160	160	160	DCLK
HSYNC front porch	thfp	16	160	216	

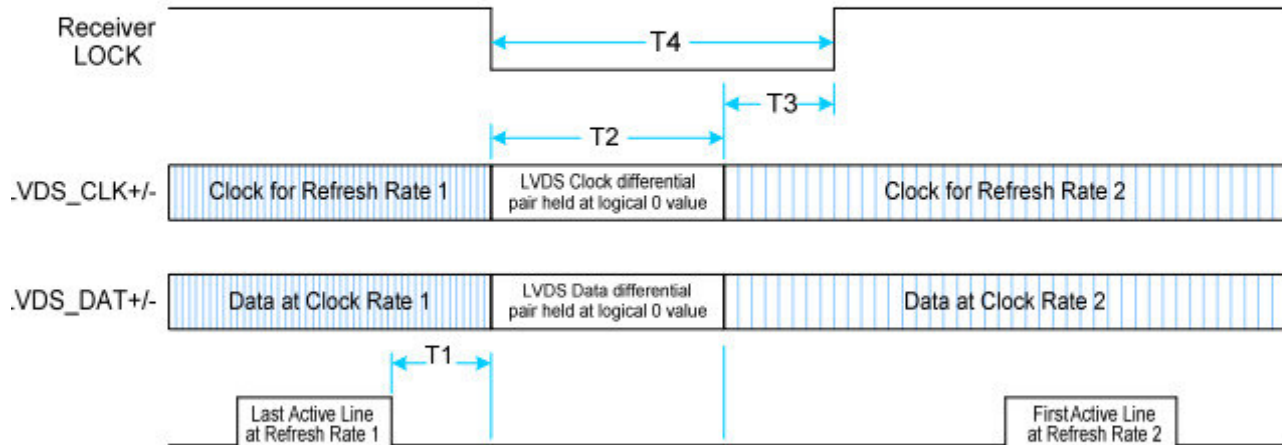
HV mode(2)

Vertical input timing					
Parameter	Symbol	Value			Unit
		Min.	Typ.	Max.	
Vertical display area	tvd	600			H
VSYNC period time	tv	624	635	750	H
VSYNC pulse width	tvpw	1	—	20	H
VSYNC back porch	tvb	23	23	23	H
VSYNC front porch	tvfp	1	12	127	H

## 2.5 Timing

### SDRRS (seamless display refresh rate switching)

When Showing the still picture, it is accept to reduce the refresh rate from 60Hz to low refresh rate (for example 40Hz). The purpose is mainly for power saving. INTEL defined a timing chart switch between different refresh rate. Following this timing chart, the switch between different refresh rates is seamless for end user.



T1 - Min delay from start of vert blank to start of timing change: 2 lines (HSYNC periods)

T2 - Max delay for clock to transition to new frequency: 100us

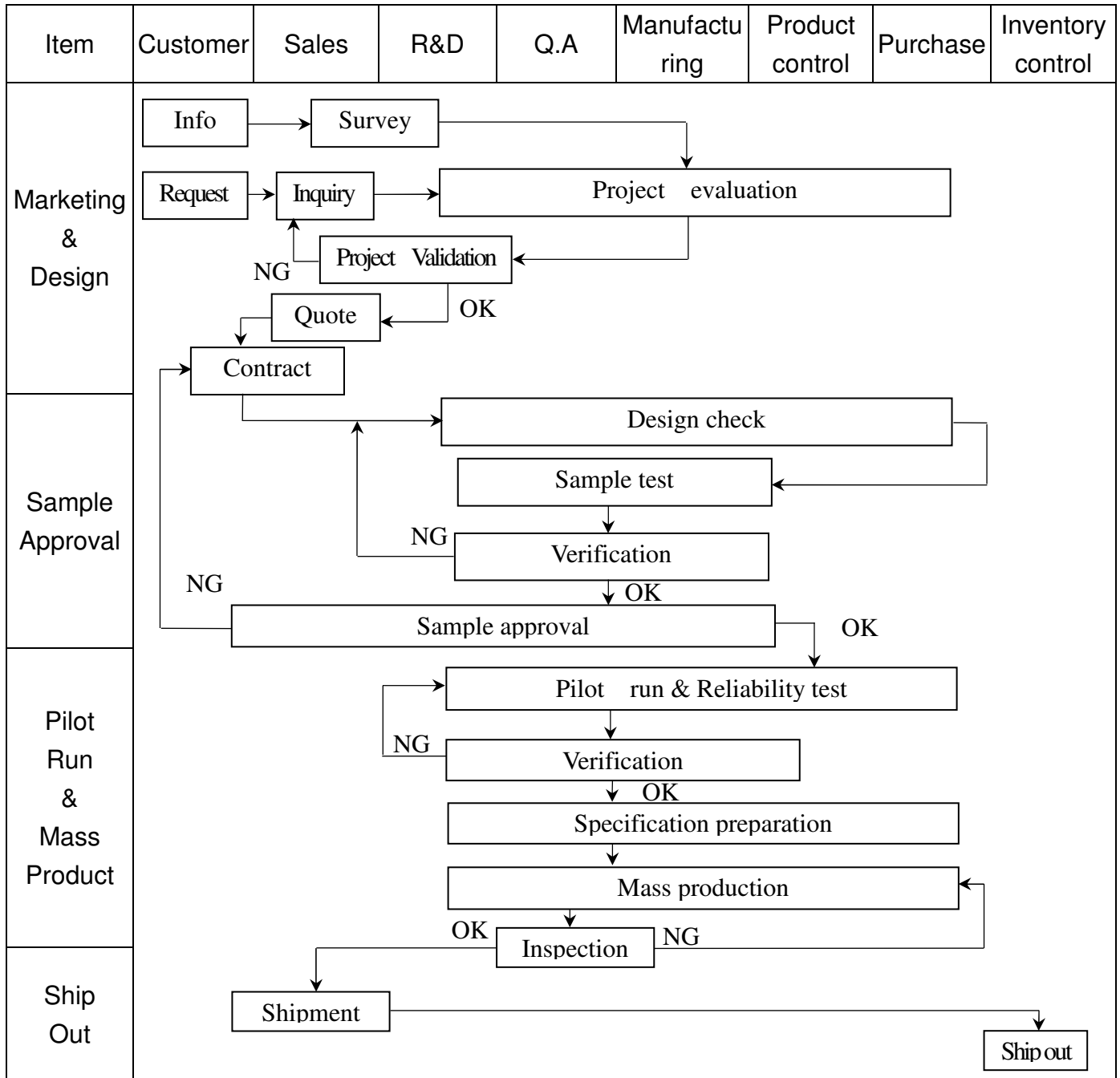
T3 - Max receiver lock delay from stable clock: Display specific (TBD)

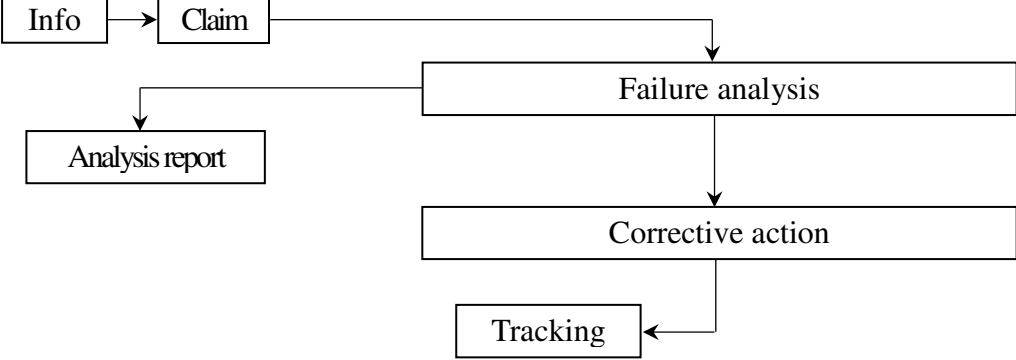
T4 - Max period during which panel maintains display (T2+T3): Display specific (TBD)



### 3. QUALITY ASSURANCE SYSTEM

#### 3.1 Quality Assurance Flow Chart



Item	Customer	Sales	R&D	Q.A	Manufacturing	Product control	Purchase	Inventory control
Sales Service	 <pre> graph TD     Info[Info] --&gt; Claim[Claim]     Claim --&gt; Failure[Failure analysis]     Failure --&gt; Report[Analysis report]     Failure --&gt; Action[Corrective action]     Action --&gt; Tracking[Tracking]           </pre>							
Q.A Activity	1. ISO 9001 Maintenance Activities 3. Equipment calibration 5. Standardization Management				2. Process improvement proposal 4. Education And Training Activities			

### 3.2. Inspection Specification

◆Scope : The document shall be applied to TFT-LCD Module for 3.5" ~15" (Ver.B01).

◆Inspection Standard : MIL-STD-105E Table Normal Inspection Single Sampling Level II.

◆Equipment : Gauge 、MIL-STD 、Powertip Tester 、Sample

◆Defect Level : Major Defect AQL : 0.4 ; Minor Defect AQL : 1.5

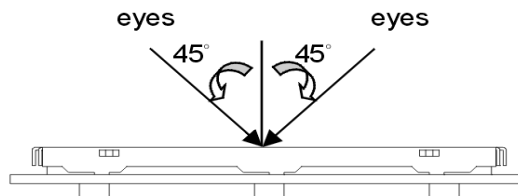
◆OUT Going Defect Level : Sampling.

◆Standard of the product appearance test :

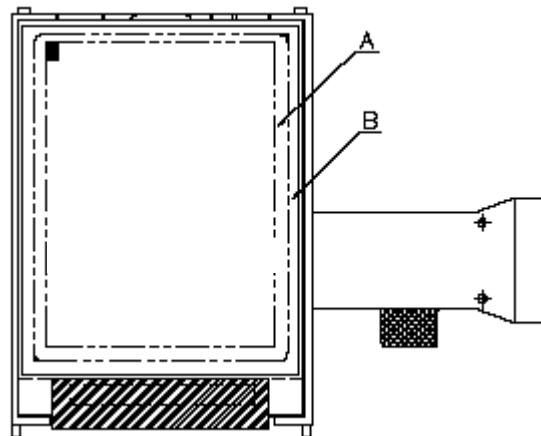
a. Manner of appearance test :

(1). The test best be under 20W×2 fluorescent light , and distance of view must be at 30 cm.

(2). The test direction is base on about around 45° of vertical line.



(3). Definition of area.



*A* area : viewing area

*B* area : Outside of viewing area

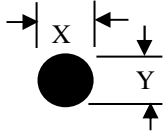
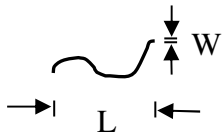
(4). Standard of inspection : (Unit : mm)

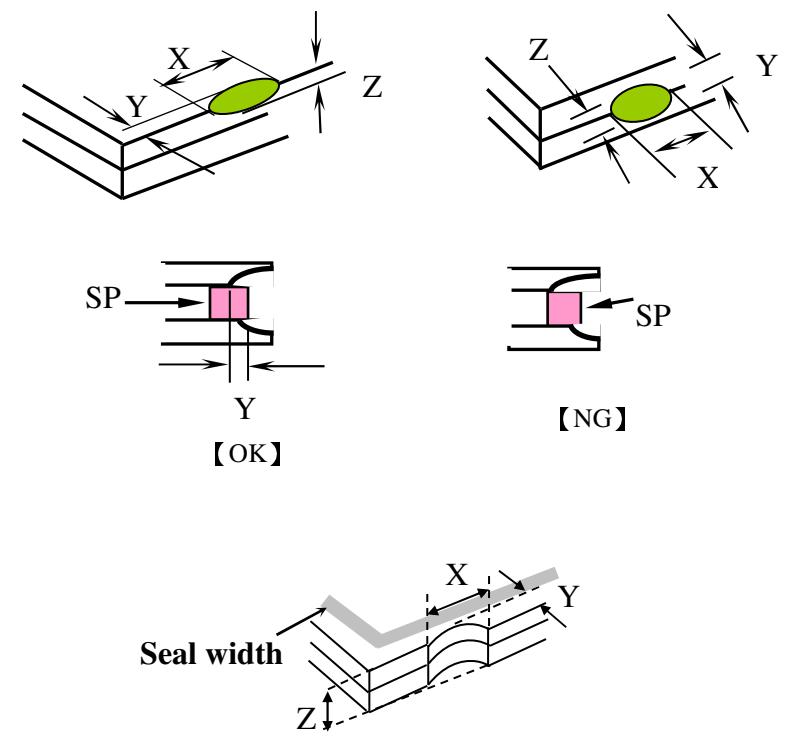
**◆Specification For TFT-LCD Module 3.5" ~15" :**
**(Ver.B01)**

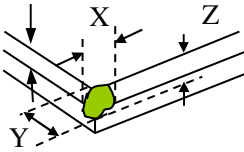
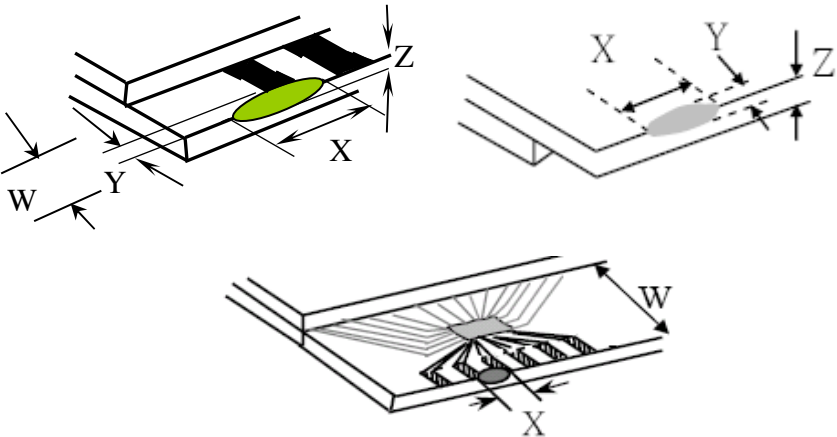
NO	Item	Criterion	Level												
01	Product condition	1. 1The part number is inconsistent with work order of production.	Major												
		1. 2 Mixed product types.	Major												
		1. 3 Assembled in inverse direction.	Major												
02	Quantity	2. 1The quantity is inconsistent with work order of production.	Major												
03	Outline dimension	3. 1 Product dimension and structure must conform to structure diagram.	Major												
04	Electrical Testing	4. 1 Missing line character and icon.	Major												
		4. 2 No function or no display.	Major												
		4. 3 Display malfunction.	Major												
		4. 4 LCD viewing angle defect.	Major												
		4. 5 Current consumption exceeds product specifications.	Major												
		4. 6 Mura can not be seen through 5% ND filter. (Mura : Under the normal examination angle of view,the picture has the non-uniform phenomenon.)	Minor												
05	Dot defect (Bright dot 、 Dark dot)  On -display	<table border="1" style="margin-left: auto; margin-right: auto;"> <thead> <tr> <th colspan="2">Item</th> <th>Acceptance (Q'ty)</th> </tr> </thead> <tbody> <tr> <td rowspan="4" style="text-align: center; vertical-align: middle;">Dot Defect</td> <td style="text-align: center;">Bright Dot</td> <td style="text-align: center;"><math>\leq 4</math></td> </tr> <tr> <td style="text-align: center;">Dark Dot</td> <td style="text-align: center;"><math>\leq 5</math></td> </tr> <tr> <td style="text-align: center;">Joint Dot</td> <td style="text-align: center;"><math>\leq 3</math></td> </tr> <tr> <td style="text-align: center;">Total</td> <td style="text-align: center;"><math>\leq 7</math></td> </tr> </tbody> </table>	Item		Acceptance (Q'ty)	Dot Defect	Bright Dot	$\leq 4$	Dark Dot	$\leq 5$	Joint Dot	$\leq 3$	Total	$\leq 7$	Minor
		Item		Acceptance (Q'ty)											
		Dot Defect	Bright Dot	$\leq 4$											
			Dark Dot	$\leq 5$											
			Joint Dot	$\leq 3$											
Total	$\leq 7$														
5. 1 Inspection pattern : full white , full black , Red , Green and blue screens.															
5. 2 It is defined as dot defect if defect area $> 1/2$ dot.															
5. 3 The distance between two dot defect $\geq 5$ mm.															
5. 4 Bright dot that can not be seen through 5% ND filter.															

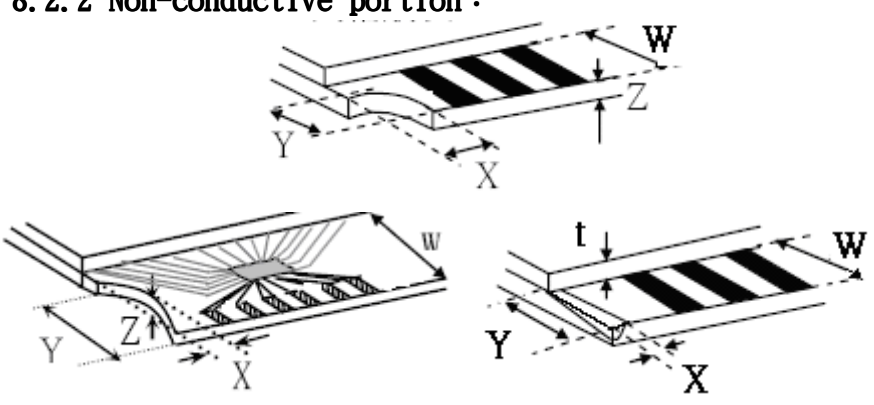
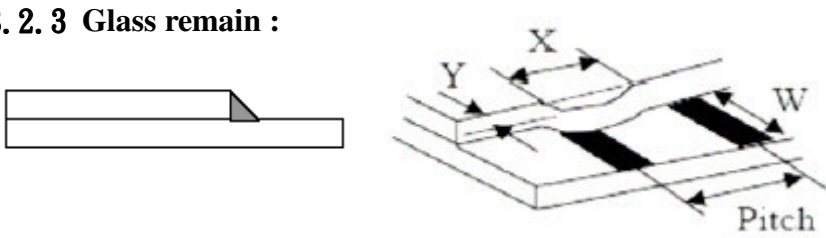
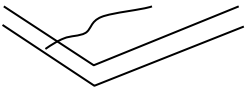
**◆Specification For TFT-LCD Module 3.5" ~15" :**

(Ver.B01)

NO	Item	Criterion	Level																																																													
06	Black or white dot、scratch、contamination  Round type  $\Phi = (x + y) / 2$  Line type 	<b>6.1 Round type ( Non-display or display ) :</b>  <table border="1"> <thead> <tr> <th rowspan="2">Dimension (diameter : <math>\Phi</math>)</th> <th colspan="2">Acceptance (Q'ty)</th> </tr> <tr> <th>A area</th> <th>B area</th> </tr> </thead> <tbody> <tr> <td><math>\Phi \leq 0.25</math></td> <td colspan="2">Ignore</td> </tr> <tr> <td><math>0.25 &lt; \Phi \leq 0.50</math></td> <td>5</td> <td rowspan="3">Ignore</td> </tr> <tr> <td><math>\Phi &gt; 0.50</math></td> <td>0</td> </tr> <tr> <td><b>Total</b></td> <td>5</td> </tr> </tbody> </table>  <b>6.2 Line type( Non-display or display ) :</b>  <table border="1"> <thead> <tr> <th rowspan="2">module size</th> <th rowspan="2">Length (L)</th> <th rowspan="2">Width (W)</th> <th colspan="2">Acceptance (Q'ty)</th> </tr> <tr> <th>A area</th> <th>B area</th> </tr> </thead> <tbody> <tr> <td rowspan="5">3.5" to less 9"</td> <td>---</td> <td><math>W \leq 0.03</math></td> <td colspan="2">Ignore</td> </tr> <tr> <td><math>L \leq 10.0</math></td> <td><math>0.03 &lt; W \leq 0.05</math></td> <td>4</td> <td rowspan="5">Ignore</td> </tr> <tr> <td><math>L \leq 5.0</math></td> <td><math>0.05 &lt; W \leq 0.10</math></td> <td>2</td> </tr> <tr> <td>---</td> <td><math>W &gt; 0.10</math></td> <td colspan="2">As round type</td> </tr> <tr> <td colspan="3">Total</td> <td colspan="2">5</td> </tr> <tr> <td rowspan="5">9" to 15"</td> <td>---</td> <td><math>W \leq 0.05</math></td> <td colspan="2">Ignore</td> </tr> <tr> <td><math>L \leq 10.0</math></td> <td><math>0.05 &lt; W \leq 0.10</math></td> <td>5</td> <td rowspan="5">Ignore</td> </tr> <tr> <td>---</td> <td><math>W &gt; 0.10</math></td> <td colspan="2">As round type</td> </tr> <tr> <td colspan="3">Total</td> <td colspan="2">5</td> </tr> </tbody> </table>	Dimension (diameter : $\Phi$ )	Acceptance (Q'ty)		A area	B area	$\Phi \leq 0.25$	Ignore		$0.25 < \Phi \leq 0.50$	5	Ignore	$\Phi > 0.50$	0	<b>Total</b>	5	module size	Length (L)	Width (W)	Acceptance (Q'ty)		A area	B area	3.5" to less 9"	---	$W \leq 0.03$	Ignore		$L \leq 10.0$	$0.03 < W \leq 0.05$	4	Ignore	$L \leq 5.0$	$0.05 < W \leq 0.10$	2	---	$W > 0.10$	As round type		Total			5		9" to 15"	---	$W \leq 0.05$	Ignore		$L \leq 10.0$	$0.05 < W \leq 0.10$	5	Ignore	---	$W > 0.10$	As round type		Total			5		Minor
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		<table border="1" data-bbox="539 1579 1353 1870"> <thead> <tr> <th>X</th> <th>Y</th> <th>Z</th> </tr> </thead> <tbody> <tr> <td><math>\leq a</math></td> <td>Crack can't enter viewing area</td> <td><math>\leq 1/2 t</math></td> </tr> <tr> <td><math>\leq a</math></td> <td>Crack can't exceed the half of SP width.</td> <td><math>1/2 t &lt; Z \leq 2 t</math></td> </tr> </tbody> </table>		X	Y	Z	$\leq a$	Crack can't enter viewing area	$\leq 1/2 t$
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		<p><b>8.2 Protrusion over terminal :</b></p> <p><b>8.2.1 Chip on electrode pad :</b></p>  <table border="1" data-bbox="557 1697 1347 1872"> <thead> <tr> <th></th> <th>X</th> <th>Y</th> <th>Z</th> </tr> </thead> <tbody> <tr> <td><b>Front</b></td> <td><math>\leq a</math></td> <td><math>\leq 1/2 W</math></td> <td><math>\leq t</math></td> </tr> <tr> <td><b>Back</b></td> <td><math>\leq a</math></td> <td><math>\leq W</math></td> <td><math>\leq 1/2 t</math></td> </tr> </tbody> </table>		X	Y	Z	<b>Front</b>	$\leq a$	$\leq 1/2 W$	$\leq t$	<b>Back</b>	$\leq a$	$\leq W$	$\leq 1/2 t$	Minor
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X	Y	Z													
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**◆Specification For TFT-LCD Module 3.5" ~15" :**
**(Ver.B01)**

NO	Item	Criterion	Level
09	Backlight elements	9. 1 Backlight can't work normally.	Major
		9. 2 Backlight doesn't light or color is wrong.	Major
		9. 3 Illumination source flickers when lit.	Major
10	General appearance	10. 1 Pin type 、 quantity 、 dimension must match type in structure diagram.	Major
		10. 2 No short circuits in components on PCB or FPC .	Major
		10. 3 Parts on PCB or FPC must be the same as on the production characteristic chart .There should be no wrong parts , missing parts or excess parts.	Major
		10. 4 Product packaging must the same as specified on packaging specification sheet.	Minor
		10. 5 The folding and peeled off in polarizer are not acceptable.	Minor
		10. 6 The PCB or FPC between B/L assembled distance(PCB or FPC ) is $\leq 1.5$ mm.	Minor

## 4. RELIABILITY TEST

(Ver.B01)

NO.	TEST ITEM	TEST CONDITION											
1	High Temperature Storage Test	Keep in <b>70 ±5°C</b> 240 hrs											
2	Low Temperature Storage Test	Keep in <b>-30 ±5°C</b> 240 hrs											
3	High Temperature / High Humidity Storage Test	Keep in <b>60 °C / 90% R.H</b> duration for 240 hrs (Excluding the polarizer)											
4	Temperature Cycling Storage Test	$\begin{array}{ccccccc} & -30^{\circ}\text{C} & \rightarrow & +25^{\circ}\text{C} & \rightarrow & 70^{\circ}\text{C} & \rightarrow & +25^{\circ}\text{C} \\ & (30\text{mins}) & & (5\text{mins}) & & (30\text{mins}) & & (5\text{mins}) \\ & \longleftarrow & & & & & & \longrightarrow \\ & & & & & & & \text{20 Cycle} \end{array}$											
5	ESD Test	<b>Air Discharge:</b> Apply <b>2 KV</b> with 5 times Discharge for each polarity +/-	<b>Contact Discharge:</b> Apply <b>250 V</b> with 5 times discharge for each polarity +/-										
		1. Temperature ambience : 15°C ~ 35°C 2. Humidity relative : 30% ~ 60% 3. Energy Storage Capacitance(Cs+Cd) : 150pF±10% 4. Discharge Resistance(Rd) : 330Ω±10% 5. Discharge, mode of operation : Single Discharge (time between successive discharges at least 1 sec) (Tolerance if the output voltage indication : ±5%)											
6	Vibration Test (Packaged)	1. Sine wave <b>10~55</b> Hz frequency (1 min/sweep) 2. The amplitude of vibration : <b>1.5</b> mm 3. Each direction (X、Y、Z) duration for <b>2</b> Hrs											
7	Drop Test (Packaged)	<table border="1" style="margin-left: auto; margin-right: auto;"> <thead> <tr> <th>Packing Weight (Kg)</th> <th>Drop Height (cm)</th> </tr> </thead> <tbody> <tr> <td>0 ~ 45.4</td> <td>122</td> </tr> <tr> <td>45.4 ~ 90.8</td> <td>76</td> </tr> <tr> <td>90.8 ~ 454</td> <td>61</td> </tr> <tr> <td>Over 454</td> <td>46</td> </tr> </tbody> </table> <p style="text-align: center;">Drop Direction : ※1 corner / 3 edges / 6 sides each 1time</p>		Packing Weight (Kg)	Drop Height (cm)	0 ~ 45.4	122	45.4 ~ 90.8	76	90.8 ~ 454	61	Over 454	46
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90.8 ~ 454	61												
Over 454	46												

◎Result Evaluation Criteria :

Under the display quality test conditions with normal operations with normal operation state.  
 Do not change these conditions as such changes may affect practical display function.  
 (Normal operation state)

Temperature : +20~30°C

Humidity : 50~70%

Atmospheric pressure : 86~106Kpa

## 5. PRECAUTION RELATING PRODUCT HANDLING

### 5.1 SAFETY

- 5.1.1 If the LCD panel breaks , be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes , please wash it off immediately by using soap and water.

### 5.2 HANDLING

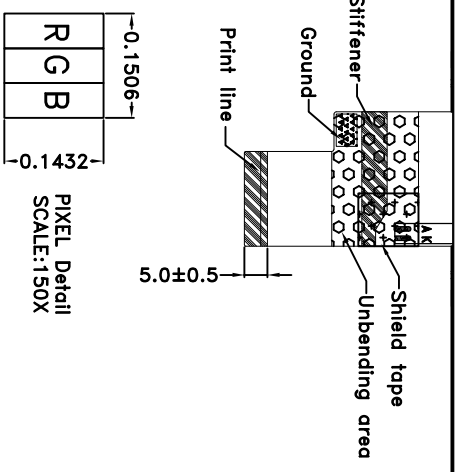
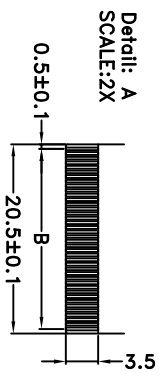
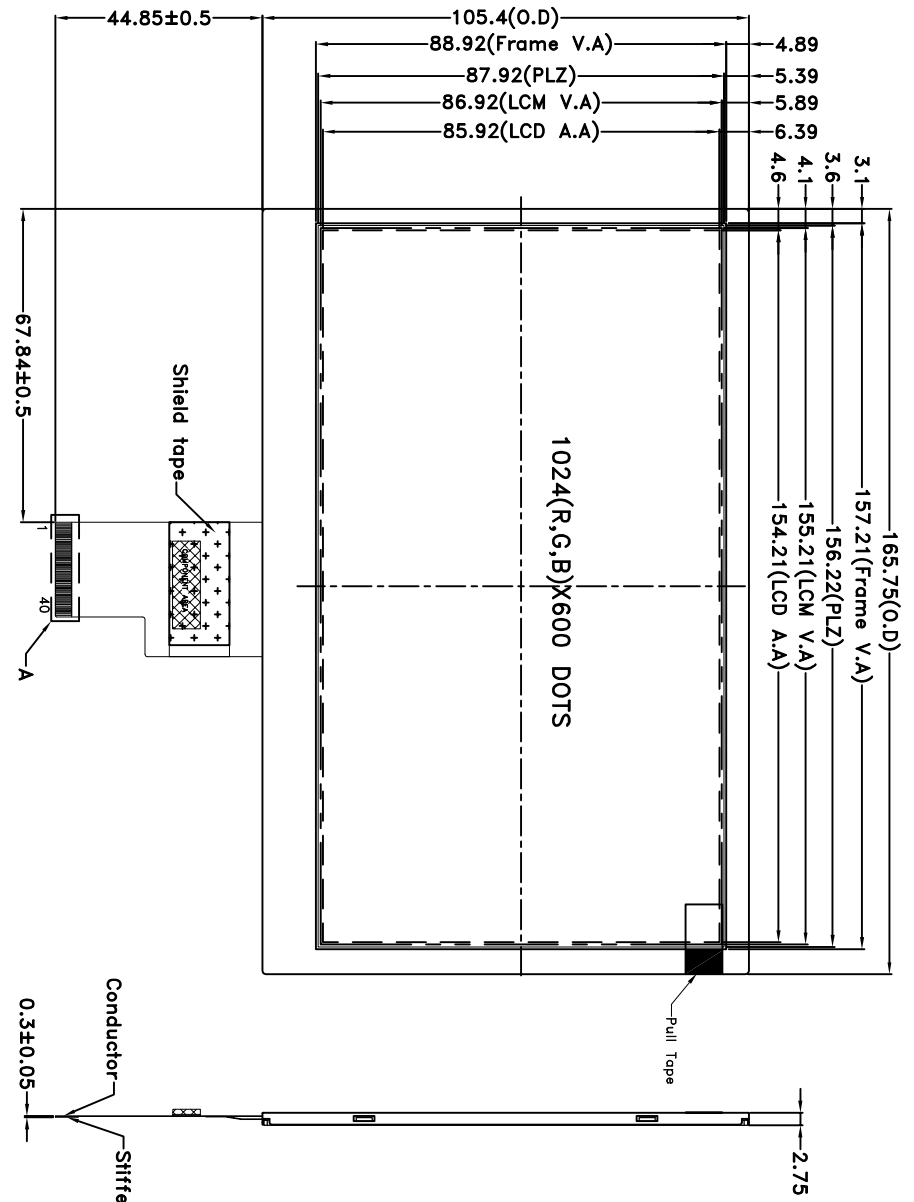
- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module , be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully ,do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth , as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands , this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is  $320\pm 10^{\circ}\text{C}$  and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM .

### 5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is  $25^{\circ}\text{C} \pm 5^{\circ}\text{C}$  and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush , shake , or jolt the module.

## 5.4 TERMS OF WARRANTY

- 5.4.1 Applicable warrant period  
The period is within thirteen months since the date of shipping out under normal using and storage conditions.
- 5.4.2 Unaccepted responsibility  
This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment , we cannot take responsibility if the product is used in nuclear power control equipment , aerospace equipment , fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.



- NOTES:
- 1.LCD TYPE: TFT LCD
  - 2.LCD DISPLAY: Normally Black/TRANSMISSIVE
  - 3.The tolerance unless classified  $\pm 0.3\text{mm}$
  - 4.B.P0.5X39=19.5±0.05; W=0.3±0.05
  5.  $\square$  FPC Component Area(H=1.0Max):
  - 6.FPC suggested connector : HIROSE FH12A-40S-0.5SH or compatible

007				PART NO.:	PH102600T013-ZFA				久正光电股份有限公司 POWER TIP TECHNOLOGY CORPORATION		Design: Bob Zhang		Surface:		Precision:	
006				DRAWING NAME:	LMD-PH102600T013-ZFA						Unit: MM (3)		Material:		Length (mm): 1 ~ 4	
005				TITLE:	LCD MODULE DRAWING		Check: Tina Chen		Scale: 1:1		Thickness:		4 ~ 16		16 ~ 63	
004				REV BY	REV BY		Approve: Jimmy Chen		Page: 1/1		Quantity:		63 ~ 250		250 ~ 1000	
003				REV	NEW DRAWING											
002																
001																
REV																



ALL TECHNOLOGIES. ALL COMPETENCIES. ONE SPECIALIST.



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More information and worldwide locations can be found at

[www.data-modul.com](http://www.data-modul.com)