DATA MODUL

SPECIFICATION

POWERTIP

PH320240T023-IHA01

7.0” - QVGA - RGB

Version: 2.8
Date: 20.03.2020

Note: This specification is subject to change without prior notice

www.data-modul.com

History of Version

Date	Ver.	Edi.	Description	Page	Design by
04/12/2016	01	001	New Drawing	-	譚超敏
04/27/2016	01	002	Update LCM Drawing	Appendix	譚超敏
07/27/2016	01	003	New Sample	-	徐明菲
09/12/2016	01	004	Modify Specification (Modify 1.5 Color of CIE Coordinate)	6	徐明菲
11/15/2016	01	005	Modify Specification (Modify 1.3 &1.4 Power Supply Voltage)	5	徐明菲
03/13/2017	02	006	Change Backlight Characteristics	6,11	張歡
03/21/2018	02	007	Modify Specification (Modify 3.2. Inspection Specification & Modify 4.1 Reliability Test Condition 96 hrs to 240 hrs)	20~27	徐明菲
03/20/2020	02	008	Modify LCM Drawing	Appendix	陳璐

Total: 28 Page

Contents

Note : For detailed information please refer to IC data sheet :
Primacy(TFT LCD): Himax: HX8238-D

1. SPECIFICATIONS

1.1 Features

Main LCD Panel

Item	Standard Value
Display Type	320* (R、G、B) * 240 Dots
LCD Type	Normally white , Transmissive type
Screen size(inch)	3.5(Diagonal)
Viewing Direction	6 O'clock
Color configuration	R.G.B. vertical stripe
Interface	Digital 24-bits RGB/3 wire SPI
Other (controller / driver IC)	Himax: HX8238-D
ROHS	THIS PRODUCT CONFORMS THE ROHS OF PTC Detail information please refer web site : http://www.powertip.com.tw/news.php?area_id_view=1085560481/

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	76.9(W) * 63.9 (L) * 3.2 (H)	mm

LCD panel

Item	Standard Value	Unit
Active Area	70.08 (W) * 52.56 (L)	mm

Note : For detailed information please refer to LCM drawing

1.3 Absolute Maximum Ratings

Module

Item	Symbol	Condition	Min.	Max.	Unit
Power Supply Voltage	DV_{DD}	GND=0	-0.3	4.0	V
Operating Temperature	T_{OP}	-	-20	70	°C
Storage Temperature	T_{ST}	-	-30	80	°C
Storage Humidity	HD	Ta < 60 °C	20	90	%RH

1.4 DC Electrical Characteristics

Module GND = 0V, Ta = 25°C

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Power Supply Voltage	DV_{DD}	-	3.0	3.3	3.6	V
V_{COM} High Voltage	V_{COMH}	-	-	-	5.54	V
V_{COM} Low Voltage	V_{COML}	-	-2.8	-	-	V
Input H/L Level Voltage	V_{IH}	-	0.8*DV_{DD}	-	DV_{DD}	V
	V_{IL}	-	0	-	0.2*DV_{DD}	V
Output H/L Level Voltage	V_{OH}	-	0.9*DV_{DD}	-	DV_{DD}	V
	V_{OL}	-	-	-	0.1*DV_{DD}	V
Supply Current	I_{DD}	DV_{DD}=3.3V	-	10	15	mA

1.5 Optical Characteristics

TFT LCD Panel

DV_{DD}=3.3V, Ta=25°C

Item		Symbol	Condition	Min.	Typ.	Max.	unit	-
Response time	Tr+Tf	25℃	-	-	40	60	ms	Note 2
Viewing angle	Top	Θ+	CR ≥ 10	-	60	-	Deg.	Note 1
	Bottom	Θ-		-	60	-		
	Left	ΘL		-	60	-		
	Right	ΘR		-	60	-		
Contrast ratio		CR		500	600	-	-	Note3
Color of CIE Coordinate (With LCD)	White	X	IF= 20 mA	0.27	0.32	0.37	-	Note4
		Y		0.30	0.35	0.40		
	Red	X		0.59	0.64	0.69		
		Y		0.29	0.34	0.39		
	Green	X		0.29	0.34	0.39		
		Y		0.56	0.61	0.66		
	Blue	X		0.09	0.14	0.19		
		Y		0.04	0.09	0.14		
Average Brightness Pattern=white display (With LCD)*1		IV	IF= 20 mA	800	1000	-	cd/m^2	
Uniformity (With LCD)*2		△B	IF= 20 mA	80	-	-	%	

Note 4 :

1：ΔB=B(min) / B(max) * 100%

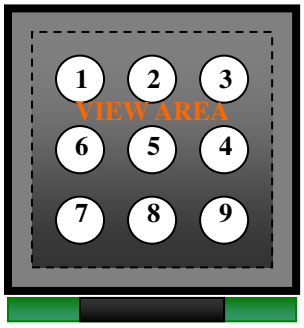
2：Measurement Condition for Optical Characteristics:

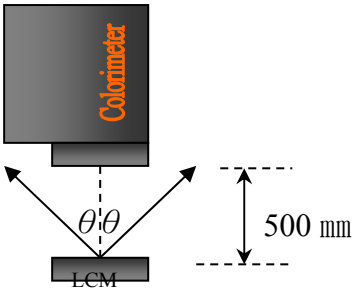
a：Environment: 25℃±5℃ / 60±20%R.H，no wind，dark room below 10 Lux at typical lamp current and typical operating frequency.

b：Measurement Distance: 500 ± 50 ㎜ ，(θ= 0°)

c：Equipment: TOPCON BM-7 fast，(field 1°)，after 10 minutes operation.

d：The uncertainty of the C.I.E coordinate measurement ±0.01，Average Brightness ± 4%



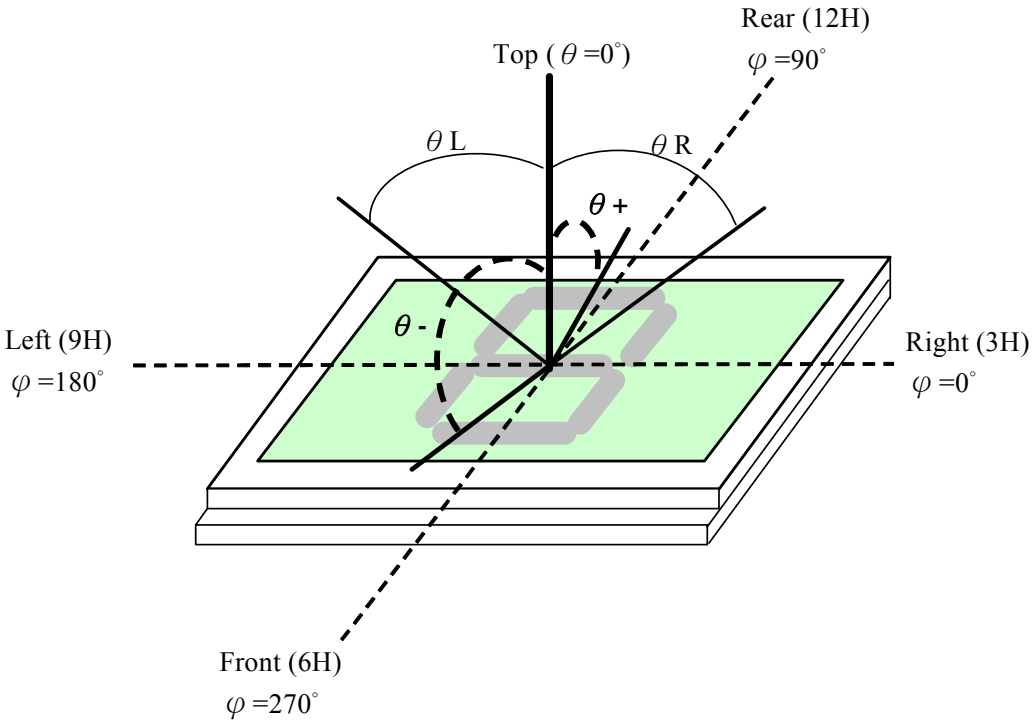


Colorimeter=BM-7 fast

Note 1.

Optical characteristics-2

Viewing angle

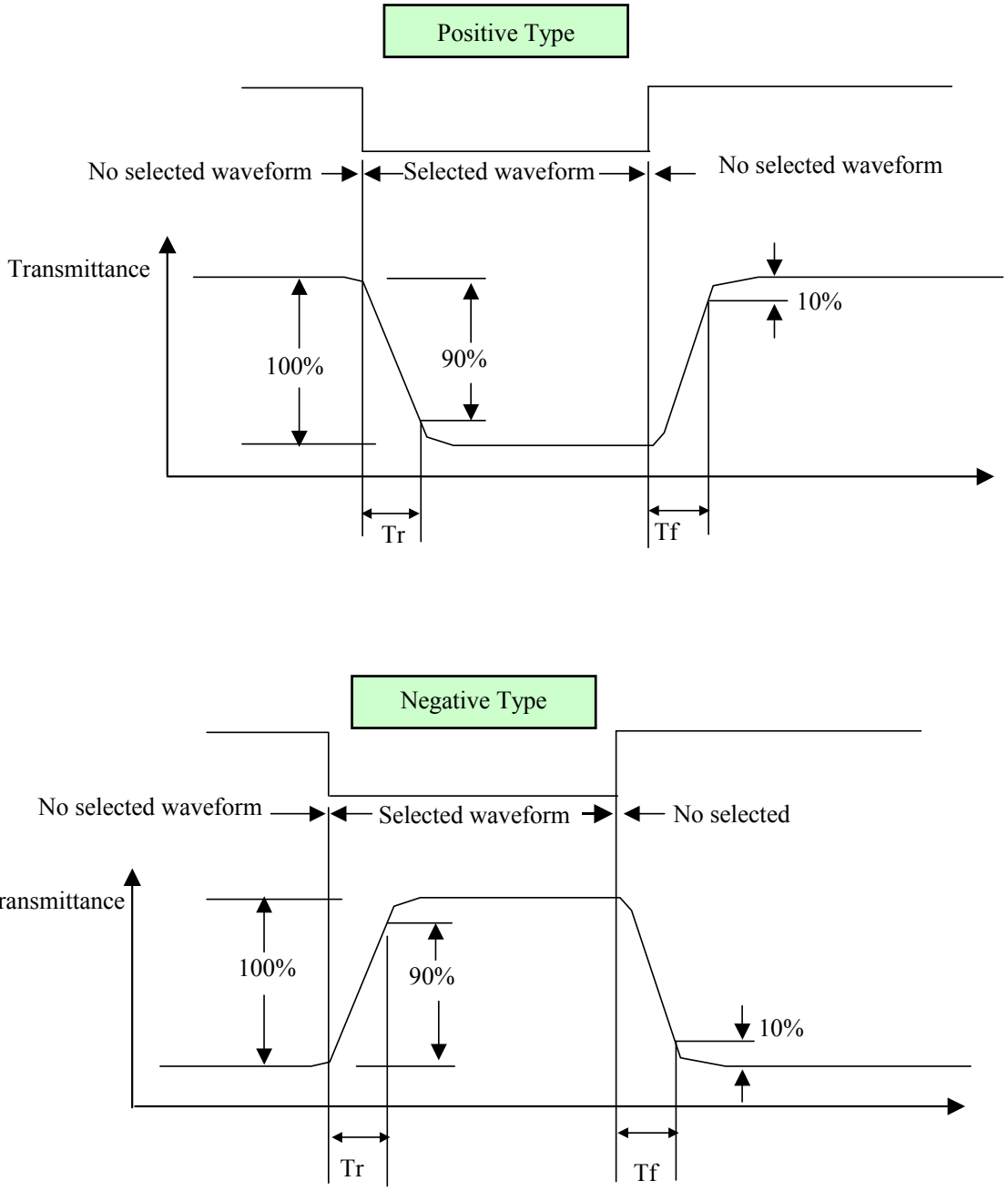


Viewing angle

Note 2.

Optical characteristics-3

Fig.2 Definition of response time



Electrical characteristics-2

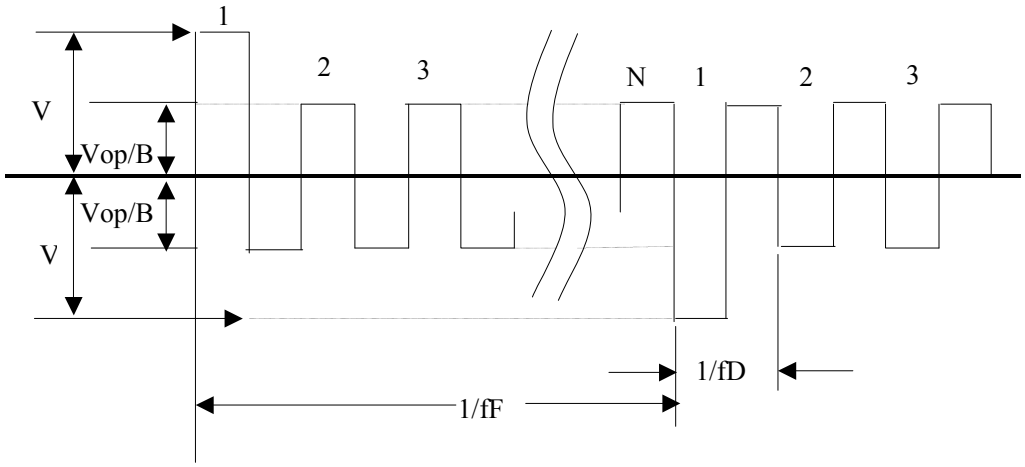
※2 Drive waveform

Vop: Drive voltage　　fF: Frame frequency

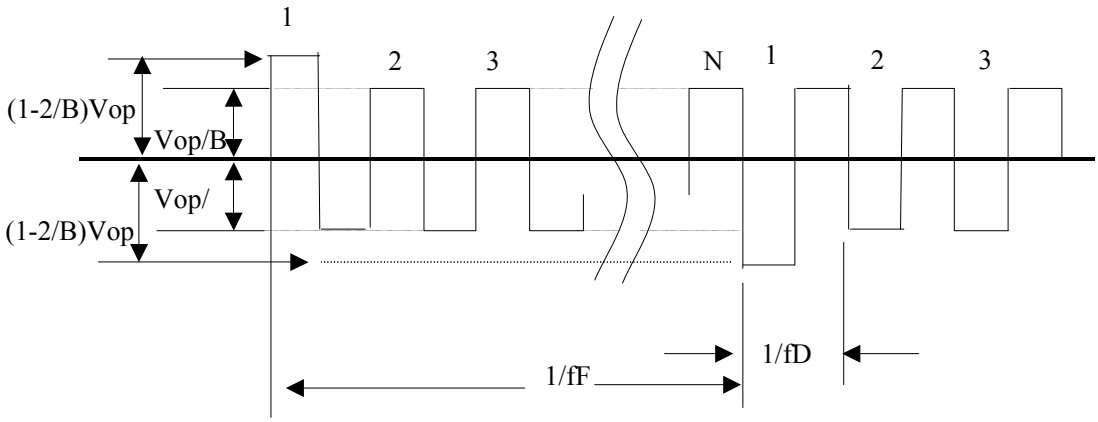
1/B: Bias　　fD: Drive frequency

N: Duty

(1) Selected waveform



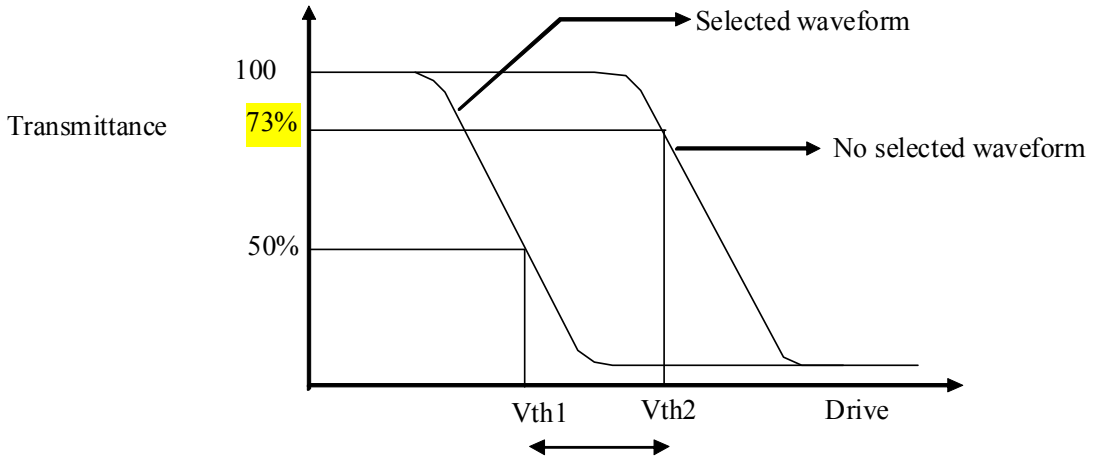
(2) Non- Selected wave form



Note:

Frame frequency is defined as follows: Common side supply voltage peak - to - peak /2 = 1 period

Note 3. : Definition of Vth

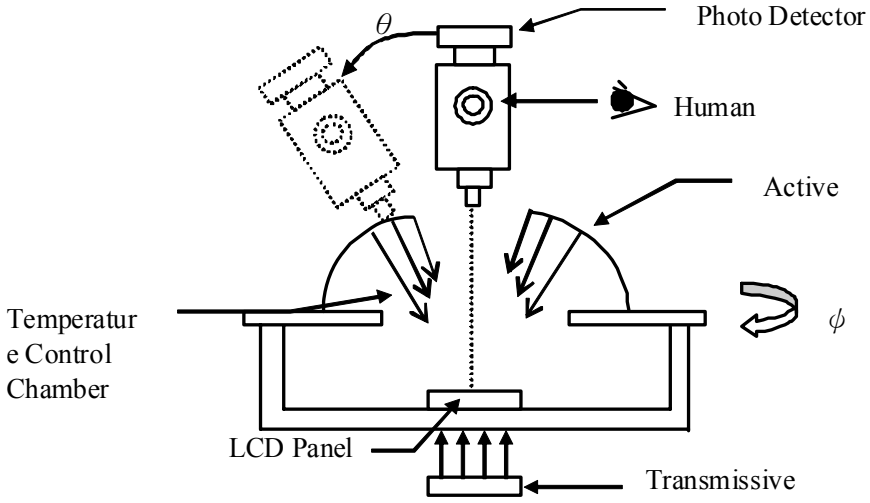


Active voltage range

	Vth1	Vth2
View direction	10 °	40 °
Drive waveform	(Selected waveform)	(No selected waveform)
Transmittance	50%	73%

※1 Contrast ratio
= (Brightness in OFF state) / (Brightness in ON state)

Outline of Electro-Optical Characteristics Measuring System



Measuring System: Autronic DMS-803

1.6 Backlight Characteristics

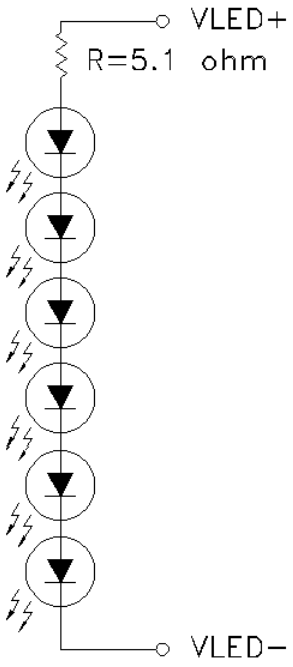
Maximum Ratings

Item	Symbol	Conditions	Min.	Max.	Unit	Remark
Reverse Voltage	IF	Ta=25℃	-	30	mA	Each LED
Forward Current	VR	Ta=25℃	-	5	V	Each LED
Power dissipation	PD	Ta=25℃	-	396	mW	-

Electrical / Optical Characteristics

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Forward Voltage	VF	IF= 20 mA	18	19	19.8	V
Average Brightness (Without LCD)	IV		7000	8000	-	cd/m^2
CIE Color Coordinate (Without LCD)	X		0.28	0.31	0.33	-
	Y		0.30	0.325	0.35	
Color	White					

Circuit diagram：



Other Description

Item	Conditions	Description
Life Time	Ta =25℃ IF= 20 mA	50000 hrs

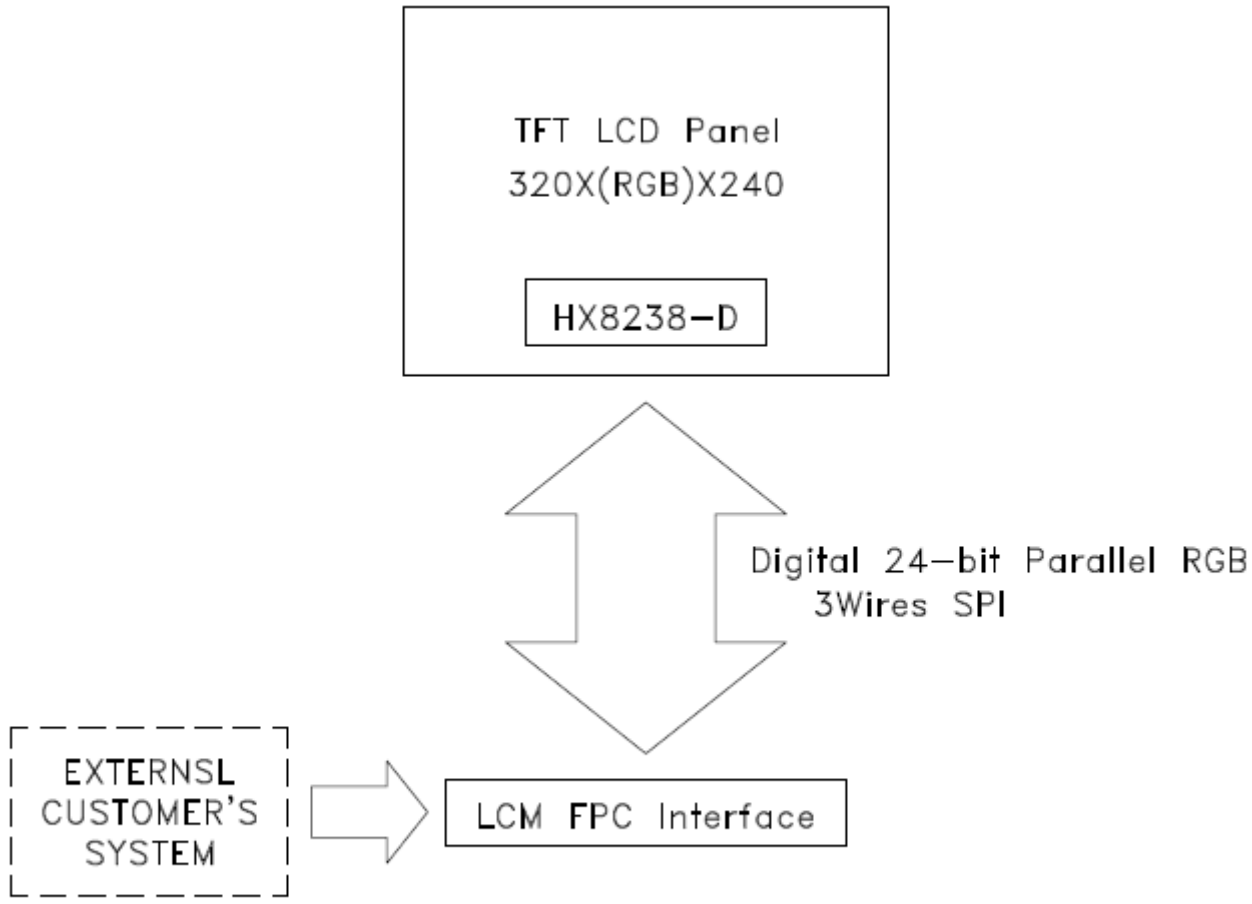
2. MODULE STRUCTURE

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

2.1.2 Block Diagram

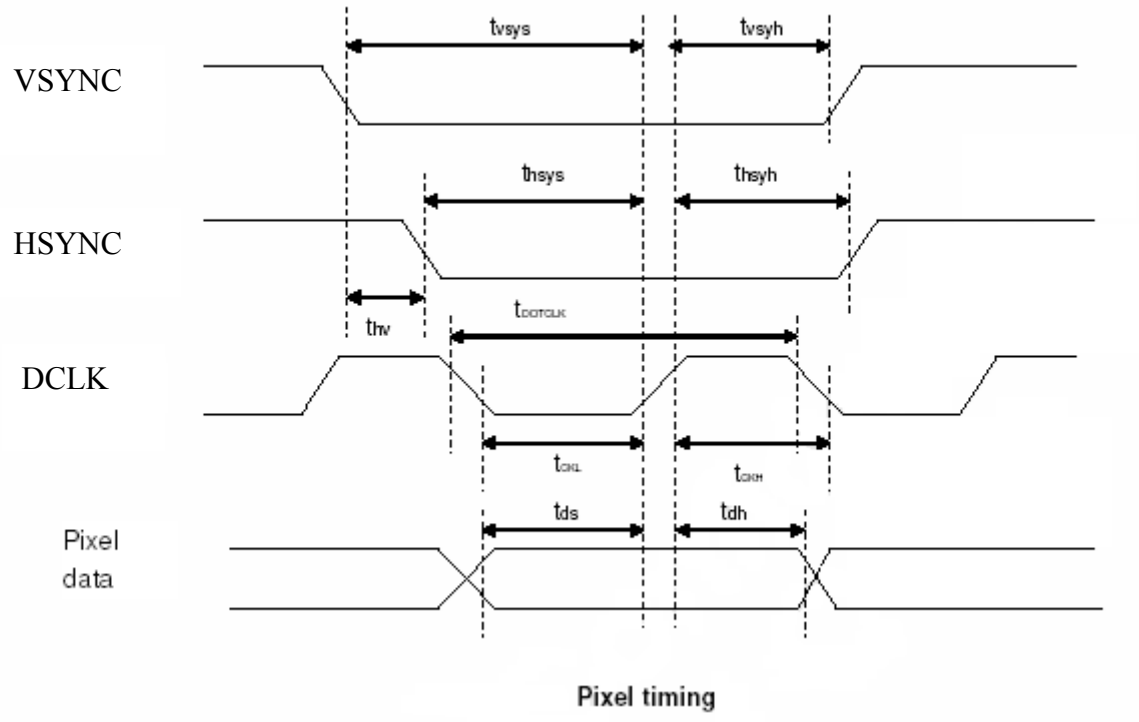


2.2 Interface Pin Description

Pin No.	Symbol	Function
1	V_{LED+}	Power For LED backlight (+).
2	V_{LED+}	Power For LED backlight (+).
3	V_{LED-}	Power For LED backlight (-).
4	V_{LED-}	Power For LED backlight (-).
5	GND	Power ground.
6	NC	No connection.
7	DV_{DD}	Power for Digital Circuit.
8	NC	No connection.
9	DEN	Data input Enable. Active High to enable the data input Bus under “DE Mode”.
10	VS	Vertical Sync input. Negative polarity.
11	HS	Horizontal Sync input. Negative polarity.
12	B7	Blue Data(MSB).
13	B6	Blue Data.
14	B5	Blue Data.
15	B4	Blue Data.
16	B3	Blue Data.
17	B2	Blue Data.
18	B1	Blue Data.
19	B0	Blue Data(LSB).
20	G7	Green Data(MSB).
21	G6	Green Data.
22	G5	Green Data.
23	G4	Green Data.
24	G3	Green Data.
25	G2	Green Data.
26	G1	Green Data.
27	G0	Green Data(LSB).
28	R7	Red Data(MSB).

Pin No.	Symbol	Function
29	R6	Red Data.
30	R5	Red Data.
31	R4	Red Data.
32	R3	Red Data.
33	R2	Red Data.
34	R1	Red Data.
35	R0	Red Data(LSB).
36	GND	Power Ground
37	DCLK	Clock signal. Latching data at the rising edge
38	GND	Power Ground.
39	NC	No connection.
40	NC	No connection.
41	NC	No connection.
42	NC	No connection.
43	NC	No connection.
44	RESETB	Active low global reset signal input.
45	CSB	Chip select pin of serial interface. Internal pull high.
46	NC	No connection.
47	NC	No connection.
48	GND	Power Ground.
49	SCK	Clock pin of serial interface. Internal pull high.
50	SDI	Data input pin in serial mode. Internal pull high.

2.3 Timing Characteristics

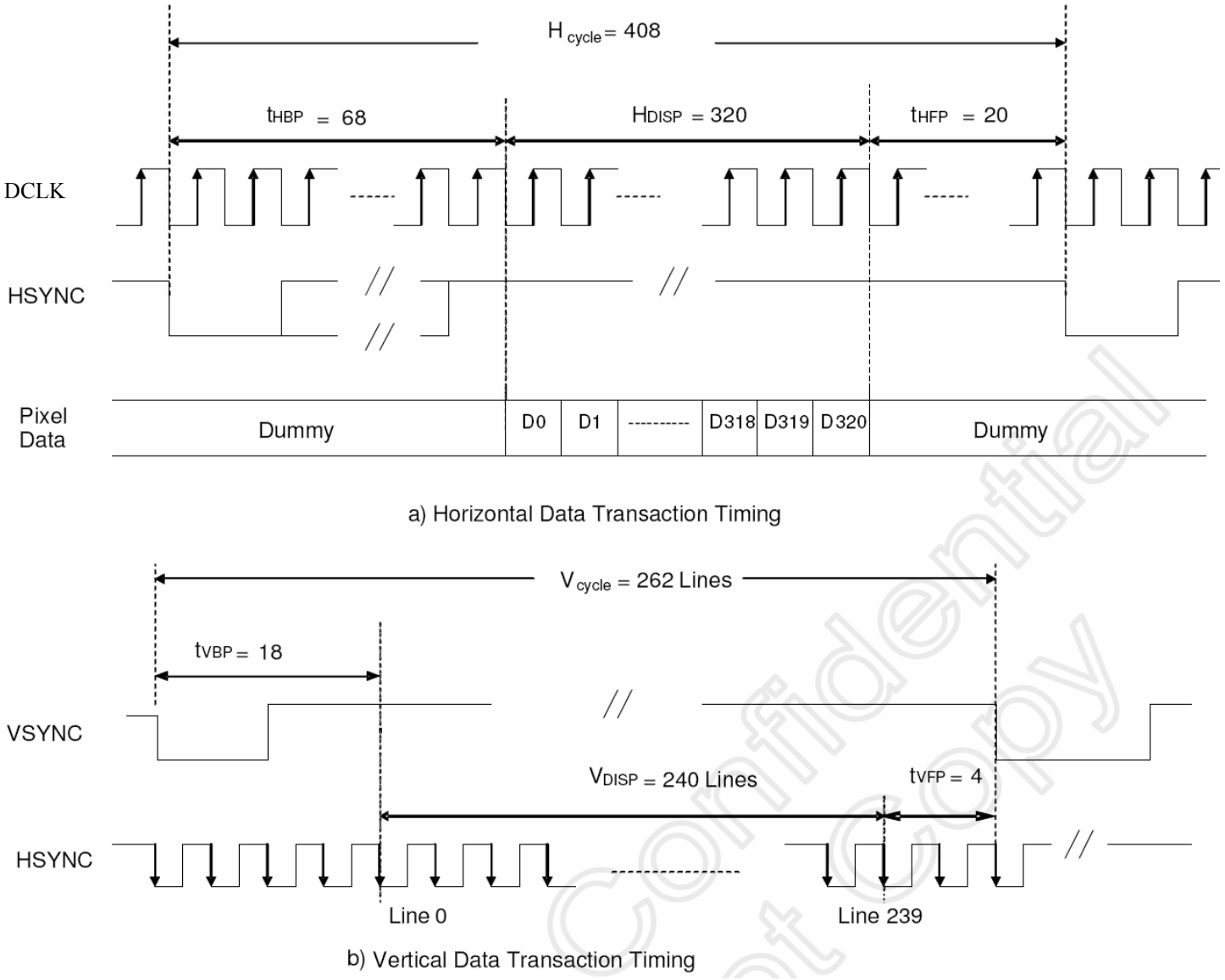


Pixel timing

Characteristics	Symbol	Min.		Typ.		Max.		Unit
		24-bit	8-bit	24- bit	8-bit	24-bit	8-bit	
DCLK Frequency	DCLK	-	-	6.5	19.5	10	30	MHz
DCLK Period	DCLK	100	33.3	154	51.3	-	-	ns
Vertical Sync Setup Time	tvsys	20	10	-	-	-	-	ns
Vertical Sync Hold Time	tvsyh	20	10	-	-	-	-	ns
Horizontal Sync Setup Time	thsys	20	10	-	-	-	-	ns
Horizontal Sync Hold Time	thsyh	20	10	-	-	-	-	ns
Phase difference of Sync Signal Falling Edge	thv	1		-		240		tDOTCLK
DCLK Low Period	tCKL	50	15	-	-	-	-	ns
DCLK High Period	tCKH	50	15	-	-	-	-	ns
Data Setup Time	tds	12	10	-	-	-	-	ns
Data hold Time	tdh	12	10	-	-	-	-	ns
Reset pulse width	tRES	10		-		-		μs

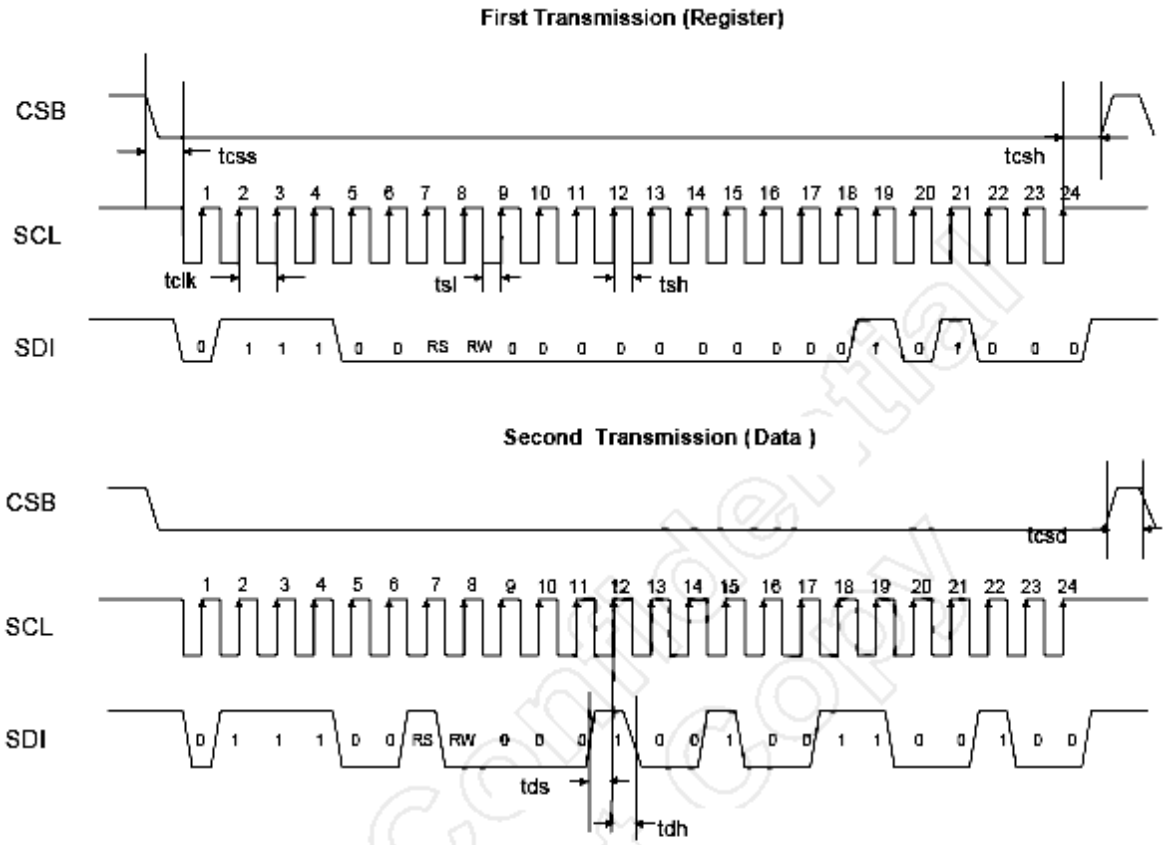
Note: External clock source must be provided to DCLK pin of HX8238-D. The driver will not operate if absent of the clocking signal.

Note：The interface of this module can drive by digital 24-bit data.



Data transaction timing in parallel RGB(24 bit)interface (SYNC mode)

Write SPI



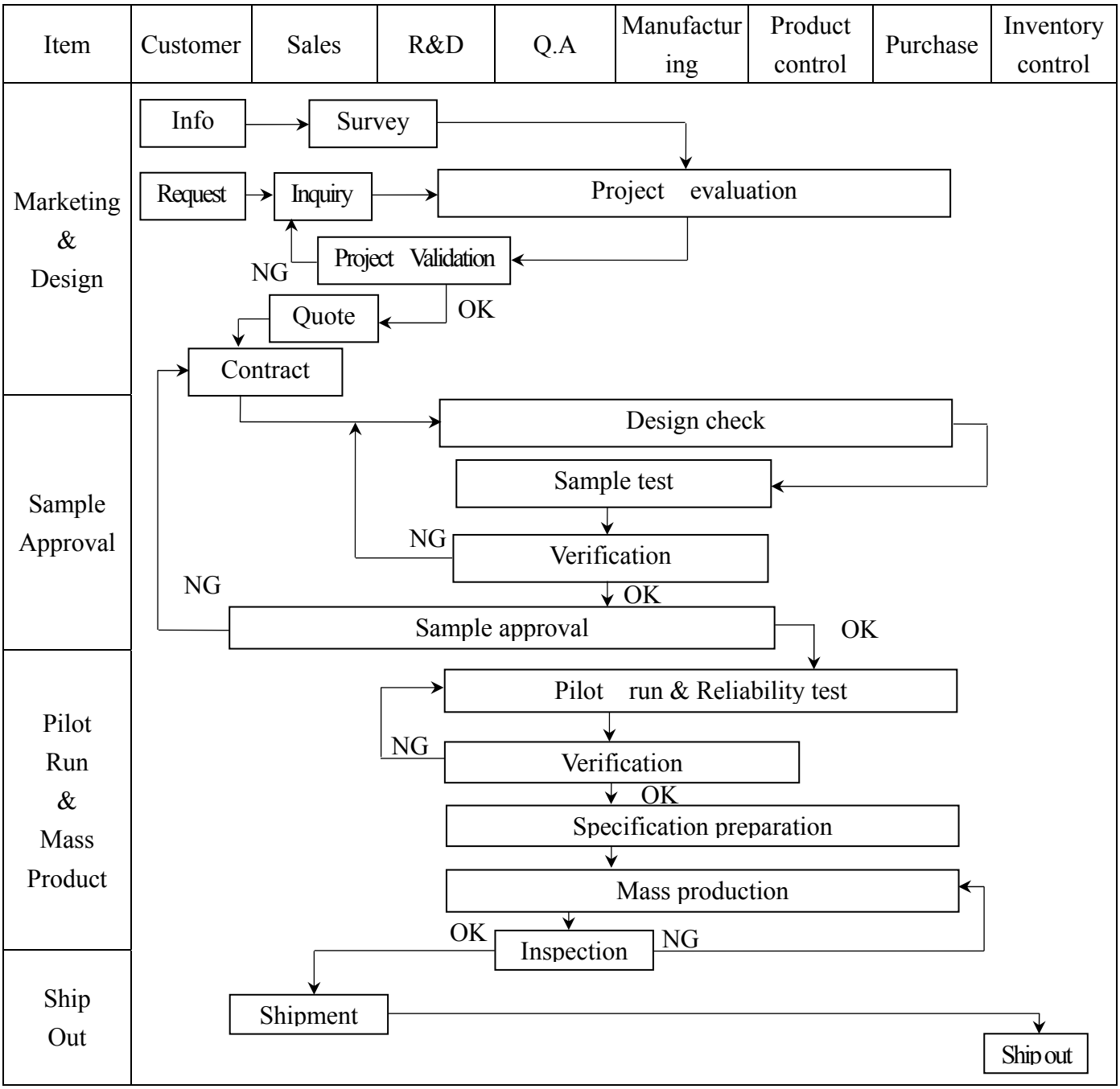
Note: The example writes "0x1264h" to register R28h.
SPID connected to VSS.

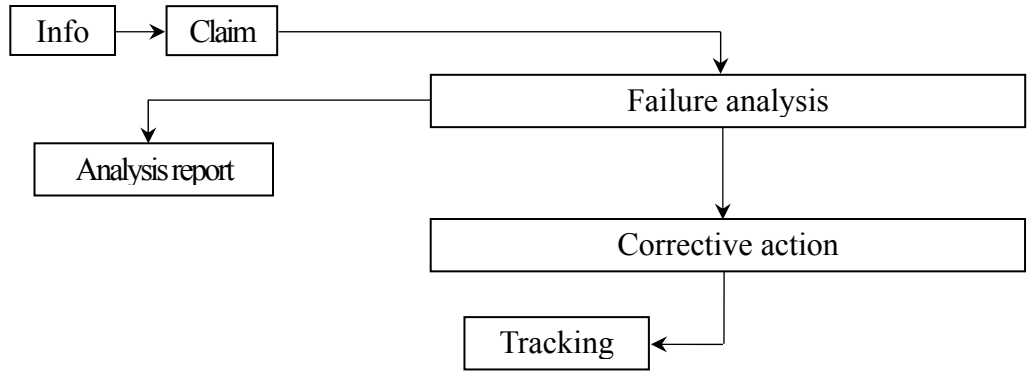
Figure 12. 14: (a) SPI interface timing diagram & write SPI example

Characteristics	Symbol	Spec.			Unit
		Min.	Typ.	Max.	
Serial Clock Frequency	fclk	-	-	20	MHz
Serial Clock Cycle Time	tclk	50	-	-	ns
Clock Low Width	tsl	25	-	-	ns
Clock High Width	tsh	25	-	-	ns
Clock Rising Time	trs	-	-	30	ns
Clock Falling Time	tfl	-	-	30	ns
Chip Select Hold Time	tcsh	10	-	-	ns
Chip Select High Delay Time	tcsd	20	-	-	ns
Data Setup Time	tds	5	-	-	ns
Data Hold Time	tdh	10	-	-	ns

3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart



Item	Customer	Sales	R&D	Q.A	Manufactu ring	Product control	Purchase	Inventory control
Sales Service								
Q.A Activity	1. ISO 9001 Maintenance Activities 2. Process improvement proposal 3. Equipment calibration 4. Education And Training Activities 5. Standardization Management							

3.2. Inspection Specification

◆Scope：The document shall be applied to TFT-LCD Module for 3.5″ ~15″ (Ver.B01).

◆Inspection Standard：MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.

◆Equipment：Gauge、MIL-STD、Powertip Tester、Sample

◆Defect Level：Major Defect AQL： 0.4 ； Minor Defect AQL： 1.5

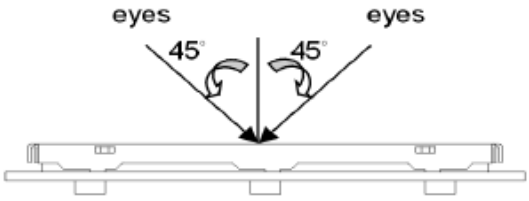
◆OUT Going Defect Level：Sampling.

◆Standard of the product appearance test：

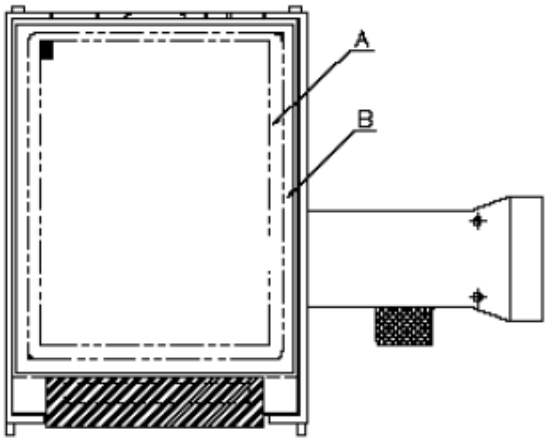
a. Manner of appearance test：

(1). The test best be under 20W×2 fluorescent light，and distance of view must be at 30 cm.

(2). The test direction is base on about around 45° of vertical line.



(3). Definition of area.



A area：viewing area

B area：Outside of viewing area

(4). Standard of inspection： (Unit：mm)

◆Specification For TFT-LCD Module 3.5″ ~15″ : (Ver.B01)

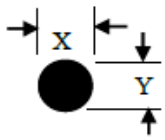
<table>
<tr><th>NO</th><th>Item</th><th>Criterion</th><th>Level</th></tr>
<tr><td rowspan="3">01</td><td rowspan="3">Product condition</td><td>1.1 The part number is inconsistent with work order of production.</td><td>Major</td></tr>
<tr><td>1.2 Mixed product types.</td><td>Major</td></tr>
<tr><td>1.3 Assembled in inverse direction.</td><td>Major</td></tr>
<tr><td>02</td><td>Quantity</td><td>2.1 The quantity is inconsistent with work order of production.</td><td>Major</td></tr>
<tr><td>03</td><td>Outline dimension</td><td>3.1 Product dimension and structure must conform to structure diagram.</td><td>Major</td></tr>
<tr><td rowspan="6">04</td><td rowspan="6">Electrical Testing</td><td>4.1 Missing line character and icon.</td><td>Major</td></tr>
<tr><td>4.2 No function or no display.</td><td>Major</td></tr>
<tr><td>4.3 Display malfunction.</td><td>Major</td></tr>
<tr><td>4.4 LCD viewing angle defect.</td><td>Major</td></tr>
<tr><td>4.5 Current consumption exceeds product specifications.</td><td>Major</td></tr>
<tr><td>4.6 Mura can not be seen through 5% ND filter.
(Mura : Under the normal examination angle of view,the picture has the non-uniform phenomenon.)</td><td>Minor</td></tr>
<tr><td>05</td><td>Dot defect

(Bright dot、Dark dot)

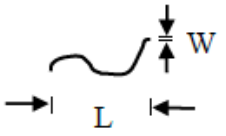
On -display</td><td>
<table>
<tr><th colspan="2">Item</th><th>Acceptance (Q'ty)</th></tr>
<tr><td rowspan="4">Dot Defect</td><td>Bright Dot</td><td>≦ 4</td></tr>
<tr><td>Dark Dot</td><td>≦ 5</td></tr>
<tr><td>Joint Dot</td><td>≦ 3</td></tr>
<tr><td>Total</td><td>≦ 7</td></tr>
</table>
5.1 Inspection pattern：full white , full black , Red , Green and blue screens.
5.2 It is defined as dot defect if defect area ＞1/2 dot.
5.3 The distance between two dot defect ≧5 mm.
5.4 Bright dot that can not be seen through 5% ND filter.
</td><td>Minor</td></tr>
</table>

◆Specification For TFT-LCD Module 3.5″ ~15″ : (Ver.B01)

<table>
<tr><th>NO</th><th>Item</th><th>Criterion</th><th>Level</th></tr>
<tr><td>06</td><td>Black or white dot、scratch、contamination

Round type
 

Φ=(x+y) / 2

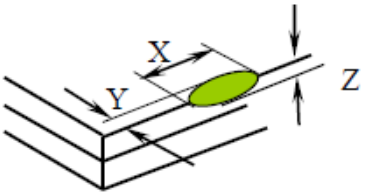
Line type
 
</td><td>6.1 Round type (Non-display or display) :
<table><tr><th rowspan="2">Dimension (diameter : Φ)</th><th colspan="2">Acceptance (Q'ty)</th></tr><tr><th>A area</th><th>B area</th></tr><tr><td>Φ ≦ 0.25</td><td>Ignore</td><td rowspan="4">Ignore</td></tr><tr><td>0.25 < Φ ≦ 0.50</td><td>5</td></tr><tr><td>Φ > 0.50</td><td>0</td></tr><tr><td>Total</td><td>5</td></tr></table>
6.2 Line type(Non-display or display) :
<table><tr><th rowspan="2">module size</th><th rowspan="2">Length (L)</th><th rowspan="2">Width (W)</th><th colspan="2">Acceptance (Q'ty)</th></tr><tr><th>A area</th><th>B area</th></tr><tr><td rowspan="5">3.5" to less 9"</td><td>---</td><td>W ≦ 0.03</td><td>Ignore</td><td rowspan="5">Ignore</td></tr><tr><td>L ≦10.0</td><td>0.03 <W ≦ 0.05</td><td>4</td></tr><tr><td>L ≦5.0</td><td>0.05 <W ≦ 0.10</td><td>2</td></tr><tr><td>---</td><td>W >0.10</td><td>As round type</td></tr><tr><td colspan="2">Total</td><td>5</td></tr><tr><td rowspan="5">9" to 15"</td><td>---</td><td>W ≦ 0.05</td><td>Ignore</td><td rowspan="5">Ignore</td></tr><tr><td>L ≦10.0</td><td>0.05 <W ≦ 0.10</td><td>5</td></tr><tr><td>---</td><td>W >0.10</td><td>As round type</td></tr><tr><td colspan="2">Total</td><td>5</td></tr></table></td><td>Minor</td></tr>
<tr><td>07</td><td>Polarizer Bubble</td><td><table><tr><th rowspan="2">Dimension (diameter : Φ)</th><th colspan="2">Acceptance (Q'ty)</th></tr><tr><th>A area</th><th>B area</th></tr><tr><td>Φ ≦ 0.25</td><td>Ignore</td><td rowspan="5">Ignore</td></tr><tr><td>0.25 < Φ ≦ 0.50</td><td>4</td></tr><tr><td>0.50 < Φ ≦ 0.80</td><td>1</td></tr><tr><td>Φ > 0.80</td><td>0</td></tr><tr><td>Total</td><td>5</td></tr></table></td><td>Minor</td></tr>
</table>

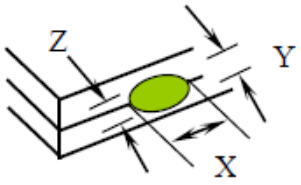
◆Specification For TFT-LCD Module 3.5″ ~15″ : (Ver.B01)

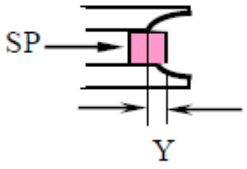
NO	Item	Criterion	Level
08	The crack of glass	Symbols : X : The length of crack Y : The width of crack. Z : The thickness of crack W : terminal length t : The thickness of glass a : LCD side length	Minor

8.1 General glass chip :

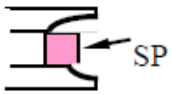
8.1.1 Chip on panel surface and crack between panels:



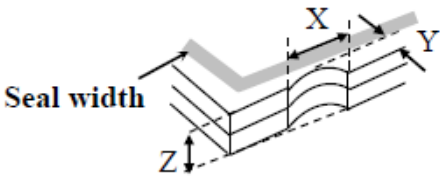




【OK】



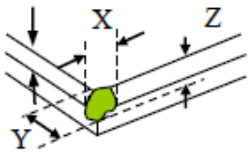
【NG】

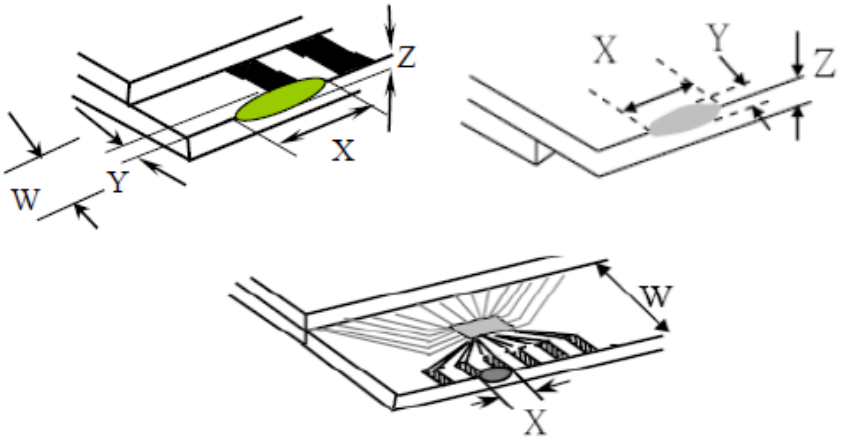


X	Y	Z
≦ a	Crack can't enter viewing area	≦1/2 t
≦ a	Crack can't exceed the half of SP width.	1/2 t < Z ≦2 t

◆Specification For TFT-LCD Module 3.5″ ~15″ : (Ver.B01)

<table>
<tr><th>NO</th><th>Item</th><th>Criterion</th><th>Level</th></tr>
<tr><td rowspan="2">08</td><td rowspan="2">The crack of glass</td><td>Symbols :

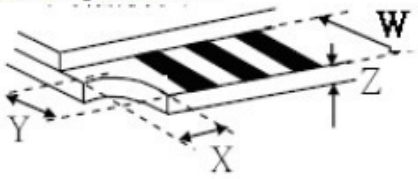
X : The length of crack
Z : The thickness of crack
t : The thickness of glass
Y : The width of crack.
W : terminal length
a : LCD side length</td><td rowspan="2">Minor</td></tr>
<tr><td>8.1.2 Corner crack :
 

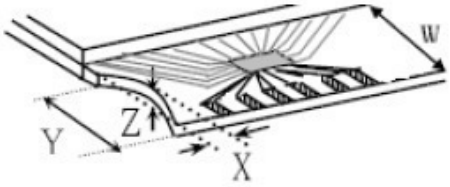
<table>
<tr><th>X</th><th>Y</th><th>Z</th></tr>
<tr><td>≦1/5 a</td><td>Crack can’t enter viewing area</td><td>Z ≦ 1/2 t</td></tr>
<tr><td>≦1/5 a</td><td>Crack can’t exceed the half of SP width.</td><td>1/2 t < Z ≦ 2 t</td></tr>
</table>
8.2 Protrusion over terminal :
8.2.1 Chip on electrode pad :
 

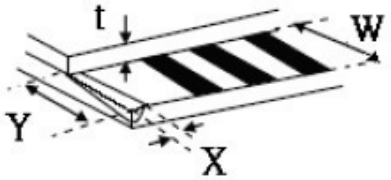
<table>
<tr><th></th><th>X</th><th>Y</th><th>Z</th></tr>
<tr><td>Front</td><td>≦ a</td><td>≦ 1/2 W</td><td>≦ t</td></tr>
<tr><td>Back</td><td>≦ a</td><td>≦ W</td><td>≦ 1/2 t</td></tr>
</table></td></tr>
</table>

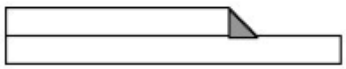
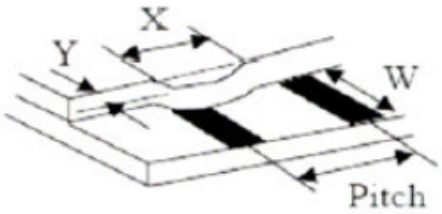
◆Specification For TFT-LCD Module 3.5″ ~15″ : (Ver.B01)

<table>
<tr><th>NO</th><th>Item</th><th>Criterion</th><th>Level</th></tr>
<tr><td rowspan="2">08</td><td rowspan="2">The crack of glass</td><td>Symbols :

X : The length of crack Y : The width of crack.
Z : The thickness of crack W : terminal length
t : The thickness of glass a : LCD side length</td><td rowspan="2">Minor</td></tr>
<tr><td>8.2.2 Non-conductive portion :
 

<table><tr><th>X</th><th>Y</th><th>Z</th></tr><tr><td>≦ 1/3 a</td><td>≦ W</td><td>≦t</td></tr></table>
⊙ If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications.
8.2.3 Glass remain :
 
 

<table><tr><th>X</th><th>Y</th><th>Z</th></tr><tr><td>≦ a</td><td>≦ 1/3 W</td><td>≦t</td></tr></table>
8.2.4 Cracking
 
Not Allowed</td></tr>
</table>

◆Specification For TFT-LCD Module 3.5″ ~15″ : (Ver.B01)

NO	Item	Criterion	Level
09	Backlight elements	9.1 Backlight can't work normally.	Major
		9.2 Backlight doesn't light or color is wrong.	Major
		9.3 Illumination source flickers when lit.	Major
10	General appearance	10.1 Pin type、quantity、dimension must match type in structure diagram.	Major
		10.2 No short circuits in components on PCB or FPC .	Major
		10.3 Parts on PCB or FPC must be the same as on the production characteristic chart .There should be no wrong parts , missing parts or excess parts.	Major
		10.4 Product packaging must the same as specified on packaging specification sheet.	Minor
		10.5 The folding and peeled off in polarizer are not acceptable.	Minor
		10.6 The PCB or FPC between B/L assembled distance(PCB or FPC) is ≦1.5 mm.	Minor

4. RELIABILITY TEST

4.1 Reliability Test Condition (Ver.B01)

<table>
<tr><th>NO.</th><th>TEST ITEM</th><th>TEST CONDITION</th></tr>
<tr><td>1</td><td>High Temperature Storage Test</td><td>Keep in +80 ±2℃ 240 hrs
Surrounding temperature, then storage at normal condition 4hrs.</td></tr>
<tr><td>2</td><td>Low Temperature Storage Test</td><td>Keep in -30 ±2℃ 240 hrs
Surrounding temperature, then storage at normal condition 4hrs.</td></tr>
<tr><td>3</td><td>High Temperature / High Humidity Storage Test</td><td>Keep in +60 ℃ / 90% R.H duration for 240 hrs
Surrounding temperature, then storage at normal condition 4hrs.
(Excluding the polarizer)</td></tr>
<tr><td>4</td><td>Temperature Cycling Storage Test</td><td>-30℃ → +25℃ → +80℃ → +25℃
(30mins) (5mins) (30mins) (5mins)
20 Cycle
Surrounding temperature, then storage at normal condition 4hrs.</td></tr>
<tr><td rowspan="2">5</td><td rowspan="2">ESD Test</td><td>Air Discharge:
Apply 2 KV with 5 times
Discharge for each polarity +/-
Contact Discharge:
Apply 250 V with 5 times
discharge for each polarity +/-</td></tr>
<tr><td>1. Temperature ambiance : 15℃～35℃
2. Humidity relative : 30%～60%
3. Energy Storage Capacitance(Cs+Cd) : 150pF±10%
4. Discharge Resistance(Rd) : 330Ω±10%
5. Discharge, mode of operation :
Single Discharge (time between successive discharges at least 1 sec)
(Tolerance if the output voltage indication : ±5%)</td></tr>
<tr><td>6</td><td>Vibration Test (Packaged)</td><td>1. Sine wave 10～55 Hz frequency (1 min/sweep)
2. The amplitude of vibration :1.5 mm
3. Each direction (X、Y、Z) duration for 2 Hrs</td></tr>
<tr><td>7</td><td>Drop Test (Packaged)</td><td><table><tr><th>Packing Weight (Kg)</th><th>Drop Height (cm)</th></tr><tr><td>0 ~ 45.4</td><td>122</td></tr><tr><td>45.4 ~ 90.8</td><td>76</td></tr><tr><td>90.8 ~ 454</td><td>61</td></tr><tr><td>Over 454</td><td>46</td></tr></table>Drop Direction :※1 corner / 3 edges / 6 sides each 1time</td></tr>
</table>

5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

5.1.1 If the LCD panel breaks , be careful not to get the liquid crystal to touch your skin.

5.1.2 If the liquid crystal touches your skin or clothes , please wash it off immediately by using soap and water.

5.2 HANDLING

5.2.1 Avoid any strong mechanical shock which can break the glass.

5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module , be sure to ground your body and any electrical equipment you may be using.

5.2.3 Do not remove the panel or frame from the module.

5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully ,do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)

5.2.5 Do not wipe the polarizing plate with a dry cloth , as it may easily scratch the surface of plate.

5.2.6 Do not touch the display area with bare hands , this will stain the display area.

5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.

5.2.8 To control temperature and time of soldering is 320±10℃and 3-5 sec.

5.2.9 To avoid liquid (include organic solvent) stained on LCM .

5.3 STORAGE

5.3.1 Store the panel or module in a dark place where the temperature is 25℃ ±5℃ and the humidity is below 65% RH.

5.3.2 Do not place the module near organics solvents or corrosive gases.

5.3.3 Do not crush , shake , or jolt the module.

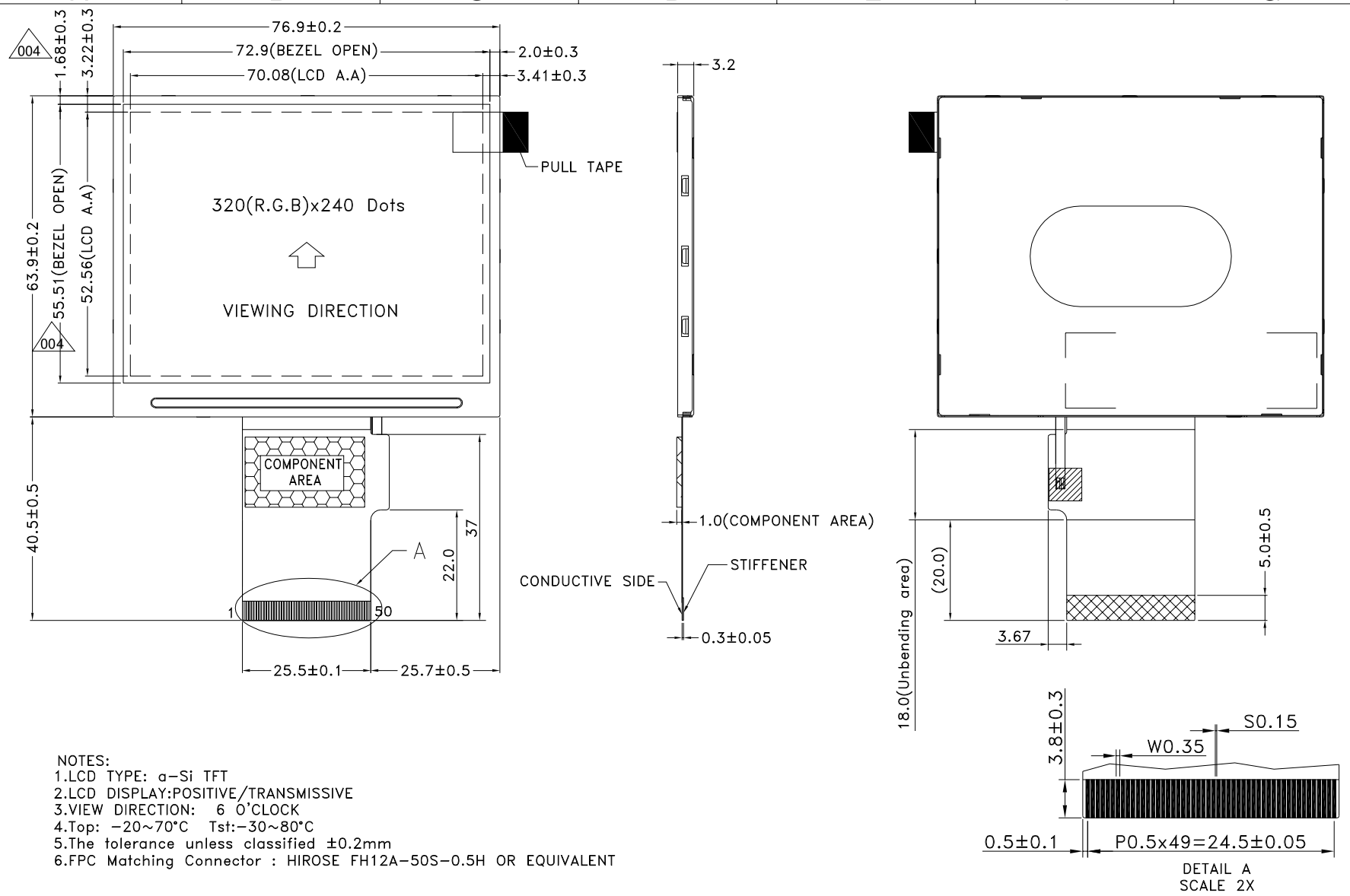
5.4 TERMS OF WARRANTY

5.4.1 Applicable warrant period

The period is within thirteen months since the date of shipping out under normal using and storage conditions.

5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment , we cannot take responsibility if the product is used in nuclear power control equipment , aerospace equipment , fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.



NOTES:
1.LCD TYPE: a-Si TFT
2.LCD DISPLAY:POSITIVE/TRANSMISSIVE
3.VIEW DIRECTION: 6 O'CLOCK
4.Top: -20~70°C Tst:-30~80°C
5.The tolerance unless classified ±0.2mm
6.FPC Matching Connector : HIROSE FH12A-50S-0.5H OR EQUIVALENT

REV	REV BY	REVISER	DATE
007			
006			
005			
004	UPDATE DIM.	Air	2020/03/19
003	MODIFY TAPE	Air	2016/06/13
002	MODIFY FPC	Air	2016/04/27
001	NEW DRAWING	Air	2016/04/12

PART NO: PH320240T023-IHA01

DRAWING NAME : JLMD-PH320240T023-IHA01

TITLE: LCD MODULE DRAWING

久正光電股份有限公司
POWERTIP TECHNOLOGY CORPORATION

Design	Air	(3)	Surface		
Check	Terry	Unit	MM	Material	
		Scale	FIT	Thickness	
Approve	Terry	Page	1/2	Quantity	

Length(Min) (mm) \ Tolerance \ Precision	Precision Level
1 ~ 4	-
4 ~ 16	-
16 ~ 63	-
63 ~ 250	-
250 ~ 1000	-

PT-A-054-01

Ver.001

Documents NO.	JPKG-PH320240T023-IHA01

LCM包裝規格書
LCM Packaging Specifications
(For Tray)

Approve	Check	Contact
Ryan	Terry	Air

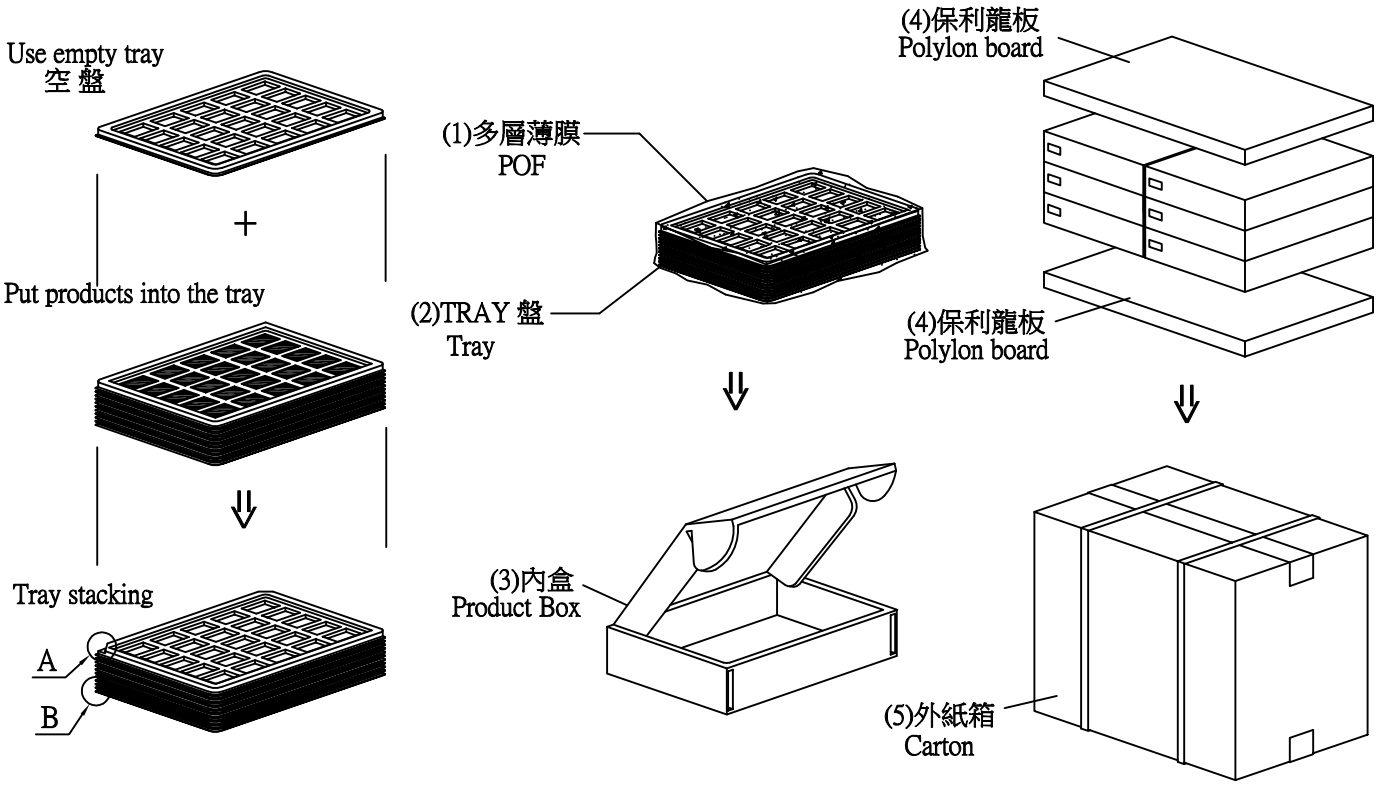
1.包裝材料規格表 (Packaging Material) : (per carton)

No.	Item	Model	Dimensions (mm)	1Pcs Weight	Quantity	Total Weight
1	成品 (LCM)	PH320240T023-IHA01	76.9 X 63.9X3.2	0.03	288	8.64
2	多層薄膜(1)POF	OTFILM0BA03ABA	19"X350X0.015	——	6	——
3	TRAY 盤 (2)Tray	TY32024001TZBA	352 X 260 X 10.8	0.1	54	5.4
4	內盒(3)Product Box	BX36627063ABBA	383 X 270 X 66	0.182	6	1.092
5	保利龍板(4)Polylon board	OTPLB00PL08ABA	550 X 393 X 20	0.0284	2	0.0568
6	外紙箱(5)Carton	BX57041027CCBA	570 X 410 X 265	1.0	1	1.0
7						
8						
9						

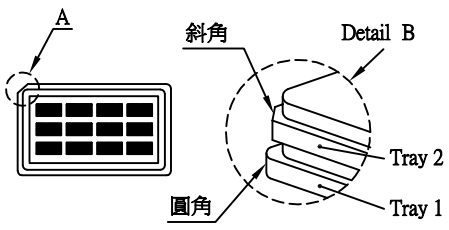
2.一 整箱總重量 (Total LCD Weight in carton) : 16.19 Kg±10%

3.單箱數量規格表 (Packaging Specifications and Quantity) :

(1)LCM quantity per box : no per tray	6	x no of tray	8	=	48
(2)Total LCM quantity in carton : quantity per box	48	x no of boxes	6	=	288



特 記 事 項 (REMARK)



4.TRAY盤相疊時,需旋轉180度,請詳見B視圖
Rotate tray 180 degrees and place on top of stack.
Check the tray stack using Fig. B.

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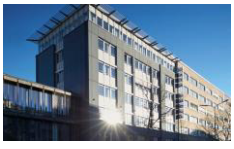
ALL TECHNOLOGIES. ALL COMPETENCIES. ONE SPECIALIST.



DATA MODUL AG

Landsberger Straße 322
DE-80687 Munich
Phone: +49-89-56017-0

DATA MODUL WEIKERSHEIM GMBH

Lindenstraße 8
DE-97990 Weikersheim
Phone: +49-7934-101-0

More information and worldwide locations can be found at

www.data-modul.com