



# **SPECIFICATION**



# PH480272T009\_IHB

4,3"-WQVGA-RGB

Version: 01.045 Date: 20.12.2019

Note: This specification is subject to change without prior notice

SPI	FCI	FIC	ΔΤΙ	NS
JEI	_0	$\mathbf{I}$	$\sim$ 1 $^{\circ}$	

CUSTOMER · CDE012

SAMPLE CODE . SH480272T009-IHB

MASS PRODUCTION CODE . PH480272T009-IHB

SAMPLE VERSION . 01

SPECIFICATIONS EDITION . 004

DRAWING NO. (Ver.) . JLMD- PH480272T009-IHB\_003

PACKAGING NO. (Ver.) . JPKG- PH480272T009-IHB\_001

# **Customer Approved**

Date:

Approved	Checked	Designer
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- ☐ Preliminary specification for design input
- Specification for sample approval

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# **History of Version**

Date (mm / dd / yyyy)	Ver.	Edi.	Description	Page	Design by
08/29/2016	01	001	New Drawing	-	徐明菲
09/09/2016	01	002	Modify Specs.(Drawing)		徐明菲
12/02/2016	01	003	New Sample	-	徐明菲
12/20/2019	01	004	Add Chromaticity coordinates specifications	9	陳璐
				<b>&gt;</b> /	

Total: 33 Page



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Note: For detailed information please refer to IC data sheet:ILITEK--- ILI6480B



# 1. SPECIFICATIONS

# 1.1 Features

Item	Standard Value
Display Resolution	480 *3 (RGB) * 272 Dots
LCD Type	a-Si TFT , Normally white , Transmissive type
Screen size(inch)	4.3 inch
Viewing Direction	6 O'clock
Surface treatment	Anti-Glare
Color configuration	R.G.B. Vertical Stripe
Weight	56.2 g
Interface	24 Bits RGB Interface
Driver IC	ILI6480B
	THIS PRODUCT CONFORMS THE ROHS OF PTC
ROHS	Detail information please refer website :
	http://www.powertip.com.tw/news.php?area_id_view=1085560481/

# 1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	105.5 (W) * 67.2 (L) * 3.85 (H)	mm

# LCD panel

Item	Standard Value	Unit
Active Area	95.040 (W) * 53.856 (L)	mm

Note: For detailed information please refer to LCM drawing.



# 1.3 Absolute Maximum Ratings

### Module

Item	Symbol	Condition	Min.	Max.	Unit
Power Supply for TFT Panel	$V_{DD}$	GND=0V	-0.3	4.5	V
Power Supply for Backlight Unit	Vcc	GND=0V	-0.3	+20.0	V
Operating Temperature	Тор	-	-20	+70	°C
Storage Temperature	T <sub>ST</sub>	-	-30	+80	°C

The absolute maximum rating values of this product are not allowed to be exceeded at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

# 1.4 DC Electrical Characteristics

Module GND = 0V, Ta =  $25^{\circ}C$ 

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Power Supply for TFT Panel	V <sub>DD</sub>	GND=0V	3.0	3.3	3.6	V
Power Supply for Backlight Unit	Vcc	GND=0V	5	12	15	V
Input Voltage for TFT	VIH	GND=0V	0.7V <sub>DD</sub>	ı	$V_{DD}$	V
Panel	VIL	GND=0V	0	-	0.3V <sub>DD</sub>	V
Supply Current for TFT Panel	I <sub>DD</sub>	I <sub>DD</sub> @V <sub>DD</sub> =3.3V	-	23	40	mA
Supply Current for Backlight Unit	lcc	Icc@Vcc=5V	-	180	300	mA
Supply Current for Backlight Unit	lcc	Icc@Vcc=12V	-	70	120	mA
Input Voltage for	V <sub>PH</sub>	GND=0V	1.2	-	-	V
PWM Signal	V <sub>PL</sub>	GND=0V	-	-	0.4	V
Dimming Clock Rate	fP	GND=0V	5	-	100	KHz



# 1.5 Optical Characteristics

VDD=3.3V, Ta=25°C

Item		Symbol	Condition	Min.	Тур.	Max.	unit	
Response tim	ne	Tr + Tf	-	-	29	44	ms	Note2
	Тор	θ+		-	60	-		
Viouing angle	Bottom	θ-	CR ≥ 10	-	60	-	Dog	Note 4
Viewing angle	Left	θL	CR 2 10	-	60	-	Deg.	Note4
	Right	θR		-	60	-		
Contrast ratio	0	CR	-	500	600	-	-	Note3
	White	Х		0.26	0.31	0.36		
	vvnite	Υ		0.28	0.33	0.38		
	Red	Х	X	0.52	0.57	0.62		
Color of CIE Coordinate	Reu	Y	VCC=12V	0.28	0.33	0.38		
( LCD & BL & TP )	Green	Х	PWM="High" (Duty=100%)	0.29	0.34	0.39	_	
,	Green	Υ	(Buty 10070)	0.56	0.61	0.66		
	Blue	X		0.09	0.15	0.19		Note1
	Diue	Υ		0.02	0.07	0.12		
Average Brightr	ness							
Pattern=white dis	splay	IV	VCC=12V	620	780	-	cd/m <sup>2</sup>	
( LCD & BL & TF	P)*1		PWM="High"					
Uniformity ( LCD & BL & TF	P)*2	△B	(Duty=100%)	70	-	-	%	



### Note 1:

\*1 : △B=B(min) / B(max) \* 100%

\*2 : Measurement Condition for Optical Characteristics:

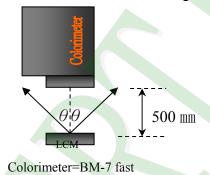
a : Environment: 25°C±5°C / 60±20%R.H , no wind , dark room below 10 Lux at typical lamp current and typical operating frequency.

b : Measurement Distance: 500  $\pm$  50 mm  $\rightarrow$  ( $\theta$ = 0°)

c: Equipment: TOPCON BM-7 fast, (field 1°), after 10 minutes operation.

d: The uncertainty of the C.I.E coordinate measurement ±0.01, Average Brightness ± 4%





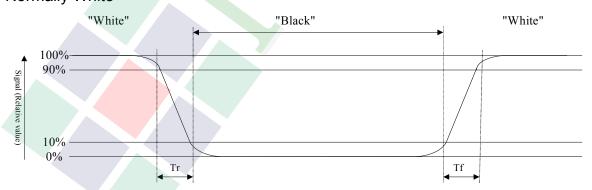
To be measured at the center area of panel with a viewing cone of 1° by Topcon luminance meter BM-7, after 10 minutes operation (module)

### Note2: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

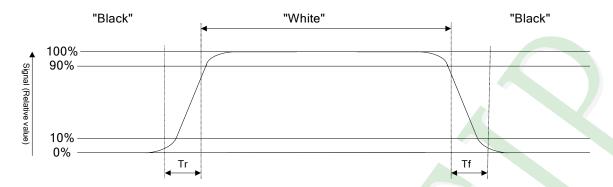
# Refer to figure as below:

### Normally White





# Normally Black



Note3: Definition of contrast ratio:

Contrast ratio is calculated with the following formula

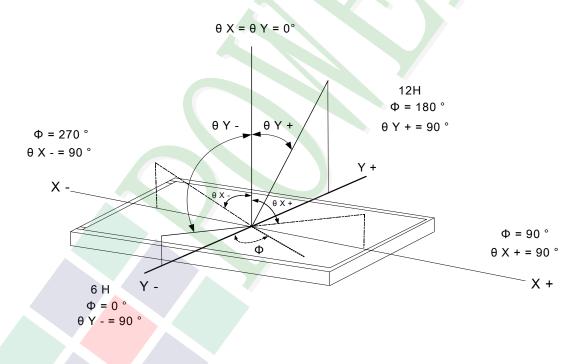
Photo detector output when LCD is at "White" state

Contrast ratio (CR) =

Photo detector output when LCD is at "Black" state

Note4: Definition of viewing angle:

Refer to figure as below:





# 1.6 Backlight Characteristics

**Maximum Ratings** 

Item	Symbol	Min.	Max.	Unit	Remark	
LED Forward Current	lF	30		mA	One LED	
LED Reverse Voltage	VR	5		V	One LED	

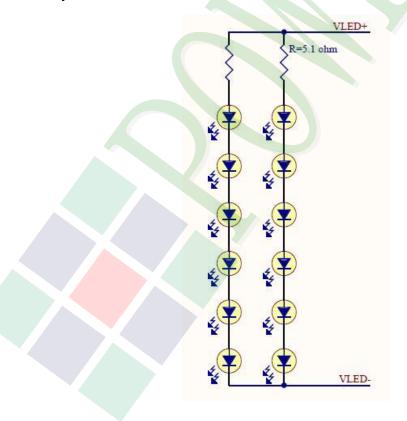
**Electrical / Optical Characteristics** 

Item	Symbol	Min.	Тур.	Max.	Unit	Remark
LED Voltage	VL	17.6	19.2	20.4	V	Note1
LED Current	lι	-	40	-	mA	_
LED life time	-	50000		-	HR	Note2

Note 1: The LED Supply Voltage is defined by the number of LED at Ta=25°C and I∟=40 mA.

Note 2: The "LED life time" is defined as the module brightness decrease to 50% original brightness at Ta=25°C and I<sub>L</sub> =40 mA. The LED life time could be decreased if operating I<sub>L</sub> is larger than 40 mA.

3:Chromaticity coordinates are E&G





# 1.7 Touch Panel Characteristics

# 1.7.1 Optical Characteristics

Item	Specification	
1.Transparency	80% Min	

### 1.7.2 Mechanical Characteristics

Item	Specification
1.Input Method	Finger or stylus pen
2.Hardness of surface	3H -pressure 500g of ,45deg.
3.Activation Force	50gf (TYP. 20gf) less individual point with stylus pen(R0.8)
	Activation force guarantee area:5.0mm inside of Active Area.
4.Linearity Force	100gf less input with stylus pen(R0.8)
	Linearity force guarantee area:3.0mm inside of Active Area.

### 1.7.3 Electrical Characteristics

Item	Specification			
1.Rated Voltage	DC 5V(DC 7V Max)			
2.Resistance Between	Direction X (Glass side): 350Ω~1240Ω			
Terminals.	Direction Y (Film side): 160Ω~640Ω			
3.Insulation Resistance	20 MΩ or more (DC 25V 1min)			
4.Linearity	≤± 1.5%			
	Linearity(%)= ΔV/ (EV-SV) *100			
	ΔV: The difference between the ideal voltage and measured			
	voltage on the each measuring line.			
	SV: Voltage of starting Points			
	EV: Voltage of Ending Points			
5.Bouncing	<10ms (Tip R 3.75mm, hardness 10°~20°, silicon rubber ,500gf			
	operation : 40 mm/sec )			



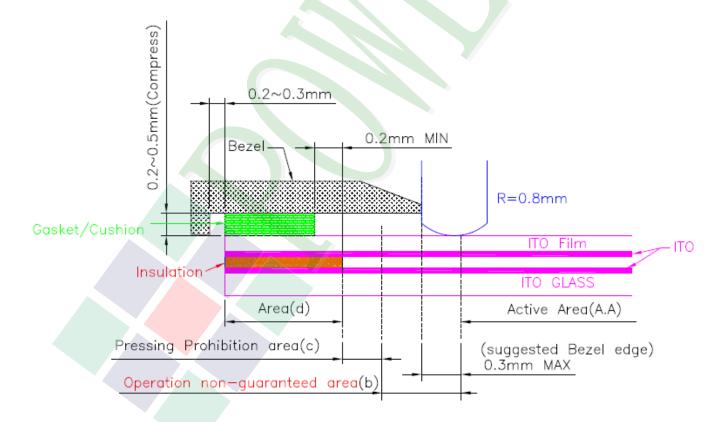
# 1.7.4 Reliability Characteristic

NO	Test Item	Test Condition	Test Result
	Hitting Durability	Hitting Durability 1,000,000times min.(R 8 mm	
1		Silicon Rubber Hardness	item4
		60°250gf 2times/sec).	
2	Pen Sliding Durability	100,000 times min(Tip	Follow 1.7.3 item2 and
		R0.8mm).	item4.
		ψ9mm steel ball is dropped on	No Crack
3	Impact Resistance	the surface from 30 cm height	
		at 1 time.	
4	Flexible pattern Bending	Bending 3 times by bending	Follow 1.7.3 item2.
4	Resistance	radius R1.0 mm	



### 1.7.5 Touch Panel Design/Handing Guide

- (1) Keep the gap, for example 0.2 to 0.3mm, between bezel edge and T/P edge.
- The reason is to avoid the bezel edge from contacting T/P surface that may cause "short" with bottom layer
- (2) Insertion a cushion material is recommended.
- (3) The cushion material should be limited on the busbar insulation paste area. If it is over the transparent insulation paste area, a "short" may be occurred.
- (4) Do not to use an adhesive tape to bond it on the front of T/P and hang it to the housing bezel.
- (5) Never expand the T/P top layer (PET Film) like a balloon by internal air pressure. The life of the T/P will extremely decreasing.
- (6) Top layer, PET, dimension is changing base on environment temperature and humidity. Please avoid a stress from housing bezel to top layer, because it may cause "waving".
- (7) The input to the Touch Panel sometimes distorts touch panel itself.
- (8)To use the stylus pen or fingernail sliding at the edge of the housing is prohibited. It would cause the cracking of the ITO coating and damage the touch panel. It also request not to press this area while assembling
- (9) Purpose: In order to prevent accidental use and performance deterioration, please keep the following precautions.



In order to prevent unusual performance degradation and malfunction of a touch panel, please carry out the set case designing and a touch panel assembling method after surely considering the definition of each area illustrated in above figure.



### Area(a): Active area

The active area is guaranteed the position data detectable precision, operation force and other operations. it is strongly recommended to place the operation button or menu keys within the active area. Due to structure, the active area is less durable at the edge or close to the edge.

### Area(b): Operation non-guaranteed area

This area does not guarantee a touch panel operation and its function. When this area is pressed, touch panel shows degradation of its performance and durability such as a pen sliding durability becomes about one-tenth compared with the active area (area-(a) as guaranteed area) and its operation force requires about double. About 0.5 mm outside from a boundary of the active area corresponds to this area.

### Area(c): Pressing prohibition area

The area which forbids pressing, because an excessive load is applied to a transparent electrode (ITO) and a serious damage is given to a touch panel function by pressing. About 0.5 mm outside from Operation non-guaranteed area.

Area(d): Non-Active area

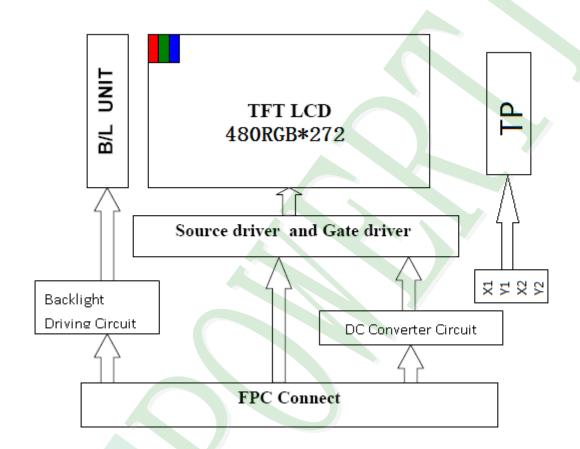
The area does not activate even if pressed.



# 2. MODULE STRUCTURE

# 2.1 Counter Drawing

- 2.1.1 LCM Mechanical Diagram
  - \* See Appendix
- 2.1.2 Block Diagram





# 2.2 Interface Pin Description

# **TFT LCM Interface**

Pin#	Name	DESCRIPTION
1	GND	Power ground.
2	VDD	Power for Digital Circuit.
3	VDD	Power for Digital Circuit.
4	VCC	Power For LED backlight.
5	VCC	Power For LED backlight.
6	PWM	Shutdown & Dimming control input for backlight. Do not allow this pin to float. "Hi" =100%, "Low" = 0%.
7	GND	Power ground.
8	R0	Red Data.
9	R1	Red Data.
10	R2	Red Data.
11	R3	Red Data.
12	GND	Power ground.
13	R4	Red Data.
14	R5	Red Data.
15	R6	Red Data.
16	R7	Red Data.
17	GND	Power ground.
18	G0	Green Data.
19	G1	Green Data.
20	G2	Green Data.
21	G3	Green Data.
22	GND	Power ground.
23	G4	Green Data.
24	G5	Green Data.
25	G6	Green Data.
26	G7	Green Data.
27	GND	Power ground.
28	В0	Blue Data.
29	B1	Blue Data.



Pin#	Name	DESCRIPTION
30	B2	Blue Data.
31	В3	Blue Data.
32	GND	Power ground.
33	B4	Blue Data.
34	B5	Blue Data.
35	B6	Blue Data.
36	B7	Blue Data.
37	GND	Power ground.
38	HS	Line synchronization signal. Horizontal Sync Input.
39	VS	Frame synchronization signal. Vertical Sync Input.
40	GND	Power ground.
41	DE	Display enable pin from controller. Data Input Enable.
42	GND	Power ground.
43	DCLK	Sample clock. Data will be latched at the falling edge of DCLK.
44	GND	Power ground.
45	CS(NC) / ID1	No Function./ ID[4:1]These pins select LCM type.
46	SDIN(NC) / ID2	No Function./ ID[4:1]These pins select LCM type.
47	SCK(NC) / ID3	No Function ./ ID[4:1]These pins select LCM type.
48	DISPLAY CONTROL / ID4	Display Enable(Hi Active)./ ID[4:1]These pins select LCM type.
49	/RESET	Global Reset (Low Active).
50	GND	Power ground.

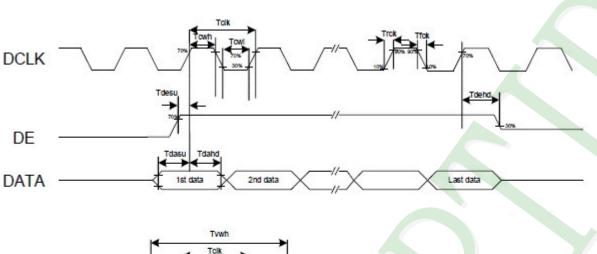
# 4-Wire Resistive Touch Screen (RTP) Interface

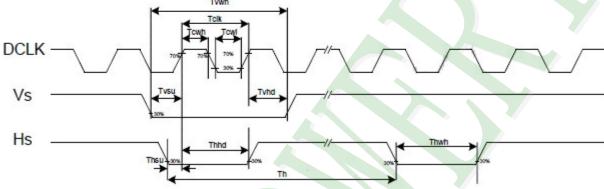
Pin No.	Symbol	Function
1	XR	TP:X right
2	YD	TP:Y bottom
3	XL	TP:X left
4	YU	TP:Y top



# 2.3 Timing Characteristics

# 2.3.1 Clock and Data Input Waveforms





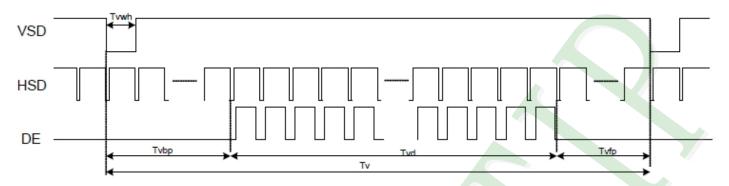


Parameters	Symbol	Min.	Тур.	Max.	Unit	Conditions
System operation timing						
VDD power source slew time	TPOR	-	-	20	ms	From 0V to 99% VDD
GRB pulse width	tRSTW	10	50	-	us	R=10Kohm, C=1uF
Input Output timing						
DCLK clock time	Tclk	33.3	-	-	ns	DCLK=30MHz
DCLK clock low period	Tcwl	40	-	60	%	
DCLK clock high period	Tcwh	40	-	60	%	
Clock rising time	Trck	9	-	-	ns	-
Clock falling time	Tfck	9	-	-	ns	
HSD width	Thwh	1	-	-/ (	DCLK	1
HSD period time	Th	55	60	65	us	7/
HSD setup time	Thsu	12	-	-	ns	
HSD hold time	Thhd	12	- /	-	ns	
VSD width	Tvwh	1	-	-	Th	
VSD setup time	Tvsu	12	-	-	ns	
VSD hold time	Tvhd	12	75-	-	ns	
Data setup time	Tdasu	12	- 3	-	ns	
Data hold time	Tdahd	12	-		ns	
DE setup time	Tdesu	12	-	-	ns	
DE hold time	Tdehd	12	-	-	ns	
Source output setting time	Tsst			TBD	us	10% to 90% CL=60pF, RL=2Kohm
Gate output setting time	Tgst	-	-	TBD	ns	10% to 90%, CL=60pF
VCOM output setting time	Tcst	-	-	TBD	us	10% to 90%, CL=40nF, RL=50ohm
Time from VSD to 1st line data input	Tvs	3	8	31	Th	HV mode By HDL[4:0] setting

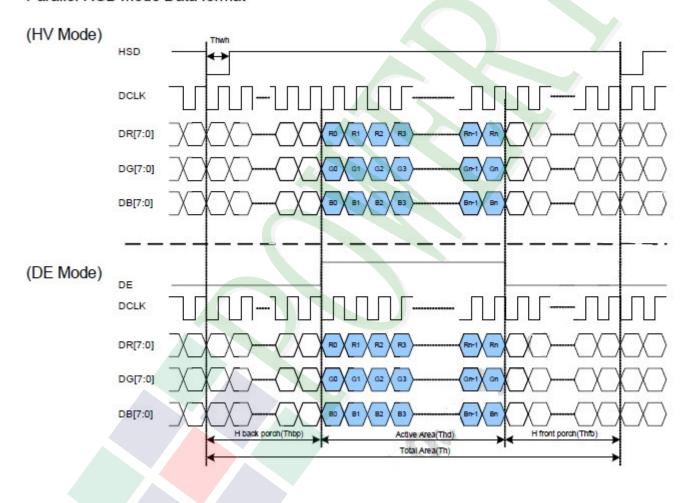


### 2.3.2 Data Input Format

### Vertical input timing



# Parallel RGB Mode Data format



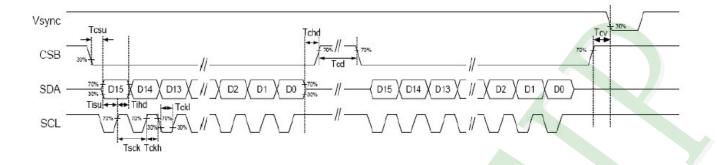


# Parallel RGB input timign table

Parameters	Symbol		Value		
		Min.	Тур.	Max.	
DCLK frequency	Fclk	5	9	12	MHz
VS period time	Tv	277	288	400	Н
VS display area	Tvd		272		Н
VS back porch	Tvb	3	8	31	Н
VS front porch	Tvfp	2	8	97	Н
HS period time	Th	520	525	800	DCLK
HS display area	Thd		480		DCLK
HS back porch	Thbp	36	40	255	DCLK
HS front porch	Thfp	4	5	65	DCLK



# 2.3.3 3-wire Timing Diagram

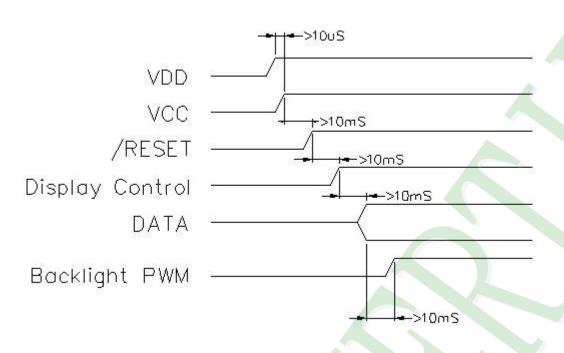


3-wire serial communication AC timing								
Serial clock	Tsck	200	-	- /	ns	For SCL Pin		
SCL pulse low period	Tckl	40	-	60	%			
SCL pulse high period	Tckh	40		60	%			
Serial data setup time	Tisu	50	-	-	ns			
Serial data hold time	Tihd	50	-	-	ns			
Serial clock high/low	Tssw	50	-	-	ns			
CS to VSD	Tcv	1			us			
CS distinguish time	Tcd	400	-	-	ns			
CS input setup time	Tcsu	50	-	_	ns			
CS input hold time	Tchd	50	-	-	ns			

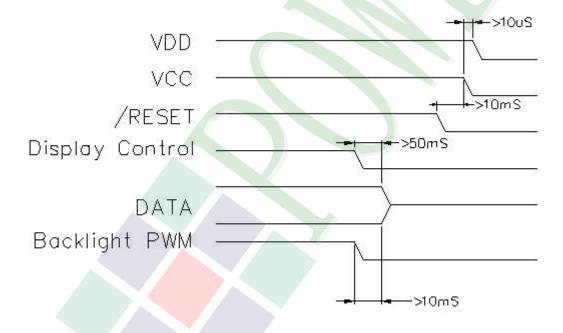


### 2.3.4 Power Sequence

### **POWER ON**



### **POWER OFF**

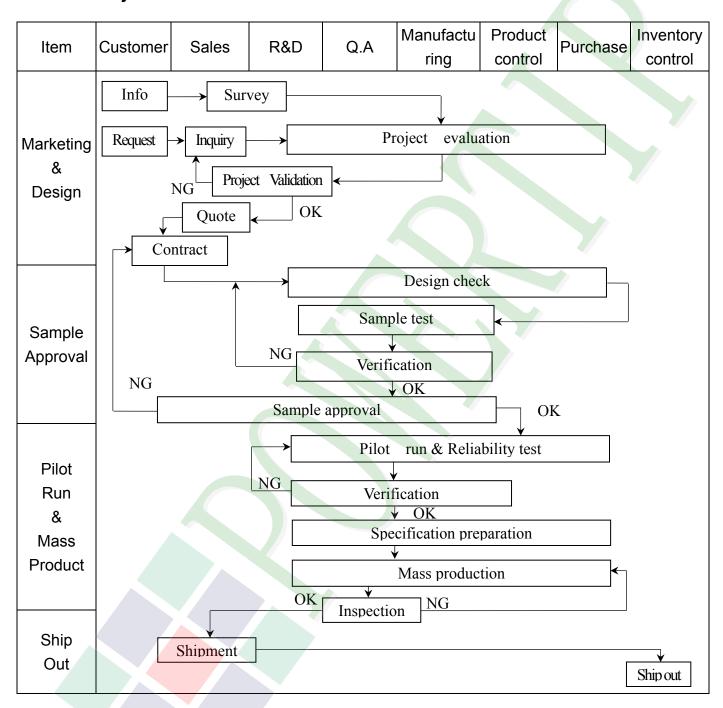


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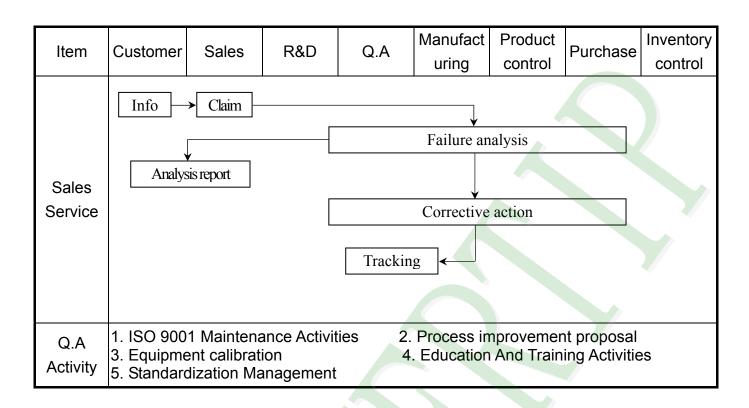


# 3. QUALITY ASSURANCE SYSTEM

# 3.1 Quality Assurance Flow Chart









# 3.2. Inspection Specification

◆Scope: The document shall be applied to TFT-LCD Module for 3, 5" ~15" (Ver.B01).

◆Inspection Standard: MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.

◆Equipment: Gauge · MIL-STD · Powertip Tester · Sample

◆Defect Level: Major Defect AQL: 0,4; Minor Defect AQL: 1,5

◆OUT Going Defect Level: Sampling.

◆Standard of the product appearance test:

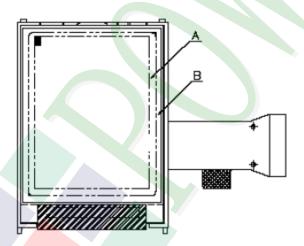
a. Manner of appearance test:

(1). The test best be under 20W×2 fluorescent light, and distance of view must be at 30 cm.

(2). The test direction is base on about around 45° of vertical line.



(3). Definition of area.



A area: viewing area

B area: Outside of viewing area

(4). Standard of inspection: (Unit: mm)



 $\spadesuit Specification For TFT-LCD Module 3, 5"~~15":$ 

NO	Item	Criterion					
		1. 1The part number is inconsistent with work order of production.	Major				
01	Product condition	1, 2 Mixed product types.	Major				
		1. 3 Assembled in inverse direction.	Major				
02	Quantity	2. 1The quantity is inconsistent with work order of production.	Major				
03	Outline dimension	3. 1 Product dimension and structure must conform to structure diagram.	Major				
		4. 1 Missing line character and icon.	Major				
		4, 2 No function or no display.					
		4, 3 Display malfunction.	Major				
04	Electrical Testing	4, 4 LCD viewing angle defect.					
		4. 5 Current consumption exceeds product specifications.					
		4. 6 Mura can not be seen through 5% ND filter. (Mura: Under the normal examination angle of view,the picture has the non-uniform phenomenon.)	Minor				
		Item Acceptance (Q'ty)					
		Bright Dot ≤ 4					
	Dot defect	Dot Dark Dot ≤ 5					
		Defect Joint Dot ≤ 3					
05	(Bright dot \ Dark dot)	Total ≤ 7	Minor				
00	On -display	5. 1 Inspection pattern: full white, full black, Red, Green and					
		blue screens.  5. 2 It is defined as dot defect if defect area >1/2 dot.  5. 3 The distance between two dot defect ≥5 mm.					
		5.4 Bright dot that can not be seen through 5% ND filter.					



# $\spadesuit Specification For TFT-LCD Module 3, 5"~15":$

NO	Item		Criterion						Level
		6. 1 Rour	nd type (	Non-displa	y or di	splay):			
		I	Dimensio		on (diameter : Φ)		Acceptance (Q'ty) A area B area		
				$\Phi \leq 0$ .	25	Ignore	1		
	Black or white dot > scratch >		0.25	< Φ ≤ 0	50	5			
	contamination			$\Phi > 0$	.50	0	Ignore		
	D 14			Total		5			
	Round type  → x ← ↓	6, 2 Line	type( No	on-display o	or displ	ay):			
06	<u> </u>	modu	ıle size	Length (L)	w	idth (W)	Acceptanc A area	e (Q'ty) B area	Minor
	$\Phi = (x+y)/2$					$W \le 0.03$	Ignore		
	¥-(x+y)/2	3.5" to less 9		L ≤10.0			4		
	Line type		less 9"	L ≦5.0	0.05	W > 0.10	As round type	Ignore	
	_ / ¥ W				Total		5		
	→ T			/		$W \le 0.05$	Ignore		
	L L			L ≤10.0	0.05	<w 0.10<="" td="" ≤=""><td>5</td><td></td><td></td></w>	5		
		9" to	9" to 15"	-	- W >0.10		As round type	Ignore	
					Total	l	5		
						A	(014-1)		
		Dir	mension	(diameter :	Φ)	A area	nce (Q'ty) B are	ea	
				$\Phi \leq 0.25$		Ignore			
07	Polarizer		0.25 <	$\Phi \le 0.50$		4			Minor
2	Bubble		0.50 <	$\Phi \le 0.80$		1	Ignor	re	
		XL		Φ > 0.80	)	0			
			]	Total		5			



# ◆Specification For TFT-LCD Module 3, 5" ~15":

NO	Item	Criterion		Level
		Z: The thickness of crack	Y : The width of crack. V : terminal length a : LCD side length	
		8. 1 General glass chip: 8. 1. 1 Chip on panel surface and cra	ick between panels:	
		Z Z	Z X	
08	The crack of glass	SP Y [OK]	[NG]	Minor
		Seal width	Y	
		X Y	Z	
		≤ a Crack can't enter viewing area	≦1/2 t	
4		≤ a Crack can't exceed the half of SP width.	1/2 t < Z ≤2 t	



# Specification For TFT-LCD Module 3, 5" ~15": (Ver.B01) NO Item Criterion Level Symbols: X: The length of crack Y: The width of crack. Z: The thickness of crack W: terminal length t: The thickness of glass a: LCD side length 8.1.2 Corner crack: $\mathbf{X}$ Y Z Crack can't enter ≤1/5 a $\leq 1/2 t$ Z viewing area Crack can't exceed the ≦1/5 a $1/2 t < Z \leq 2 t$ half of SP width. 08 The crack of glass Minor 8.2 Protrusion over terminal: 8.2.1 Chip on electrode pad: Х Y Z $\leq 1/2 \text{ W}$ ≦ t Front ≦ a

≦ a

Back

**≤** W

 $\leq 1/2 t$ 



# ◆Specification For TFT-LCD Module 3, 5″ ~15″: (Ver.B01)

NO	Item	Criterion	
NO 08	The crack of glass	Symbols:  X: The length of crack Z: The thickness of crack t: The thickness of glass  8. 2. 2 Non-conductive portion:  X Y Z  X Y Z  S  X Y Z  S  X Y Z  S  X Y Z  S  S  S  S  S  S  S  S  S  S  S  S	Level



◆Specification For TFT-LCD Module 3, 5″ ~15″:

NO	Item	Criterion	Level
09	Backlight elements	9. 1 Backlight can't work normally.	Major
		9. 2 Backlight doesn't light or color is wrong.	Major
		9. 3 Illumination source flickers when lit.	Major
10	General	10. 1 Pin type \quantity \quantity \dimension must match type in structure diagram.	Major
		10, 2 No short circuits in components on PCB or FPC .	Major
		10. 3 Parts on PCB or FPC must be the same as on the production characteristic chart . There should be no wrong parts, missing parts or excess parts.	Major
		10. 4 Product packaging must the same as specified on packaging specification sheet.	Minor
		10. 5 The folding and peeled off in polarizer are not acceptable.	Minor
		10. 6 The PCB or FPC between B/L assembled distance(PCB or FPC ) is ≤1.5 mm.	Minor



# 4. RELIABILITY TEST

# 4.1 Reliability Test Condition

NO.	TEST ITEM	TEST CONDITION		
1	High Temperature Storage Test	Keep in +80 ±2°C 96 hrs Surrounding temperature, then storage at normal condition 4hrs.		
2	Low Temperature Storage Test	Keep in −30 ±2°C 96 hrs Surrounding temperature, then storage at normal condition 4hrs.		
3	High Temperature / High Humidity Storage Test	Keep in +60 °C / 90% R.H duration for 96 hrs Surrounding temperature, then storage at normal condition 4hrs. (Excluding the polarizer)		
4	Temperature Cycling Storage Test	$-30^{\circ}\text{C} \rightarrow +25^{\circ}\text{C} \rightarrow +80^{\circ}\text{C} \rightarrow +25^{\circ}\text{C}$ $(30_{\text{mins}})  (5_{\text{mins}})  (5_{\text{mins}})$ $10 \text{ Cycle}$ Surrounding temperature, then storage at normal condition 4hrs.		
5	ESD Test	<ul><li>4. Discharge Resistance</li><li>5. Discharge, mode of of</li></ul>	$60\% \sim 60\%$ acitance(Cs+Cd): $150$ pF $\pm 10\%$ e(Rd): $330\Omega \pm 10\%$ operation: uccessive discharges at least 1 sec)	
6	Vibration Test (Packaged)	<ol> <li>Sine wave 10~55 Hz frequency (1 min/sweep)</li> <li>The amplitude of vibration :1. 5 mm</li> <li>Each direction (X \cdot Y \cdot Z) duration for 2 Hrs</li> </ol>		
7	Drop Test (Packaged)	Packing Weight (Kg)  0 ~ 45. 4  45. 4 ~ 90. 8  90. 8 ~ 454  Over 454  Drop Direction: %1 corner / 3 edge	122 76 61 46	



# 5. PRECAUTION RELATING PRODUCT HANDLING

### **5.1 SAFETY**

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

### **5.2 HANDLING**

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully ,do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is 320±10°C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM.

### **5.3 STORAGE**

- 5.3.1 Store the panel or module in a dark place where the temperature is 25°C ±5°C and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

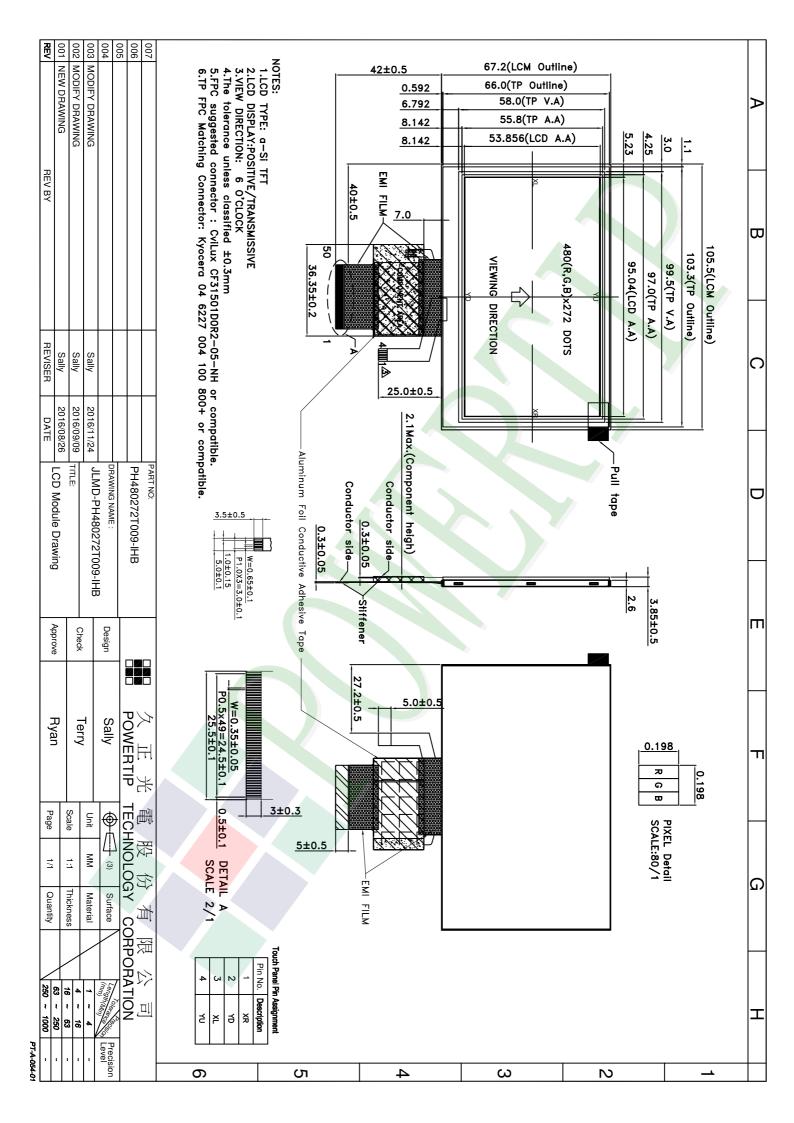
### **5.4 TERMS OF WARRANTY**

5.4.1 Applicable warrant period

The period is within thirteen months since the date of shipping out under normal using and storage conditions.

5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.



Approve Check Contact Ver.001 LCM包裝規格書 LCM Packaging Specifications Ryan Documents NO. JPKG-PH480272T009-IHB Terry Sally (For Tray) 1.包裝材料規格表 (Packaging Material): (per carton) Item 1Pcs Weight Total Weight No. Dimensions (mm) Quantity 1 成品 (LCM) PH480272T009-IHB 105.5 X 67.2 X 3.85 144 0.057 8.208 2 多層薄膜(1)POF 19"X350X0.015 6 OTFILM0BA03ABA 3 352 X 260 X 12.8 42 TRAY 盤 (2)Tray TY0000000393 4.2 0.1 4 内盒(3)Product Box BX36627063ABBA 6 1.092 383 X 270 X 66 0.182 OTPLB00PL08ABA 2 5 550 X 393 X 20 0.0284 0.0568 保利龍板(4)Polylon board 6 外紙箱(5)Carton BX57041027CCBA 570 X 410 X 265 1.0 1 1.0 7 8 9 2. 一整箱總重量 (Total LCD Weight in carton ): 3.單箱數量規格表 (Packaging Specifications and Quantity): (1)LCM quantity per box : no per tray x no of tray 6 24 (2) Total LCM quantity in carton: quantity per box x no of boxes 24 6 144 (4)保利龍板 Polylon board Use empty tray 空盤 (1)多層薄膜 POF Put products into the tray (2)TRAY 盤 (4)保利龍板 Tray Polylon board 仆 (3)内盒 Tray stacking Product Box (5)外紙箱 Carton 特 記 事 項 (REMARK) Detail B

# A Detail B Tray 2 Tray 1

4.TRAY盤相疊時,需旋轉180度,請詳見B視圖 Rotate tray 180 degrees and place on top of stack Check the tray stack using Fig. B.





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More information and worldwide locations can be found at