

	SPECIFICATIONS					
JSTOMER	:					
MPLE CODE	SH800480T0	13-IHA01				
ASS PRODUCTION CODE	PH800480T0	PH800480T013-IHA01				
MPLE VERSION	. 01	: 01				
PECIFICATIONS EDITION	006	006				
RAWING NO. (Ver.)	LMD-PH8004	80T013-IHA01 (Ver.002)				
		80T013-IHA01 (Ver.001)				
CKAGING NO. (Ver.)	Eustomer Approved					
	ustomer Approved	Date:				
C	Customer Approved	Date:				
	ustomer Approved					
C	Customer Approved	Date:				
Approved	Checked	Date: Designer				

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History of Version

Date (mm / dd / yyyy)	Ver.	Edi.	Description	Page	Design by
04/12/2016	01	001	New Drawing.		Eric
06/07/2016	01	002	New Sample.	-	Eric
08/12/2016	01	003	Update Optical Characteristics.	6	Ackey
09/27/2017	01	004	Update DC Electrical Characteristics.	5	Ackey
09/12/2019	01	005	Update Counter Drawing.	10	Ackey
06/16/2021	01	006	Change Vcom Voltage: 3.9 V To 3.6V. Update Reliability Test. Update Quality Assurance System. Update Precaution Relating Product Handling.	5 18 25 26	Ackey



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Appendix : LCM Drawing.

LCM Packaging Specifications



1. SPECIFICATIONS

1.1 Features

Item	Standard Value
Display Resolution	800 *3 (RGB) * 480 Dots
LCD Type	a-Si TFT, Normally white, Transmissive type
Screen size(inch)	7.0 inch
Viewing Direction	6 O'clock
Surface treatment	Anti-Glare
Color configuration	R.G.B. Vertical Stripe
Backlight Type	White LED B/L
Weight	115 g
Interface	24 Bits RGB Interface
	THIS PRODUCT CONFORMS THE ROHS OF PTC
ROHS	Detail information please refer website :
	http://www.powertip.com.tw/news_detail.php?Key=1&cID=1

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	164.9 (W) * 100.0 (L) * 3.4 (H)	mm

LCD panel

Item	Standard Value	Unit
Active Area	154.08 (W) * 85.92 (L)	mm

Note : For detailed information please refer to LCM drawing.



1.3 Absolute Maximum Ratings

Module

Item	Symbol	Condition	Min.	Max.	Unit	Remark
Power Supply Voltage	VDD	GND=0	-0.3	+5.0	V	
Power Supply Voltage	AVDD	GND=0	6.5	+13.5	V	
Operating	T _{OP}	_	-20	+70	°C	-
Temperature	I OP		-20	+70	U	
Storage Temperature	Τ _{ST}	-	-30	+80	°C	

The absolute maximum rating values of this product are not allowed to be exceeded at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

1.4 DC Electrical Characteristics

Module		GND = 0V, Ta					
Item	Symbol	Min.	Тур.	Max.	Unit	Remark	
		3.0	3.3	3.6			
Supply Voltage	V _{GH}	15.3	16.0	16.7	v		
Supply Voltage	V _{GL}	-7.7	-7.0	-6.3	v		
	AV _{DD}	10.2	10.4	10.6		-	
V _{com}	V _{com}	-	3.6	-	V		
Input signal Voltage	V _{IH}	$0.7 \text{ DV}_{\text{DD}}$	-	DV_DD	v		
input signal voltage	VIL	0	-	$0.3 \text{ DV}_{\text{DD}}$	V		
Cumply Current		-	80	-		Pattern= Full display	
Supply Current	IDV _{DD}	-	80	120	mA	Pattern= Black *1	

Note1: Maximum current display.



1.5 Optical Characteristics

TFT LCD Module

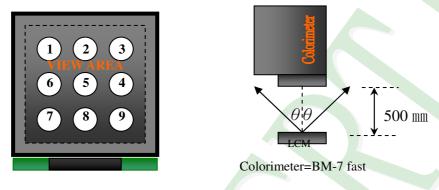
VDD = 3.3 V, Ta=25 ℃

Item		Symbol	Condition	Min.	Тур.	Max.	unit	
Dooponoo timo	Rise	Tr	Ta = 25 ℃	-	10	20		Note 2
Response time	Fall	Tf	$\theta X, \theta Y = 0^{\circ}$	-	15	30	ms	Note 2
	Тор	θY+		40	50	-		
Viowing angle	Bottom	θY-	CR ≥ 10	60	70	1	Dog	Note 4
Viewing angle	Left	θХ-	$CH \ge 10$	60	70	-	Deg.	NOLE 4
	Right	θX+		60	70	-		
Contrast ratio)	CR		400	500	-	-	Note 3
	White	Х		0.23	0.28	0.33		
	vvriite	Y		0.27	0.32	0.37		
	Red	Х	T- 0500	0.52	0.57	0.62		
Color of CIE Coordinate	neu	Y	Ta = 25 ℃ θX , θY = 0°	0.31	0.36	0.41		Note1
(With B/L)	Green	Х	0,1,01 = 0	0.29	0.34	0.39	-	NOLET
	Green	Y		0.55	0.60	0.65		
	Blue	X		0.09	0.14	0.19		
	Diue	Y		0.02	0.07	0.12		
Average Brightn	ess							
Pattern=white display		IV	-	800	1000	-	cd/m ²	Note1
(With B/L)*1								
Uniformity (With B/L)*2		∆B	-	70	-	-	%	Note1



Note 1:

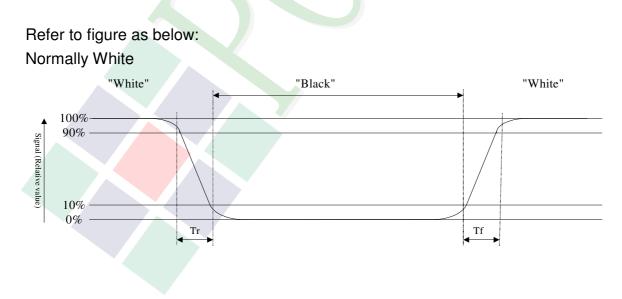
- *1 : △B=B(min) / B(max) * 100%
- *2 : Measurement Condition for Optical Characteristics:
 - a : Environment: 25°C ±5°C / 60±20%R.H , no wind , dark room below 10 Lux at typical lamp current and typical operating frequency.
 - b : Measurement Distance: 500 \pm 50 $\,{\rm mm}^{-}$, (0= 0 °)
 - c : Equipment: TOPCON BM-7 fast , (field 1°) , after 10 minutes operation.
 - d: The uncertainty of the C.I.E coordinate measurement ±0.01, Average Brightness ± 4%



To be measured at the center area of panel with a viewing cone of 1° by Topcon luminance meter BM-7, after 10 minutes operation (module)

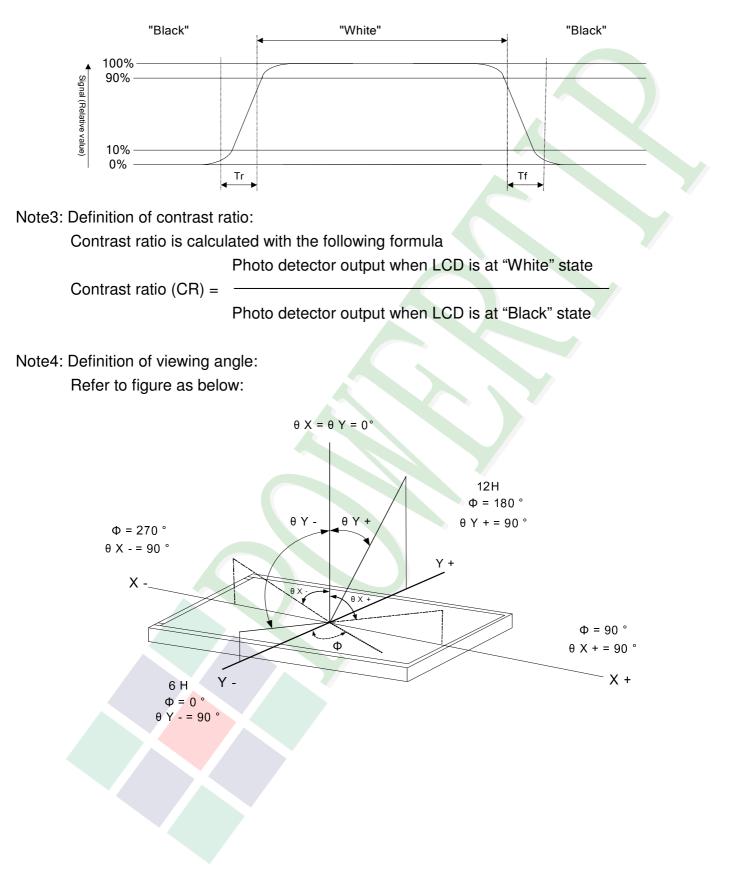
Note2: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white"(falling time) and from "white" to "black"(rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.





Normally Black





1.6 Backlight Characteristics

Maximum Ratings

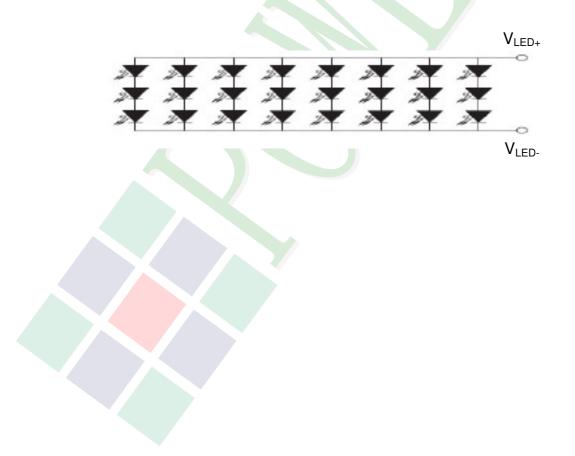
ltem	Symbol	Min.	Max.	Unit	Remark
LED Forward Current	I _F	3	5	mA	One LED
LED Reverse Voltage	V _R	1	0	۷	One LED

Electrical / Optical Characteristics

Item	Symbol	Min.	Тур.	Max.	Unit	Remark
LED Voltage	VL	17.4	18.6	19.8	V	Note1
LED Current	١L		140		mA	_
LED life time	-	50000	-	-	Hr	Note2

Note 1: The LED Supply Voltage is defined by the number of LED at Ta=25 $^{\circ}$ C and IL =180mA.

Note 2: The "LED life time" is defined as the module brightness decrease to 50% original brightness at Ta=25 °C and I∟=140mA. The LED lifetime could be decreased if operating I∟ is lager than 160mA.





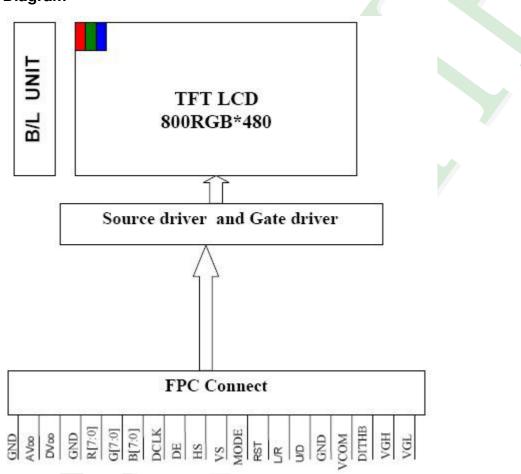
2. MODULE STRUCTURE

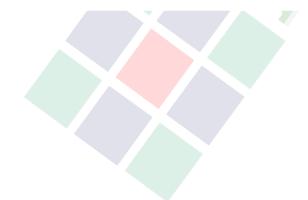
2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

2.1.2 Block Diagram







2.2 Interface Pin Description

TFT LCM Interface

Pin#	Name	DESCRIPTION					
1	LEDA	LED backlight (Anode).					
2	LEDA	ED backlight (Anode).					
3	LEDK	LED backlight (Cathode).					
4	LEDK	LED backlight (Cathode).					
5	GND	Power ground					
6	VCOM	Common Voltage.					
7	DVDD	Digital Power.					
8	MODE	DE/SYNC mode select. Normally pull high. H: DE mode. L: HSD/VSD mode.					
9	DE	Data Enable signal.					
10	VS	Vertical sync input. Negative polarity.					
11	HS	Horizontal sync input. Negative polarity.					
12	Β7	Blue Data Input (MSB).					
13	B6	Blue Data Input.					
14	В5	Blue Data Input.					
15	B4	Blue Data Input.					
16	B3	Blue Data Input.					
17	B2	Blue Data Input.					
18	B1	Blue Data Input.					
19	BO	Blue Data Input (LSB).					
20	G7	Green Data Input (MSB).					
21	G6	Green Data Input.					
22	G5	Green Data Input.					
23	G4	<mark>Gr</mark> een Data Input.					
24	G3	Green Data Input.					
25	G2	Green Data Input.					
26	G1	Green Data Input.					
27	GO	Green Data Input (LSB).					
28	R7	Red Data Input (MSB).					
29	R6	Red Data Input.					

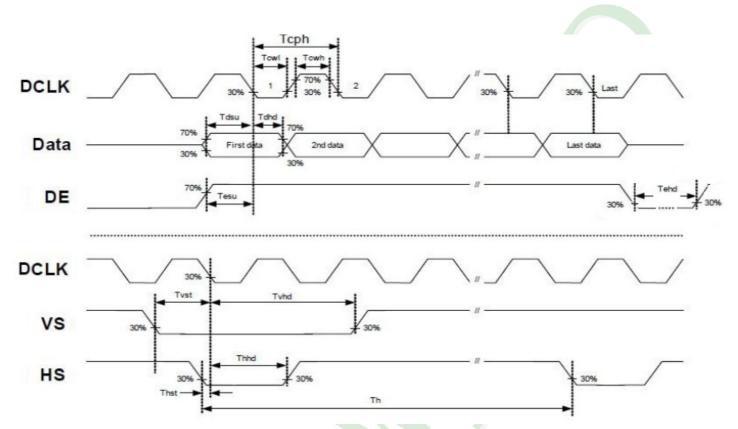


Pin#	Name	DESCRIPTION
30	R5	Red Data Input.
31	R4	Red Data Input.
32	R3	Red Data Input.
33	R2	Red Data Input.
34	R1	Red Data Input.
35	RO	Red Data Input (LSB).
36	GND	Power ground.
37	DCLK	Clock input.
38	GND	Power ground.
39	L/R	Left or Right Display Control.
40	U/D	Up / Down Display Control.
41	VGH	Positive Power for TFT.
42	VGL	Negative Power for TFT.
43	AVDD	Analog Power.
44	RESET	Global reset pin. Active low to enter reset state. Suggest to connecting with an RC reset circuit for stability. Normally pull high. (R=10K Ω , C=1 μ F)
45	NC	Not connect.
46	VCOM	Common Voltage.
47	DITHB	Dithering function enable control. (Normally pull high) DITHB= "L", to enable internal dithering function. DITHB= "H", to disable internal dithering function.
48	GND	Power ground.
49	NC	Not connect.
50	NC	Not connect.



2.3 Timing Characteristics

2.3.1 Signal AC Characteristics



ltow	Cumhal		Values		Unit	Demerik
ltem	Symbol	Min	Тур	Max	Unit	Remark
HS setup time	Thst	8	-	-	ns	
HS hold time	Thhd	8	-	-	ns	
VS setup time	Tvst	8	-	-	ns	
VS hold time	Tvhd	8	-	-	ns	
Data setup time	Tdsu	8	-	-	ns	
Data hole time	Tdhd	8	-	-	ns	
DE setup time	Tesu	8	-	-	ns	
DE hold time	Tehd	8	-	-	ns	
DVDD Power On Slew rate	TPOR	-	-	20	ms	From 0 to 90% DVDD
RESET pulse width	TRst	1	-	-	ms	
DCLK cycle time	Tcph	20	30	-	ns	
Low Level Width	Tcwl	8	-	-	ns	
High Level Width	Tcwh	8	-	-	ns	
DCLK pulse duty	Duty	40	50	60	%	Tcwh / Tcph

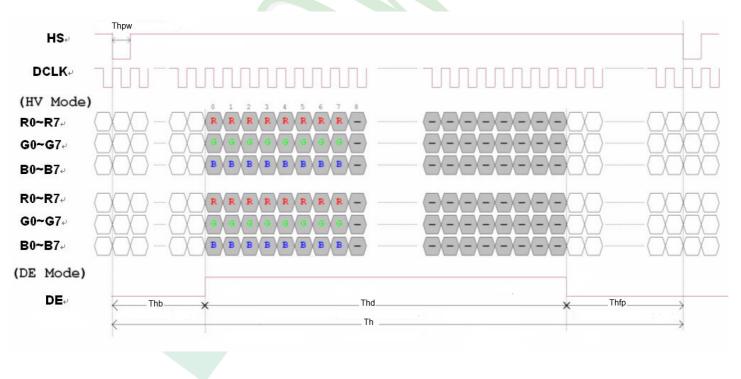


2.3.2 Input Timing Setting

Item	Symbol	Values			Unit	Remark
		Min.	Тур.	Max.		
Horizontal Display Area	Thd		800		DCLK	
DCLK Frequency	Fclk	26.4	33.3	46.8	MHz	
One Horizontal Line	Th	862	1056	1200	DCLK	
HS pulse width	Thpw	1		40	DCLK	
HS Blanking	Thb	46	46	46	DCLK	
HS Front Porch	Thfp	16	210	354	DCLK	

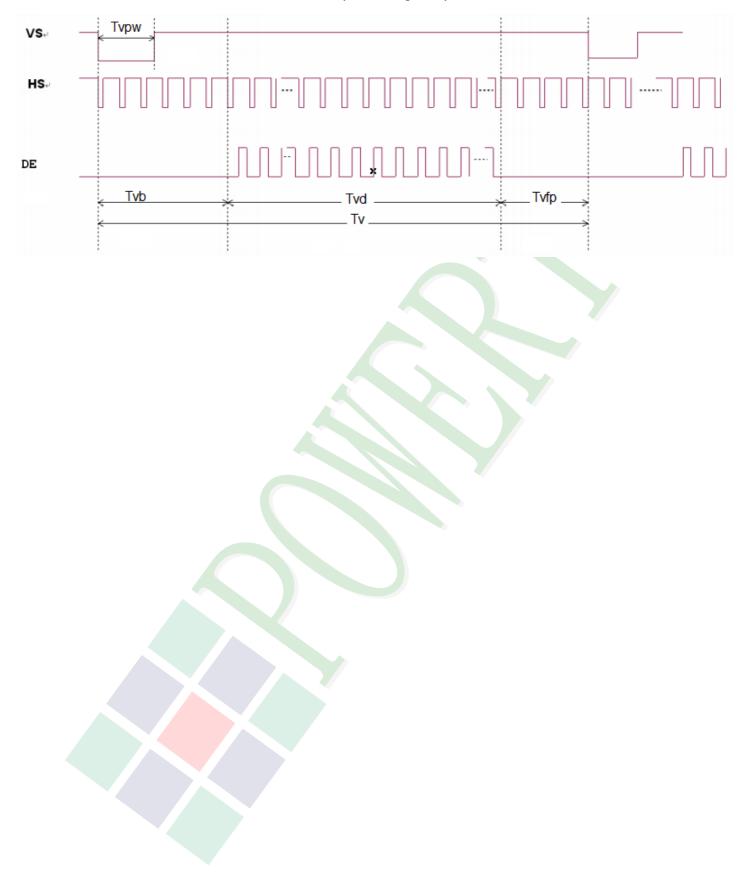
Item	Symbol	Values		Unit	Remark	
		Min.	Тур.	Max.		
Vertical Display Area	Tvd		480		TH	
VS period time	Τv	510	525	650	TH	
VS pulse width	Tvpw	1		20	TH	
VS Blanking	Tvb	23	23	23	TH	
VS Front Porch	T∨fp	7	22	147	TH	

Horizontal input timing diagram

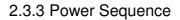




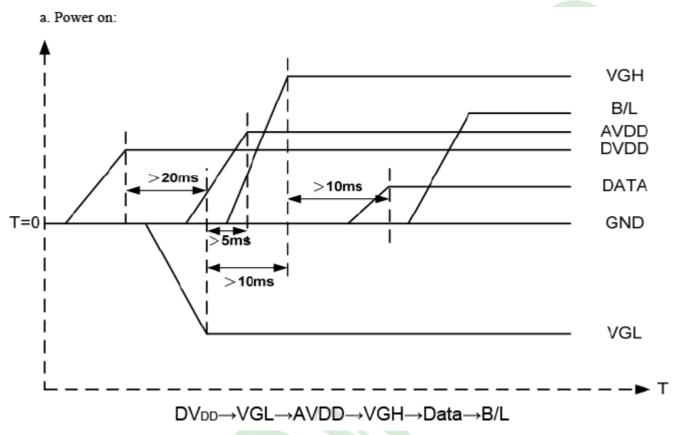
Vertical input timing diagram





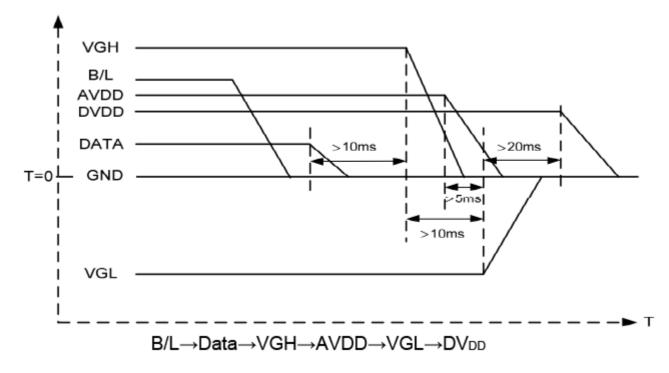


POWER ON



POWER OFF

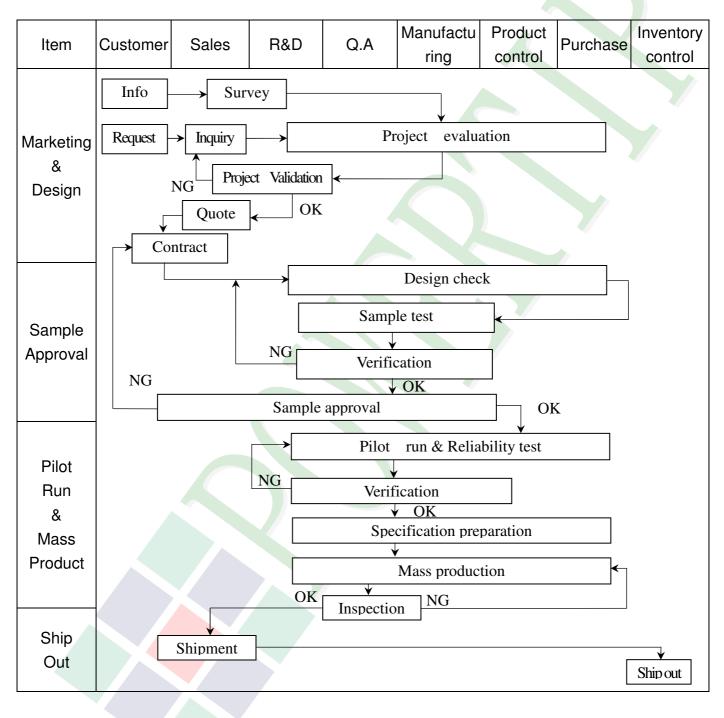
b. Power off:





3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart





Item	Customer	Sales	R&D	Q.A	Manufact uring	Product control	Purchase	Inventory control
Sales Service	Info	→ Claim sis report	[Trackin	Failure an Corrective			
Q.A Activity	1. ISO 900 3. Equipme 5. Standard	ent calibrat	ion	4	Process in . Education			es

POWERTIP

3.2. Inspection Specification

Scope : The document shall be applied to TFT-LCD Module for 3. 5" ~10" (Ver.B01).

◆Inspection Standard : MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.

◆Equipment : Gauge、MIL-STD、Powertip Tester、Sample

◆Defect Level : Major Defect AQL : 0.4 ; Minor Defect AQL : 1.5

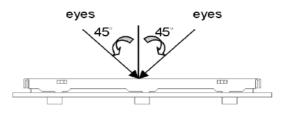
♦OUT Going Defect Level : Sampling.

◆Standard of the product appearance test ∶

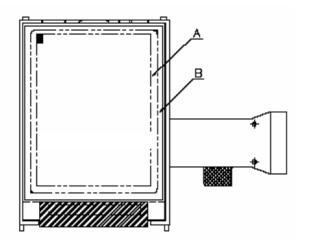
a. Manner of appearance test :

(1). The test best be under 20W×2 fluorescent light, and distance of view must be at 30 cm.

(2). The test direction is base on about around 45° of vertical line.



(3). Definition of area.



A area : viewing area

B area : Outside of viewing area

(4). Standard of inspection : (Unit : mm)



◆Specification For TFT-LCD Module 3. 5″~10″:

♦Spe	cification For TFT-L	CD Modu	ıle 3. 5″	~10″:		(Ver.B01)		
NO	Item		Criterion					
			part nur duction.	umber is inconsistent with work order of n.				
01	Product condition	1. 2 Mixed product types.						
		1. 3 Asso	embled i	n inverse direction.		Major		
02	Quantity	2. 1The	2. 1 The quantity is inconsistent with work order of production.					
03	Outline dimension		3.1 Product dimension and structure must conform to structure diagram.					
		4.1 Mis	4. 1 Missing line character and icon.					
		4. 2 No function or no display.						
04	Electrical Testing	4. 3 Display malfunction.						
		4. 4 LCD viewing angle defect.						
		4. 5 Current consumption exceeds product specifications.						
				Item	Acceptance (Q'ty)			
	Dot defect			Bright Dot	≦ 4			
	Dot delect		Dot	Dark Dot	≦ 5			
	(Bright dot \		Defect	Joint Dot	≦ 3			
05	Dark dot)		Ì	Total	≦ 7	Minor		
	On -display	5. 1 Inspection pattern : full white , full black , Red , Green and						
				blue screen				
				as dot defect if defe				
		5. 3 The	distance	e between two dot d	lefect ≥ 5 mm.			



♦ Speci	fication For TFT-L	CD Mod	ule 3. 5″	~10″:					(Ver.B01)
NO	Item			Cri	iterion				Level
		6. 1 Ro	6. 1 Round type (Non-display or display) :						
			Dimension	(diameter∶Φ)	Ac	ceptance	e (Q	'ty)	
	Black or white				A ai	rea	B	area	
	dot、scratch、			$\Phi \leq 0.25$	Ign	ore			
	contamination		0.25 <	$\Phi \leq 0.50$	5	;	Ŧ		
	Round type			$\Phi > 0.50$	0)	Ig	gnore	
	\rightarrow X \leftarrow		r	Fotal	5	;			
06	$\Phi = (x+y)/2$	6. 2 Lin	ne type(No	on-display or di	isplay):				Minor
				X 77 1.1		Accep	otan	ce (Q'ty)	
	Line type		ength (L)	Width (w)	A are	ea	B area	
	⊂ /¥w			W	≤ 0.03	Igno	re		
	→ _L ⊷	L	<i>.</i> ≦10.0	0.03 < W	≤ 0.05	4			
		I	L≦5.0	0.05 < W	≤ 0.10	2		Ignore	
				W	>0.10	As rou type			
				Total		5			
					A -		. (0	94	
		Dir	mension (d	liameter∶Φ) -	AC A ar	ceptanc ea		B area	
				$\Phi \leq 0.25$	Igno	ore			
07	Polarizer		0.25 <	$\Phi \leq 0.50$	4				Minor
	Bubble		0.50 <	$\Phi \leq 0.80$	1			Ignore	
				$\Phi > 0.80$	0				
			То	tal	5				



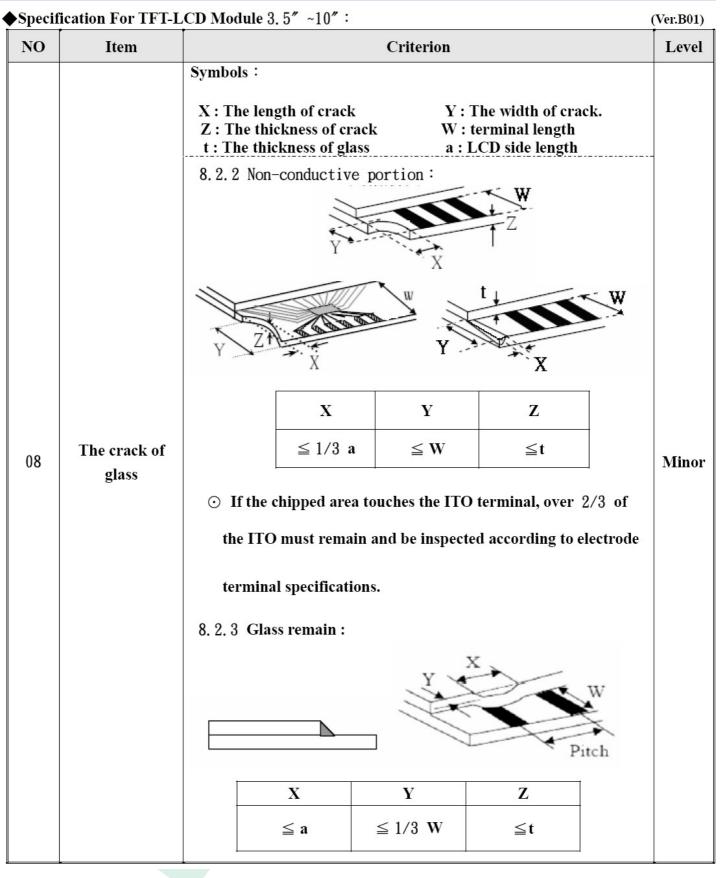
◆Specification For TFT-LCD Module 3. 5″ ~10″:

◆Specification For TFT-LCD Module 3. 5″~10″:						
NO	Item	Criterion		Level		
		Criterion Symbols : X : The length of crack Z : The thickness of crack t : The thickness of glass 8. 1 General glass chip : 8. 1. 1 Chip on panel surface and cra SP Y [OK] Seal width Z Y Cox	Y ING	(Ver.B01) Level		
		X Y	Z			
		$\leq a \qquad \begin{array}{c} Crack \ can't \ enter \\ viewing \ area \end{array}$	$\leq 1/2 t$			
		$\leq a \qquad \begin{array}{c} Crack \ can't \ exceed \ the \\ half \ of \ SP \ width. \end{array}$	$1/2 t < Z \leq t$			



♦Specifi	cation For TFT-LCD	Module 3. 5"	~10″:			(Ver.B01)	
NO	Item		Criter	ion		Level	
		Symbols :X : The length of crack Z : The thickness of crack t : The thickness of glassY : The width of crack. W : terminal length a : LCD side length8. 1. 2 Corner crack :					
		X	Y		Z		
		$\leq 1/5$ a	Crack can't enter viewing area	r Z	$\leq 1/2 t$		
		$\leq 1/5$ a	Crack can't exceed half of SP width.	1/7 1 2	$< \mathbf{Z} \leq 2 \mathbf{t}$		
08	The crack of glass		sion over terminal p on electrode pad X $\leq a$ $\leq a$		$\frac{Z}{\leq t} \leq 1/2 t$	Minor	







4. RELIABILITY TEST

4.1 Reliability Test Condition

(Ver.B01)

TEST ITEMHigh Temperature Storage TestLow Temperature Storage TestHigh Temperature / High Humidity Storage TestTemperature Cycling Storage Test		brage at normal condition 4hrs. brage at normal condition 4hrs. In for 240 hrs brage at normal condition 4hrs. $+80^{\circ}C \rightarrow +25^{\circ}C$				
Storage Test Low Temperature Storage Test High Temperature / High Humidity Storage Test Temperature Cycling	Surrounding temperature, then sto Keep in -30 ±2°C 240 hrs Surrounding temperature, then sto Keep in +60°C / 90% R.H duration Surrounding temperature, then sto (Excluding the polarizer) $-30°C \rightarrow +25°C \rightarrow$ (30mins) (5mins) (brage at normal condition 4hrs. In for 240 hrs brage at normal condition 4hrs. $+80^{\circ}C \rightarrow +25^{\circ}C$				
Storage Test High Temperature / High Humidity Storage Test Temperature Cycling	Surrounding temperature, then sto Keep in +60°C / 90% R.H duration Surrounding temperature, then sto (Excluding the polarizer) $-30°C \rightarrow +25°C \rightarrow$ (30mins) (5mins) (for 240 hrs brage at normal condition 4hrs. +80°C → +25°C				
High Humidity Storage Test Temperature Cycling	Surrounding temperature, then sto (Excluding the polarizer) $-30^{\circ}C \rightarrow +25^{\circ}C \rightarrow$ (30mins) (5mins) (prage at normal condition 4hrs. +80°C → +25°C				
	(30 mins) (5 mins) (
ESD Test	Air Discharge: Apply 2 KV with 5 times Discharge for each polarity +/- 1. Temperature ambiance : 15°C ~ 2. Humidity relative : 30% ~60% 3. Energy Storage Capacitance(C 4. Discharge Resistance(Rd) : 330 5. Discharge, mode of operation : Single Discharge (time between suc (Tolerance if the output voltage inc	s+Cd) : 150pF±10% Ω2±10% ccessive discharges at least 1 sec)				
Vibration Test (Packaged)	 Sine wave 10~55 Hz frequency (1 min) The amplitude of vibration :1.5 mm Each direction (X \ Y \ Z) duration for 2 Hrs 					
Drop Test (Packaged)	Packing Weight (Kg) 0 ~ 45. 4 45. 4 ~ 90. 8 90. 8 ~ 454 Over 454	Drop Height (cm) 122 76 61 46 x / 6 sides each 1 times				
	Vibration Test (Packaged) Drop Test	3. Energy Storage Capacitance(C4. Discharge Resistance(Rd) : 3305. Discharge, mode of operation :Single Discharge (time between suc (Tolerance if the output voltage ind)1. Sine wave 10~55 Hz frequence2. The amplitude of vibration :1.3. Each direction (X \ Y \ Z) durPacking Weight (Kg)0 ~ 45.445.4 ~ 90.890.8 ~ 454				



5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So, please handle it very carefully, do not touch, push or rub the exposed polarizing with anything harder than an HB pencil lead (glass, tweezers, etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is $320 \pm 10^{\circ}$ C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM
- 5.2.10 Caution!(LCM products with Capacitive Touch Panel)Strong EMI-sources such as switch-mode power supplies (SMPS) can lead to touch malfunction (e.g. ghost-touches).

Therefore, the touch needs to be thoroughly tested inside the target application.

- 5.2.11 CAUTION: Continuously displaying same static image will result in high possibility of image sticking/image burn-in effect due to TFT panel characteristic.
- 5.2.12 Double-sided tape designed to be attach with the customer's mechanical device, please follow up the rules and regulations published by the original manufacturer of double-sided tape for the attachment operation.

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is $25^{\circ}C \pm 5^{\circ}C$ and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

5.4 TERMS OF WARRANTY

- 5.4.1 Applicable warrant period The period is within thirteen months since the date of shipping out under normal using and storage conditions.
- 5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.

