



SPECIFICATION



PH800480T032-ZHA

7.0" - WVGA - RGB

Version: 1.4

Date: 04.12.2020

Note: This specification is subject to change without prior notice



History of Version

Date (mm / dd / yyyy)	Ver.	Edi.	Description	Page	Design by
05/04/2020	01	001	New Drawing According to customer request modify Contents	-	Yuan
06/23/2020	01	02	First Sample	-	Yuan
11/20/2020	01	03	Modify Parallel 24-bit RGB Input Timing	16	Yuan
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		X			



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Note: For detailed information please refer to IC data sheet:

Himax---HX8249-A02 Himax---HX8678-C



1. SPECIFICATIONS

1.1 Features

<u>Item</u>	Standard Value			
Display Resolution	800 *3 (RGB) * 480 Dots			
LCD Type	IPS, Normally Black, Transmissive type			
Screen size(inch)	7 inch			
Surface treatment	Anti-Glare			
Color configuration	R.G.B. Vertical Stripe			
Weight	-			
Interface	Parallel RGB (Data), SPI (Configuration)			
Driver IC	HimaxHX8249-A02			
Driver 10	HimaxHX8678-C			
	THIS PRODUCT CONFORMS THE ROHS OF PTC			
ROHS	Detail information please refer website:			
	http://www.powertip.com.tw/news_detail.php?Key=1&cID=1			

1.2 Mechanical Specifications

<u>ltem</u>	Standard Value	<u>Unit</u>
Outline Dimension	164.4 (W) * 105.9 (L) * 5.7 (H)	mm

LCD panel

<u>ltem</u>	Standard Value	<u>Unit</u>
View Area	153.40 (W) * 92.44 (L)	mm
Active Area	152.40 (W) * 91.44 (L)	mm

Note: For detailed information please refer to LCM drawing.



1.3 Absolute Maximum Ratings

Module

<u>Item</u>	<u>Symbol</u>	<u>Condition</u>	Min.	Max.	<u>Unit</u>	Remark
Power Supply for TFT Panel	V_{DD}	GND=0V	-0.3	3.96	V	
Power Supply for Backlight Unit	Vcc	GND=0V	-0.3	+20.0	V	_
Operating Temperature	Тор	-	-20	+70	°C	
Storage Temperature	Tst	-	-30	+80	°C	

The absolute maximum rating values of this product are not allowed to be exceeded at any time. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

Note 1: Ts is the temperature of panel's surface

Note 2: Ta is the ambient temperature of samples

1.4 DC Electrical Characteristics

Module GND = 0V, Ta = 25°C

<u>ltem</u>	Symbol	<u>Condition</u>	Min.	Typ.	Max.	<u>Unit</u>
Power Supply for TFT Panel	VDD	GND=0V	3.0	3.3	3.6	V
Power Supply for Backlight Unit	VCC	GND=0V	5	12	15	V
Input Voltage for	VIH	GND=0V	0.7VDD	-	VDD	
TFT Panel	VIL	GND=0V	0	-	0.3VDD	V
Supply Current for TFT Panel	IDD	IDD@VDD=3.3V	-	100	150	mA
Supply Current for Backlight Unit	ICC	ICC@VCC=5V	-	1.5	2	Α
Supply Current for Backlight Unit	ICC	ICC@VCC=12V	-	0.4	0.6	A
Input Voltage for	VPH	GND=0V	1.2	-	-	V
PWM Signal	VPL	GND=0V	-	-	0.4	V
Dimming Clock Rate	fP	GND=0V	0.1	-	8	KHz



1.5 Optical Characteristics

VDD=3.3V, Ta=25°C

<u>ltem</u>	Syr	<u>nbol</u>	Condition	Min.	Typ.	Max.	<u>unit</u>	
Response time	Tr	+Tf	Ta = 25° C θ X, θ Y = 0°	-	30	45	ms	Note 2
	Тор	θΥ+		-	80	-		
Viewing angle	Bottom	θΥ-	CR ≥ 10		80	1	Deg.	Note 4
viewing angle	Left	θХ-	ON 2 10	6	80	ı	Deg.	Note 4
	Right	θХ+		ľ	80	1		y
Contrast ratio)	CR		650	800	-		Note 3
	White	Х	Ta = 25°C θX , θY = 0°	0.25	0.30	0.35		Note1
	vvriite	Υ		0.29	0.34	0.39		
	Red	Х		0.59	0.64	0.69		
Color of CIE Coordinate		Υ		0.30	0.35	0.40		
(With B/L)	Green	X		0.26	0.31	0.36		
,		Y		0.59	0.64	0.69		
	Blue	X		0.08	0.13	0.18		
		Y		0.01	0.06	0.11		
Average Brightness			VCC=12.0V					
Pattern=white display		F	PWM="High"	800	1000	-	cd/m ²	Note1
(With LCD)*1			(Duty=100%)					
Uniformity			VCC=12.0V					
(With LCD)*2		VB	PWM="High"	70	-	-	%	Note1
(VVIIII LOD) Z			(Duty=100%)					



Note 1:

*1: △B=B(min) / B(max) * 100%

*2: Measurement Condition for Optical Characteristics:

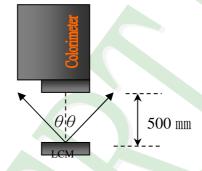
a: Environment: 25°C±5°C / 60±20%R.H, no wind, dark room below 10 Lux at typical lamp current and typical operating frequency

b: Measurement Distance: 500 ± 50 mm, $(\theta = 0^{\circ})$

c: Equipment: TOPCON BM-7 fast, (field 1°), after 10 minutes operation

d: The uncertainty of the C.I.E coordinate measurement ±0.01, Average Brightness ± 4%





Colorimeter=BM-7 fast

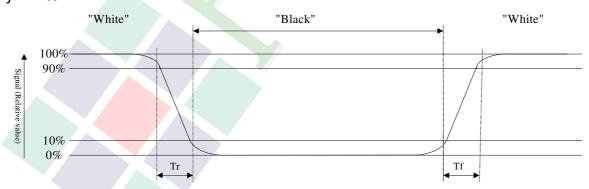
To be measured at the center area of panel with a viewing cone of 1° by Topcon luminance meter BM-7, after 10 minutes operation (module)

Note 2: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

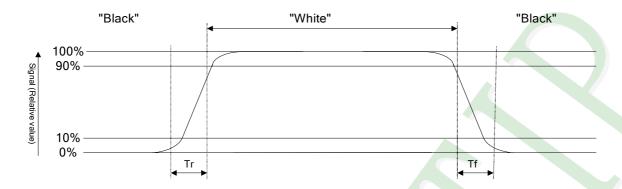
Refer to figure as below:

Normally White





Normally Black



Note 3: Definition of contrast ratio:

Contrast ratio is calculated with the following formula

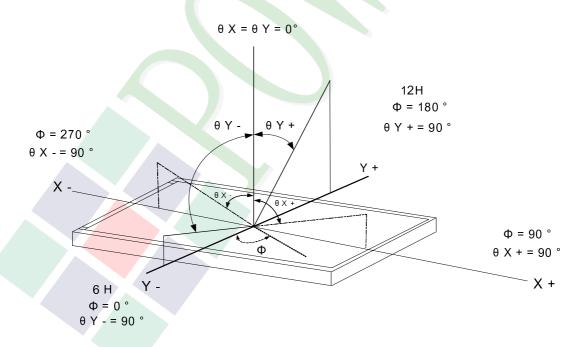
Photo detector output when LCD is at "White" state

Contrast ratio (CR) =

Photo detector output when LCD is at "Black" state

Note 4: Definition of viewing angle:

Refer to figure as below:





1.6 Backlight Characteristics

Maximum Ratings

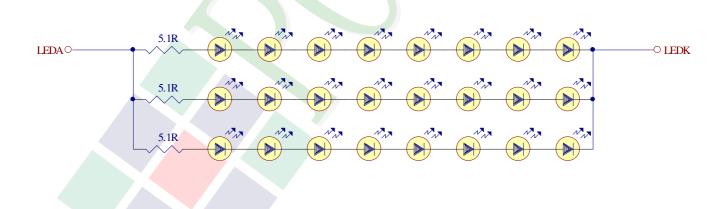
<u>ltem</u>	<u>Symbol</u>	Min.	Max.	<u>Unit</u>	<u>Remark</u>
LED Forward Current	lF	-	90	mA	Dor
LED Reverse Voltage	VR	-	1.2	V	Per

Electrical / Optical Characteristics

<u>Item</u>	<u>Symbol</u>	Min.	<u>Typ.</u>	Max.	<u>Unit</u>	<u>Remark</u>
LED Voltage	VL	22.0	24.0	26.8	V	Note1
LED Current	IL	-	210		mA	-
LED life time	-	50,000	-	1	Hr	Note2

Note 1: The LED Supply Voltage is defined by the number of LED at Ta=25 °C and I∟=210 mA

Note 2: The "LED life time" is defined as the module brightness decrease to 50% original brightness at Ta=25 °C and I∟=210 mA. The LED life time could be decreased if operating I∟ is larger than 210 mA





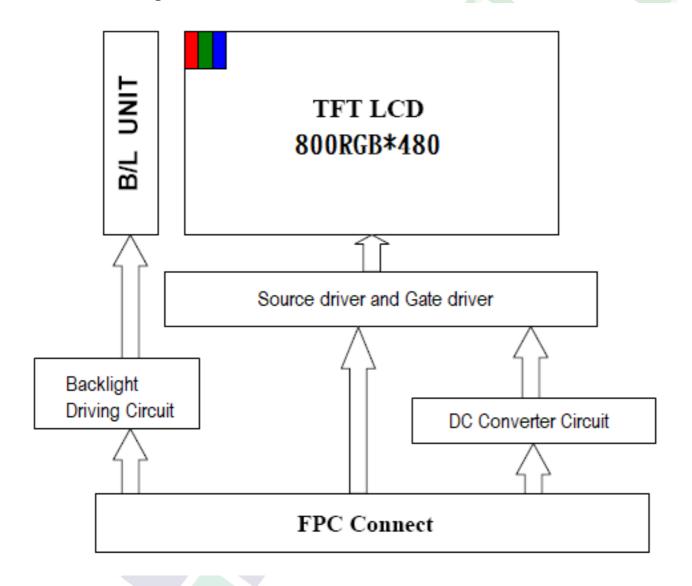
2. Module Structure

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

2.1.2 Block Diagram





2.2 Interface Pin Description

TFT LCM Interface

Pin#	<u>Name</u>	<u>Description</u>
1	GND	Power ground.
2	VDD	Power for Digital Circuit.
3	VDD	Power for Digital Circuit.
4	VCC	Power For LED backlight.
5	VCC	Power For LED backlight.
6	PWM	Shutdown & Dimming control input for backlight. Do not allow this pin to float. "Hi" =100%, "Low" = 0%.
7	GND	Power ground.
8	R0	Red Data.
9	R1	Red Data.
10	R2	Red Data.
11	R3	Red Data.
12	GND	Power ground.
13	R4	Red Data.
14	R5	Red Data.
15	R6	Red Data.
16	R7	Red Data.
17	GND	Power ground.
18	G0	Green Data.
19	G1	Green Data.
20	G2	Green Data.
21	G3	Green Data.
22	GND	Power ground.
23	G4	Green Data.
24	G5	Green Data.
25	G6	Green Data.
26	G7	Green Data.
27	GND	Power ground.
28	В0	Blue Data.
29	B1	Blue Data.



Pin#	<u>Name</u>	<u>Description</u>
30	B2	Blue Data.
31	В3	Blue Data.
32	GND	Power ground.
33	B4	Blue Data.
34	B5	Blue Data.
35	B6	Blue Data.
36	B7	Blue Data.
37	GND	Power ground.
38	HS	Line synchronization signal. Horizontal Sync Input.
39	VS	Frame synchronization signal. Vertical Sync Input.
40	GND	Power ground.
41	DE	Data Enable
42	GND	Power ground.
43	DCLK	Sample clock. Data will be latched at the falling edge of DCLK.
44	GND	Power ground.
45	L CS / ID1	Serial communication chip selection/ID[4:1]These pins select LCM type. See NOTE1
46	SDIN/ID2	Serial communication data/ ID[4:1]These pins select LCM type. See NOTE1
47	SCK/ID3	Serial communication clock/ ID[4:1]These pins select LCM type. See NOTE1
48	DISPLAY	Display Enable(Hi Active)./ ID[4:1]These pins select LCM type.
40	CONTROL / ID4	See NOTE1
49	/RESET	Global Reset (Low Active).
50	GND	Power ground.

Note1:

ID Pins Definition:

	<u>PIN 45 ID1</u>	<u>PIN 46 ID2</u>	<u>PIN 47 ID3</u>	<u>PIN 48 ID3</u>
3.5" Module	Х	0	0	Х
4.3" Module	X	1	0	X
5.0" Module	X	0	1	X
7.0" Module	X	1	1	X

^{1.} Resistor = 10k ohm

^{2. &}quot;X" = No use



2.3 Timing Characteristics

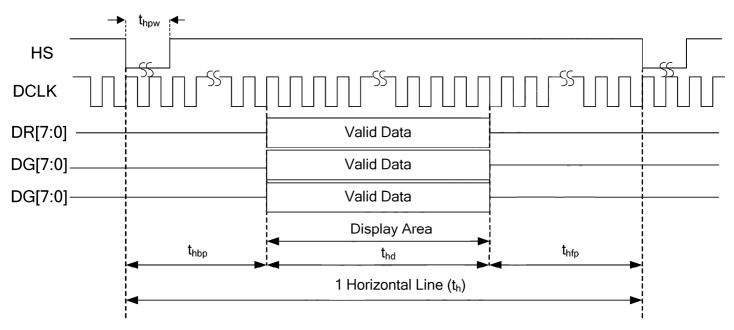
2.3.1 RGB Mode Selection Table

RGB Mode Selection Table	<u>DCLK</u>	<u>HSYNC</u>	<u>VSYNC</u>	<u>DE</u>
SYNC Mode	Input	Input	Input	GND
DE Mode	Input	GND	GND	Input



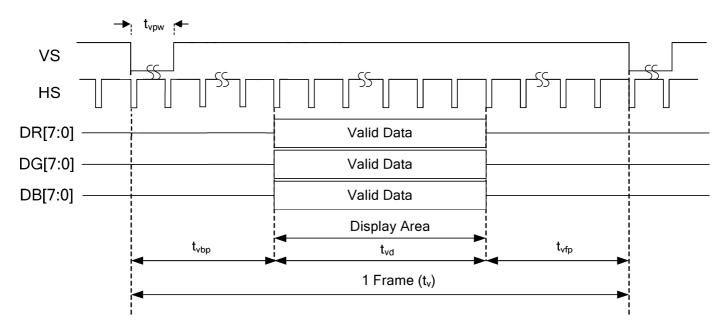
2.3.2 Parallel RGB SYNC Mode

Horizontal



Horizontal input timing at Sync mode

Vertical





2.3.3 Parallel RGB at DE mode

Horizontal

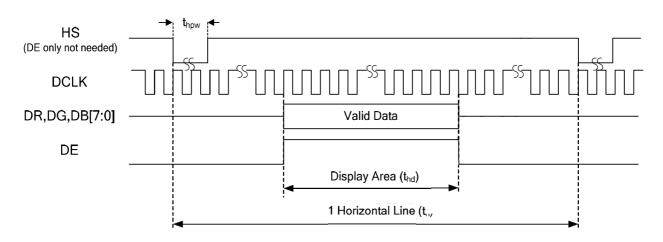
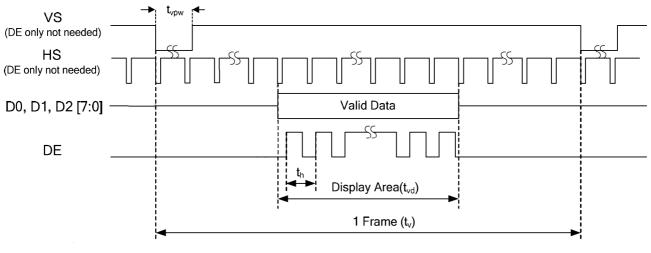
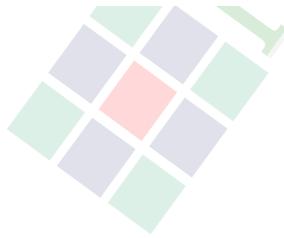


Figure 7.9: Horizontal input timing at DE only mode

Vertical







2.3.4 Parallel 24-bit RGB Input Timing

<u>Item</u>	<u>Symbol</u>	<u>Min</u>	<u>Typ.</u>	<u>Max</u>	<u>Unit</u>	<u>Note</u>
DCLK Frequency	FDCLK	25.2	27.2	30.5	MHz	
Horizontal valid data	thd		800		DCLK	
Hsync pulse width	thpw	1	2	100	DCLK	
Hsync back porch	thbp	5	16	101	DCLK	
Hsync front porch	thfp	19	44	115	DCLK	
1 horizontal line	th	856	860	920	DCLK	
Vertical valid data	tvd		480		HSYNC	
Vsync pulse width	tvpw	1	2	66	HSYNC	
Vsync back porch	tvbp	5	5	67	HSYNC	
Vsync front porch	tvfp	5	43	67	HSYNC	
1 vertical field	tv	490	528	552	HSYNC	

Note:

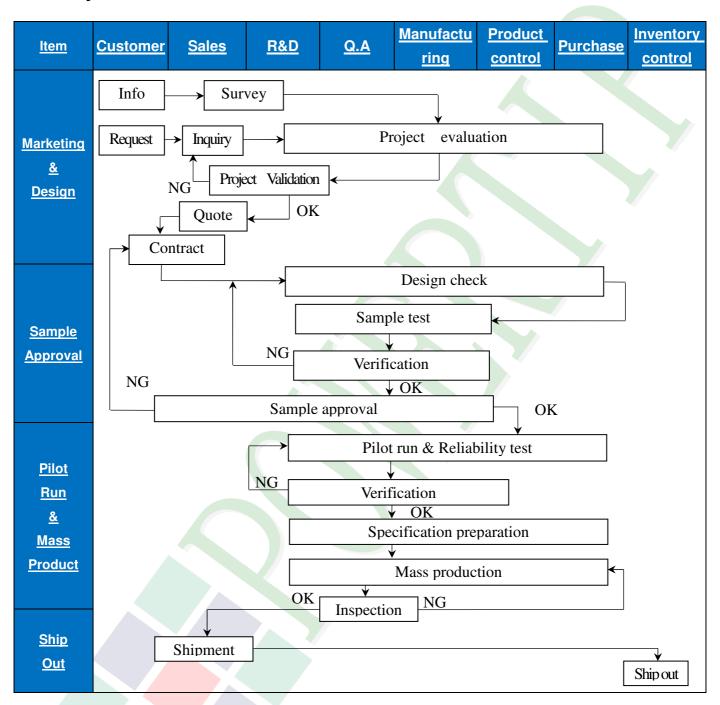
- (1) thd is same to Hactive, and tvd is same to Vactive in chapter 5.1.
- (2) DCLK frequency min/max value is base on frame rate 60 Hz
- (3) thbp+thpw+thfp≥56 DCLK, tvbp+tvpw+tvfp≥10



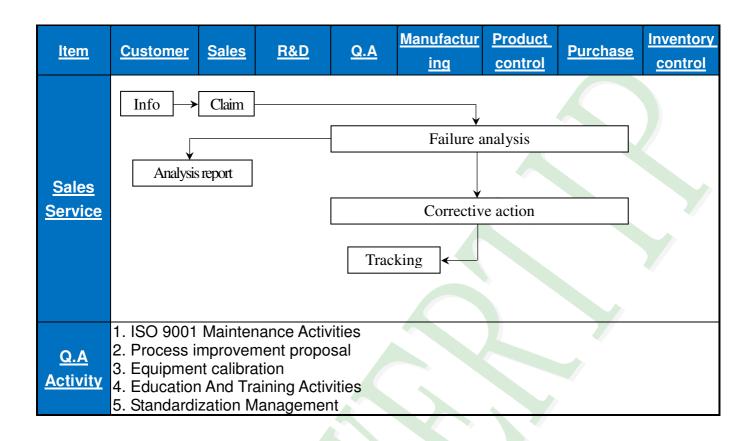


3. Quality Assurance System

3.1 Quality Assurance Flow Chart









3.2 Inspection Specification

♦Scope: The document shall be applied to TFT-LCD Module for 3. 5" −15" (Ver.B01).

♦ Inspection Standard: MIL-STD-105E Table Normal Inspection Single Sampling Level II.

◆Equipment: Gauge, MIL-STD, Powertip Tester, Sample

◆Defect Level: Major Defect AQL: 0. 4; Minor Defect AQL: 1. 5

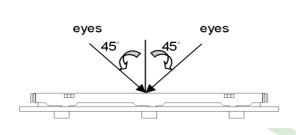
♦OUT Going Defect Level: Sampling

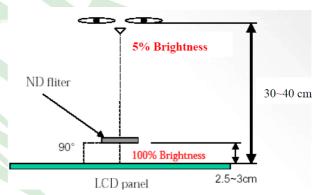
♦Standard of the product appearance test:

a. Manner of appearance test:

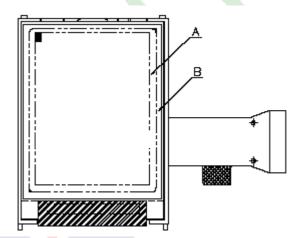
(1). The test best be under 20W×2 fluorescent light(about 300lux ~500lux) and distance of view must be at 30~40 cm.

(2). The test direction is base on about around 45° of vertical line.





(3). Definition of area.



A area: viewing area

B area: Outside of viewing area

(4). Standard of inspection: (Unit: mm)



◆Specification For TFT-LCD Module 3. 5" ~15":

<u>NO</u>	<u>Item</u>		<u>Criteri</u>	<u>on</u>	<u>Level</u>		
		1.1 The part number is inconsistent with work order of production.					
01	Product condition	1. 2 Mixed product types.					
		1. 3 Assembled in	n inverse direction.		Major		
02	Quantity	2.1 The quantit	2. 1 The quantity is inconsistent with work order of production.				
03	Outline dimension	3. 1 Product din diagram.	nension and struct	ure must conform to stru	ncture Major		
		4. 1 Missing line	character and icon		Major		
		4. 2 No function	or no display.		Major		
	Electrical Testing	4. 3 Display malfunction.					
04		4. 4 LCD viewing angle defect.					
		4. 5 Current consumption exceeds product specifications.					
				5% ND filter at 50% Gray angle of 90 degree.	y, Minor		
			<u>Item</u>	Acceptance (Q'ty)			
			Bright Dot	≤ 4			
	Dot defect	Dot	Dark Dot	≦ 5			
		<u>Defect</u>	Joint Dot	≦ 3			
0.5	(Bright dot,		Total	≦ 7	DAT:		
05	Dark dot)				Minor		
	On -display 5. 1 Inspection pattern: full white, full black, Red, Green and blue screens.						
			as dot defect if defe				
5. 3 The distance between two dot defect ≥ 5 mm.							
		5.4 Bright dot t	hat can not be seen	through 5% ND filter.			



♦Specification For TFT-LCD Module 3. 5" ~15":

NO	<u>Item</u>		<u>Criterion</u>					Level	
		6. 1 Rou	6. 1 Round type (Non-display or display):						
						A	(0)		
		Dimens		ion (diameter: Φ)		Acceptance (Q'ty) A area B area			
			$\Phi \leq 0$		25	Ignore			
	Black or white		0.25	< Φ ≤ 0.5	50	5	7		
	Dot, scratch,			$\Phi > 0$.50	0	lg	nore	
	contamination			Total		5			
	Round type								
		6. 2 Line	e type(No	on-display	or disp	lay):			
	$X \longrightarrow X$			Length			Accept	ance (Q'ty)	
00	<u>Y</u>	modi	ule size	(L)	W	<u>idth (W)</u>	A are		2.61
06	$\Phi = (x+y)/2$					$W \le 0.03$	Ignor	e	Minor
	(A 1 J) / Z			L ≤10.0	0.03	$<$ W ≤ 0.05	4		
	3		3.5" to less	L ≦5.0	0.05	<w 0.10<="" td="" ≤=""><td>2</td><td> Ignore</td><td></td></w>	2	Ignore	
	Line type	2	9"			W >	As		
	r ↓ W					0.10	round type		
	_ ~ ↑ "				Tota	1	5		
						$W \le 0.05$	Ignor	e	
				L ≤10.0	0.05	$<$ W \leq 0.10	5		
		9" t	to 15"			W >	As round	Ignore	
						0.10	type		
					Tota	l	5		
								× • · · · ·	
		<u>Dim</u>	ension (diameter: 🤇	<u>D)</u>	Accept A area	tance (C	<u>D'ty)</u> B area	
	D. 1			$\Phi \leq 0.25$		Ignore		<u>D arca</u>	
07	Polarizer Bubble			$\Phi \leq 0.50$		4			Minor
	Dubble			$\Phi \le 0.80$		1		Ignore	
				$\Phi > 0.80$		0			
			To	tal		5			



♦ Specification For TFT-LCD Module 3. 5″ ~15″:

NO	<u>Item</u>	<u>Criterion</u>				
<u>NO</u>	<u>Item</u>	Criterion	<u>Level</u>			
		X: The length of crack Z: The thickness of crack T: The thickness of glass X: The width of crack. W: terminal length A: LCD side length 8. 1 General glass chip: 8. 1. 1 Chip on panel surface and crack between panels:				
08	The crack of glass	Y [NG]	Minor			
		Seal width Z				
		$\underline{\mathbf{X}}$ $\underline{\mathbf{Y}}$ $\underline{\mathbf{Z}}$				
		$\leq \leq a$ Crack can't enter viewing area $\leq 1/2 t$				
		\leq a Crack can't exceed the half of SP width. 1/2 t < Z \leq 2 t				



◆Specification For TFT-LCD Module 3. 5″ ~15″:

<u>NO</u>	<u>Item</u>	<u>Criterion</u>	Level				
	Symbols: X: The length of crack Z: The thickness of crack t: The thickness of glass 8. 1. 2 Corner crack:						
		<u>X</u> <u>Y</u> <u>Z</u>					
		≤1/5 a Crack can't enter viewing area $ Z ≤ 1/2 t$					
		$\leq 1/5$ a Crack can't exceed the half of SP width. 1/2 t < Z ≤ 2 t					
			Minor				
08	The crack of glass	8.2 Protrusion over terminal:					
		8.2.1 Chip on electrode pad:					
		X X Z X X Y Z					
		W X					
		$\underline{\mathbf{X}}$ $\underline{\mathbf{Y}}$ $\underline{\mathbf{Z}}$					
		Front $\leq a \leq 1/2 \text{W} \leq t$					
		$\begin{array}{c ccccccccccccccccccccccccccccccccccc$					



♦ Specification For TFT-LCD Module 3. 5″ ~15″:

<u>NO</u>	<u>Item</u>	<u>Criterion</u>	Level
NO 08	The crack of glass	Symbols: X: The length of crack Z: The thickness of crack T: The thickness of glass 3: LCD side length 3: LCD side length 3: LCD side length 4: The chipped area touches the ITO terminal, over $\frac{1}{2}$ 3 of the ITO must remain and be inspected according to electrode terminal specifications. 8: 2: 3: Glass remain: X	Level



♦Specification For TFT-LCD Module 3. 5″ ~15″:

NO	<u>Item</u>	Criterion	(Ver.B01) Level
NU	<u>Itelli</u>	Criterion	Level
		9. 1 Backlight can't work normally.	Major
09	Backlight elements	9. 2 Backlight doesn't light or color is wrong.	Major
		9. 3 Illumination source flickers when lit.	Major
	General	10. 1 Pin type, quantity, dimension must match type in structure diagram.	Major
		10. 2 No short circuits in components on PCB or FPC.	Major
		10. 3 Parts on PCB or FPC must be: no wrong parts, missing parts or excess parts.	Major
10		10. 4 Product packaging must the same as specified on packaging specification sheet.	Minor
		10. 5 The folding and peeled off in polarizer are not acceptable.	Minor
		10. 6 The PCB or FPC between B/L assembled distance(PCB or FPC) is ≤ 1.5 mm.	Minor



4. Reliability Test

4.1 Reliability Test Condition

(Ver.B01)

NO.	TEST ITEM	TEST CO	<u>ONDITION</u>						
1	High Temperature Storage Test	Keep in 80 ±5℃ 240 hrs							
2	Low Temperature Storage Test	Keep in −30 ±5°C 240 hrs	Keep in −30 ±5°C 240 hrs						
3	High Temperature / High Humidity Storage Test	Keep in 60 ℃ / 90% R.H duration for 240 hrs (Excluding the polarizer)							
4	Temperature Cycling Storage Test	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$							
5	ESD Test	Air Discharge: Apply 2 KV with 5 times Discharge for each polarity +/- 1. Temperature ambiance: 15°C ~35°C 2. Humidity relative: 30% ~60% 3. Energy Storage Capacitance(Cs+Cd): 150pF±10% 4. Discharge Resistance(Rd): 330 Ω±10% 5. Discharge, mode of operation: Single Discharge (time between successive discharges at least 1 sec)							
6	Vibration Test (Packaged)	 (Tolerance if the output voltage indication: ±5%) Sine wave 10~55 Hz frequency (1 min/sweep) The amplitude of vibration: 1.5 mm Each direction (X, Y, Z) duration for 2 hrs 							
7	Drop Test (Packaged)	Packing Weight (Kg	122 76 61 46						

©Result Evaluation Criteria:

Under the display quality test conditions with normal operations with normal operation state. Do not change these conditions as such changes may affect practical display function.

(Normal operation state)
Temperature: +20~30°C
Humidity: 50~70%

Atmospheric pressure: 86~106Kpa



5. Precaution Relating Product Handling

5.1 Safety

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 Handling

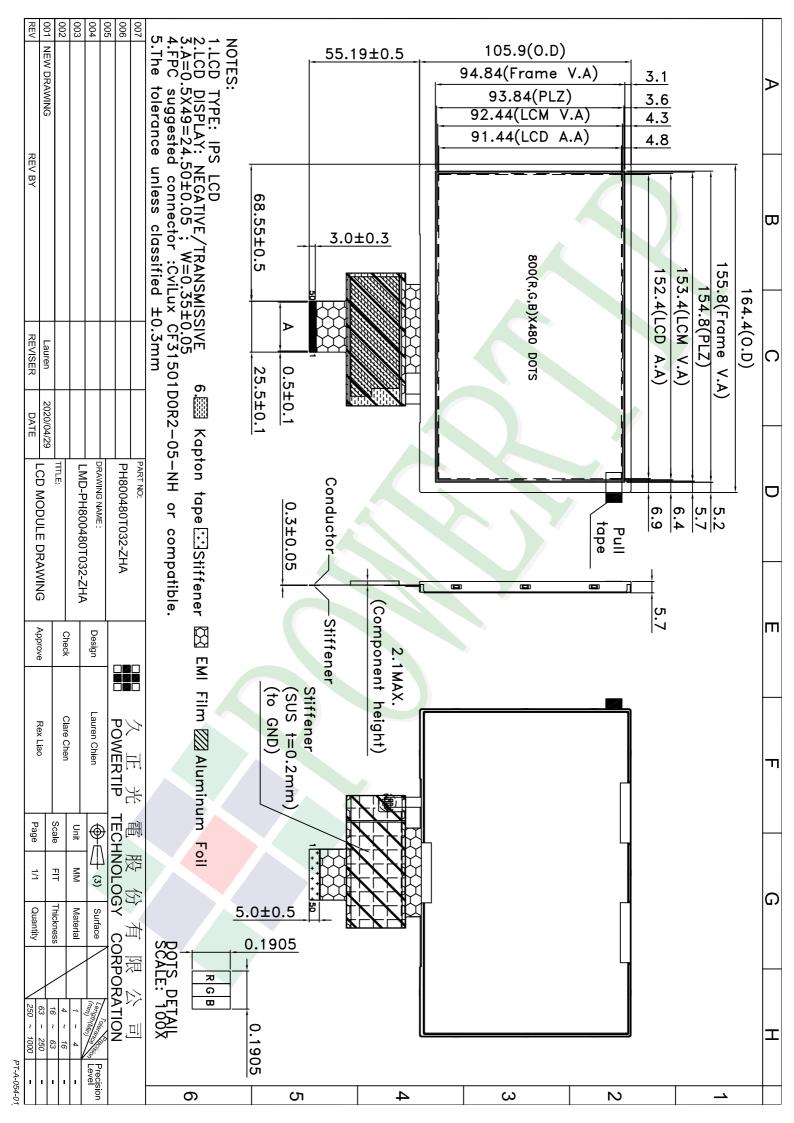
- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So, please handle it very carefully, do not touch, push or rub the exposed polarizing with anything harder than an HB pencil lead (glass, tweezers, etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is $320 \pm 10^{\circ}$ C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM
- 5.2.10 Caution! (LCM products with Capacitive Touch Panel)
 Strong EMI-sources such as switch-mode power supplies (SMPS) can lead to touch malfunction (e.g. ghost-touches).
 - Therefore, the touch needs to be thoroughly tested inside the target application.
- 5.2.11 Caution: Continuously displaying same static image will result in high possibility of image sticking/image burn-in effect due to TFT panel characteristic.

5.3 Storage

- 5.3.1 Store the panel or module in a dark place where the temperature is 25°C ± 5°C and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

5.4 Terms of Warranty

- 5.4.1 Applicable warrant period
 The period is within thirteen months since the date of shipping out under normal using and storage conditions.
- 5.4.2 Unaccepted responsibility
 - This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.



Ver	001	ICME		Approve	Check	Contact
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Doc	uments NO. PKG-PH800480T032-ZF	_	ng Specification	ns Rex	Clare	Lauren
	'	· · · · · · · · · · · · · · · · · · ·	r Tray)			
1.包	D裝材料規格表 (Packaging Ma	aterial): (per carton)				
No.	Item	Model	Dimensions (mm)	1Pcs Weight	Quantity	Total Weight
1	成品 (LCM)	PH800480T032-ZHA	164.4X105.9X5.7	0.1853	48	8.8944
2	多層薄膜(1)POF	OTFILM0BA03ABA	19"X350X0.015		6	
3	TRAY盤(2)Tray	TYSG000000490	352 X 260 X 15.8	0.099	30	2.97
4	舒美墊(3) EPE	FOAM00000047	350 X 255 X 5	0.011	6	0.066
5	舒美墊(4) EPE	FOAM00000091	160X 118 X 1	0.0005	96	0.048
6	内盒(5)Product Box	BX36627063ABBA	383 X 270 X 66	0.182	6	1.092
7	保利龍板(6)Polylon board	OTPLB00PL08ABA	550 X 393 X 20	0.0284	2	0.0568
8	外紙箱(7)Carton	BX57041027CCBA	570 X 410 X 265	1.0	1	1.0
9						
	整箱總重量 (Total LCD Weight		.0%			
	箱數量規格表 (Packaging Specifi					
	CM quantity per box : no per tray otal LCM quantity in carton : quant	ity per box 2	x no of tray x no of boxes	4	= 8 = 48	
(2)1	Otal ECIVI quantity in Carton , quant			6	= 48	
		(4)EPE (1)多層	(6.1)	保利龍板		
		PC	Poly	ylon board		
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		(2)TRAY 盤				
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	空盤	Tiuj		\rightarrow		
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				(6)保利龍板 ^ Polylon board		
	II			rolyloli boald		
Put p	products into the tray	(3)EPE			\bigvee	
LCI	M —	(5)EIE	<u> </u>		\sim	
		T				
		(5)內盒 /				7
		Product Box				
Tray	stacking					
٨			(7)\$1	紙箱		
<u>A</u>				rton		
В						
		特記事	頁 (REMARK)			
			,			
	A <u>斜角</u> Detail 1	В				
`\						
	Ti	ray 2				
	_{圓角} / _{Ti}	ray 1				
	AY盤相疊時,需旋轉180度,請詳見B視					
	te tray 180 degrees and place on top of s ck the tray stack using Fig. B.	таск.				
	way comen wound 1 15. D.					





ALL TECHNOLOGIES. ALL COMPETENCIES. ONE SPECIALIST.



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More information and worldwide locations can be found at