DATA MODUL

INNOLUX

Specification

S366AJ1-LE1

36.6" - 1920 x 290 – LVDS

Spec Revision: 2.3
Revision Date: 15.11.2024

Note: This specification is subject to change without prior notice

Passion Displayed

□ Tentative Specification
□ Preliminary Specification
■ Approval Specification

MODEL NO.: S366AJ1

SUFFIX: LE1

Revision : C3
Customer :

APPROVED BY SIGNATURE

Name / Title _____ _____
Note

_____
Please return 1 copy for your confirmation with your signature and comments.

Approved By	Checked By	Prepared By
ChihFan Ting	John.Hsieh	CS Tsai

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REVISION HISTORY

Version	Date	Page(New)	Section	Description
Ver. 2.0	Mar.20, 2023	ALL	ALL	Approval Spec was first issued.
Ver. 2.1	Sep.19, 2023	6	1.4	Modify Weight typ.2947→2850g
		7	2.1	Modify ABSOLUTE RATINGS OF ENVIRONMENT
		15	4.1	Modify Pin 12~Pin 39
		31	7.2	Modify [1.1] and [1.2]
				Delete [2.1] (* The moving picture can be allowed for 24 hours a day)
		33	7.5	Add 7.5 Precautions for Strong Light Exposure
Ver.2.2	Dec.18, 2023	36~37	9.2	Modify 9.2 PACKAGING METHOD(AL bag or anti bag)
Ver.2.3	Nov.15, 2024		2.1	Update Note[3]
		7	2.3.2	2.3.2 BACKLIGHT CONVERTER UNIT
		11	3.2.1	CONVERTER CHARACTERISTICS
		12	3.2.1	Note(6) drawing
		21	5.1	add Horizontal frequency
		24	5.2	modify power squence
		31	7.2	Update Note[1],[2]
		36	9.2	Deleate Anti-bag package

1. GENERAL DESCRIPTION

1.1 OVERVIEW

S366AJ1-LE1 is a 36.6" TFT Liquid Crystal Display PID module with LED Backlight unit and 2ch-LVDS interface. This module supports 1920 x 290 Full HDTV format and can display 16.7M colors (8-bit). Special material applied into this model :

Liquid crystal：Advanced wide temperature LC (-40 ºC~110 ºC)

1.2 FEATURES

- High brightness 1000 nits
- High contrast ratio 5000:1
- Fast response time Gray to gray average 11 ms
- High color saturation NTSC 72%
- Half HDTV (1920 x 290 pixels) resolution, true HDTV format
- DE (Data Enable) only mode
- LVDS (Low Voltage Differential Signaling) interface
- Optimized response time for 50Hz/60Hz frame rate
- Ultra wide viewing angle : Super MVA technology
- Viewing Angle : 178(H)/178(V) (CR ≥ 10) VA Technology
- RoHs compliance
- T-con input frame rate: 50Hz/60Hz, output frame rate: 50Hz/60Hz

1.3 GENERAL SPECIFICATIONS

Item	Specification	Unit	Note
Active Area	919.296(H) x 138.852(V) (36.6" diagonal)	mm	(1)
Bezel Opening Area	923.3(H) x 142.8(V)	mm	
Driver Element	a-si TFT active matrix	-	-
Pixel Number	1920 x R.G.B. x290	pixel	-
Pixel Pitch (Sub Pixel)	0.1596 (H) x 0.4788 (V)	mm	-
Pixel Arrangement	RGB vertical stripe	-	-
Display Colors	16.7M colors (8-bit)	color	-
Display Operation Mode	Transmissive mode / Normally black	-	-
Surface Treatment	Anti-Glare coating(Haze 1%) , Hardness 3H	-	(2)
Rotation Function	Unachievable		(3)
Display Orientation	Portrait/Landscape Enabled		(4)

Note (1) Please refer to the attached drawings in chapter 9 for more information about the front and back outlines.

Note (2) The spec. of the surface treatment is temporarily for this phase. INX reserves the rights to change this feature.

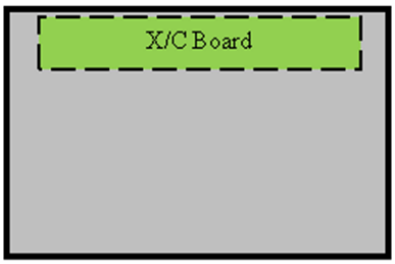
Note (3) Rotate Function refers to LCD display could be able to rotate. This function does not work in this model.

Note (4)

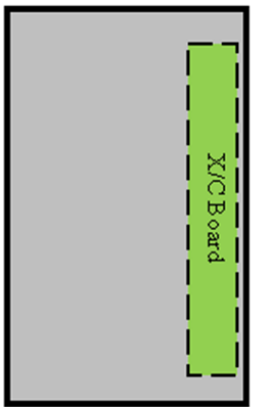
a. Landscape Mode: The default placement is X/C Board Side on the upper side and the image is shown upright via viewing from the front.

b. Portrait Mode: The default placement is that X/C Board Side has to be placed on the right side via viewing from the front.

Landscape (Front view)



Portrait (Front view)



1.4 MECHANICAL SPECIFICATIONS

Item		Min.	Typ.	Max.	Unit	Note
Module Size	Horizontal (H)	950.3	951.3	952.3	mm	(1)
	Vertical (V)	169.8	170.8	171.8	mm	(1)
	Depth (D)	10.4	11.4	12.4	mm	(2)
		27	28	29		(3)
Weight		2707	2850	2993	g	

Note (1) Please refer to the attached drawings for more information of front and back outline dimensions.

Note (2) Module Depth is between bezel to Rear.

Note (3) Module Depth is between bezel to Converter cover

2. ABSOLUTE MAXIMUM RATINGS

2.1 ABSOLUTE RATINGS OF ENVIRONMENT

Item	Symbol	Value		Unit	Note
		Min.	Max.		
Storage Temperature	T_{ST}	-20	+60	°C	(1),(3),(4)
Operating Ambient Temperature	T_{OP}	-20	+60	°C	(1), (2), (3), (4)
Panel Surface Temperature	T_{PS}	-	+70	°C	(2)

Note (1) Temperature and relative humidity range is shown in the figure below.

(a) 90 %RH Max. (Ta ≦ 40 °C).

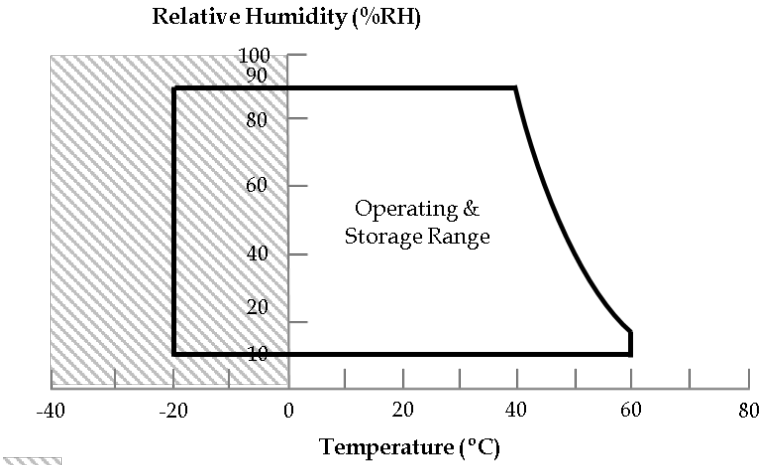
(b) Wet-bulb temperature should be 39 °C Max.

(c) No condensation.

Note (2) Surface temperature is measured at 60 °C Dry condition.

Note (3) The specified temperature range is determined by the design of the product module. When integrated with the customer's system, it is imperative to control environmental conditions within this prescribed range; otherwise, the operational capability of the product cannot be guaranteed.

Note (4) Response time depends on the temperature. (In lower temperature, it becomes longer.)



: In natural enviroments, the humidity is negligible in temperature below 0℃

2.2 PACKAGE STORAGE

When storing modules as spares for a long time, the following precaution is necessary.

(a) Do not leave the module in high temperature, and high humidity for a long time, It is highly recommended to store the module with temperature from 0 to 35 ºC at normal humidity without condensation.

(b) The module shall be stored in dark place. Do not store the TFT-LCD module in direct sunlight or fluorescent light.

2.3 ELECTRICAL ABSOLUTE RATINGS

2.3.1 TFT LCD MODULE

Item	Symbol	Value		Unit	Note
		Min.	Max.		
Power Supply Voltage	VCC	-0.3	13.5	V	(1)
Logic Input Voltage	VIN	-0.3	3.6	V	

Note (1) Permanent damage to the device may occur if maximum values are exceeded. Function operation should be restricted to the conditions described under Normal Operating Conditions

2.3.2 BACKLIGHT CONVERTER UNIT

Item	Symbol	Value		Unit	Note
		Min.	Max.		
Light Bar Voltage	VW	–	41.5	VRMS	
Converter Input Voltage	VBL	0	26.4	V	(1)
Control Signal Level	–	-0.3	6	V	(1), (3)

Note (1) Permanent damage to the device may occur if maximum values are exceeded. Functional operation should be restricted to the conditions described under normal operating conditions.

Note (2) No moisture condensation or freezing.

Note (3) The control signals include On/Off Control and External PWM Control.

3. ELECTRICAL CHARACTERISTICS

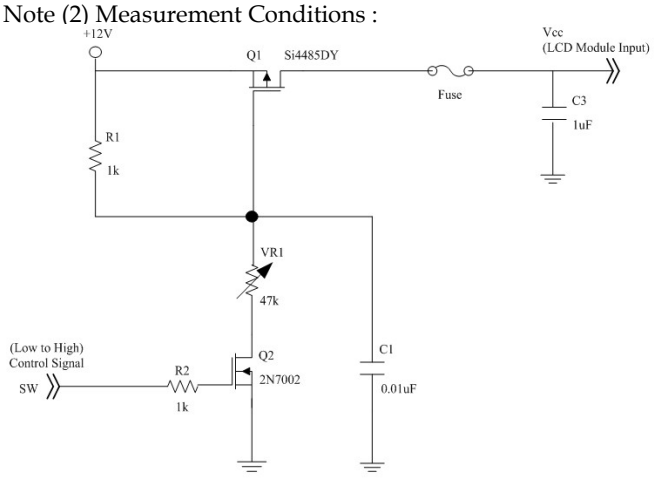
3.1 TFT LCD MODULE

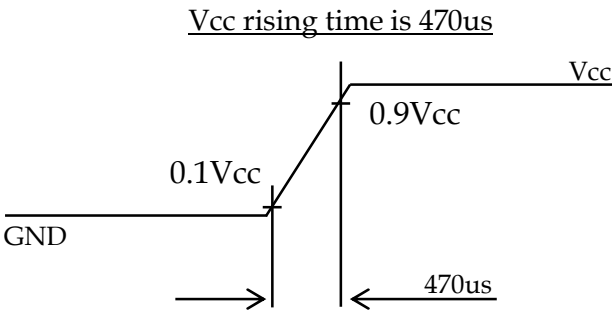
(Ta = 25 ± 2 °C)

Parameter		Symbol	Value			Unit	Note
			Min.	Typ.	Max.		
Power Supply Voltage		V_{CC}	10.8	12	13.2	V	(1)
Rush Current		I_{RUSH}	—	—	2.22	A	(2)
Power Consumption	White Pattern	P_T	—	3.59	3.95	W	(3)
	Black Pattern	P_T	—	3.38	3.71	W	
	Horizontal Stripe	P_T	—	3.59	3.95	W	
Power Supply Current	White Pattern	—	—	0.31	0.38	A	
	Black Pattern	—	—	0.29	0.35	A	
	Horizontal Stripe	—	—	0.31	0.38	A	
LVDS interface	Differential Input High Threshold Voltage	V_{LVTH}	—	—	+100	mV	(4)
	Differential Input Low Threshold Voltage	V_{LVTL}	-100	—	—	mV	
	Common Input Voltage	V_{CM}	1.0	1.2	1.4	V	
	Differential input voltage (single-end)	$\|V_{ID}\|$	100	—	600	mV	
	Terminating Resistor	R_T	—	100	—	ohm	
CMIS interface	Input High Threshold Voltage	V_{IH}	2.7	—	3.3	V	
	Input Low Threshold Voltage	V_{IL}	0	—	0.7	V	

Note (1) The module should be always operated within the above ranges. The ripple voltage should be controlled under 10% of Vcc (Typ.)

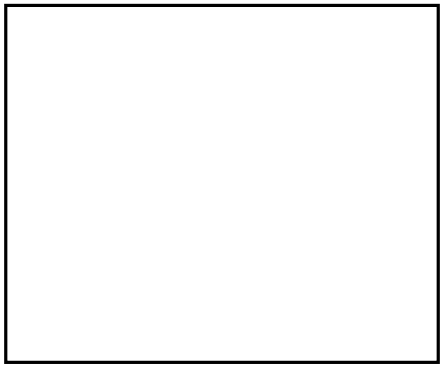
Note (2) Measurement Conditions :





Note (3) The specified power supply current is under the conditions at Vcc = 12 V, Ta = 25 ± 2 °C, fv = 60 Hz, whereas a power dissipation check pattern below is displayed.

a. White Pattern

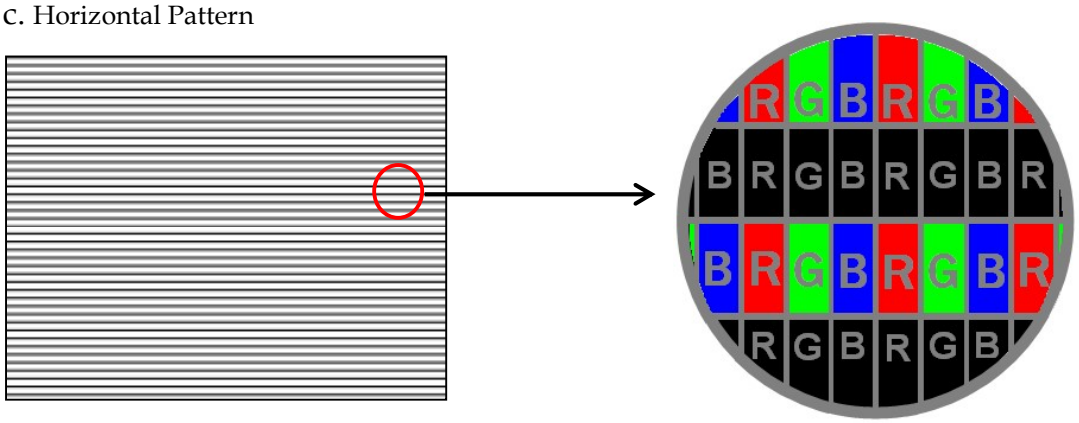


Active Area

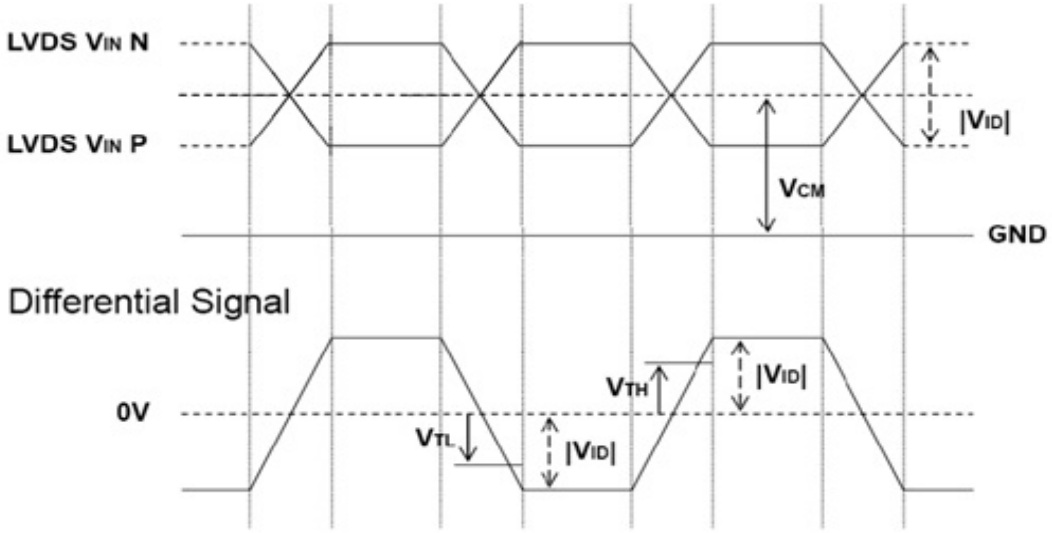
b. Black Pattern

Active Area

c. Horizontal Pattern



Note (4) The LVDS input characteristics is shown as below : The position of measurement is TCON LVDS input pin. The differential voltage must be higher than VTH and lower than VTL to ensure that the receiver indicates a valid logic state at its output.



3.2 BACKLIGHT CONVERTER UNIT

3.2.1 CONVERTER CHARACTERISTICS

Parameter	Symbol	Value			Unit	Note
		Min.	Typ.	Max.		
Power Consumption	P_{BL}	-	31.0	36.0	W	(1), (2)
Converter Input Voltage	VBL	22.8	24	25.2	VDC	
Converter Input Current	I_{BL}	-	1.3	1.5	A	Non Dimming
Input Inrush Current	I_R	-	-	5.54	Apeak	V_{BL}=22.8V, (3)
Dimming Frequency	FB	150	-	170	Hz	
Dimming Duty Ratio	DDR	5	-	100	%	(4)
Life Time	-	50,000		-	Hrs	(5)

Note (1) The power supply capacity should be higher than the total converter power consumption PBL. Since the pulse width modulation (PWM) mode was applied for backlight dimming, the driving current changed as PWM duty on and off. The transient response of power supply should be considered for the changing loading when converter dimming.

Note (2) The measurement condition of Max. value is based on 36.6" backlight unit under input voltage 24V, at 2D Mode and lighting 1 hour later.

Note (3) For input inrush current measure, the VBL rising time from 10% to 90% is about 20ms

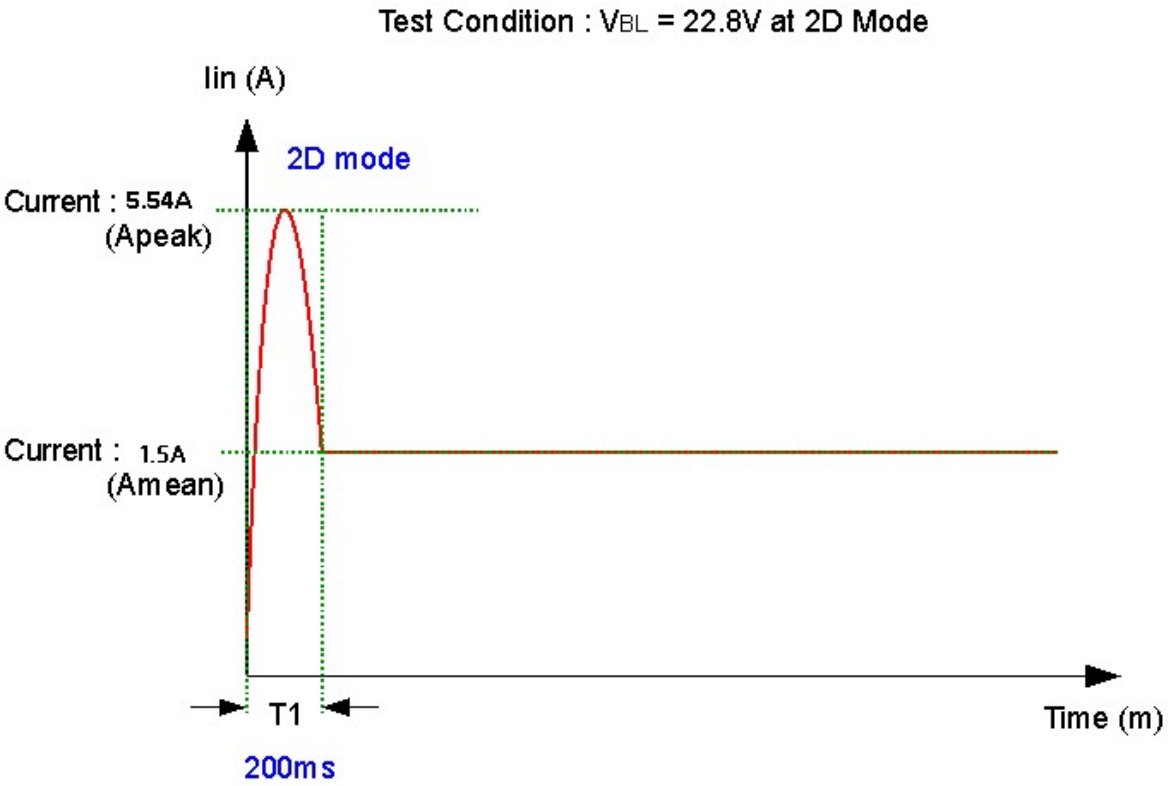
Note (3) For input inrush current measure, the VBL rising time from 10% to 90% is about 20ms.

Note (4) EPWM signal have to input available duty range. 5% minimum duty ratio is only valid for electrical operation.

Note (5) The lifetime is defined as the time which luminance of the LED decays to 50% compared to the initial value,

Operating condition: Continuous operating at Ta = 25±2℃

Note (6) Below diagram is only for power supply design reference.



Note (7) The lifetime is defined as the time which luminance of the LED decays to 50% compared to the initial value,

Operating condition: Continuous operating at Ta = 25±2℃

3.2.2 CONVERTER INTERFACE CHARACTERISTICS

Parameter		Symbol	Test Condition	Value			Unit	Note	
				Min.	Typ.	Max.			
On/Off Control Voltage	ON	VBLON	—	2.0	—	5.0	V		
	OFF		—	0	—	0.8	V		
External PWM Control Voltage	HI	VEPWM	—	2.0	—	5.0	V	Duty on	(5)
	LO		—	0	—	0.8	V	Duty off	
Error Signal		ERR	—	—	—	—	—	Abnormal: Open collector	
VBL Rising Time		Tr1	—	20	—	—	ms	10%-90%V_{BL}	
Control Signal Rising Time		Tr	—	—	—	100	ms		
Control Signal Falling Time		Tf	—	—	—	100	ms		
PWM Signal Rising Time		TPWMR	—	—	—	50	us		
PWM Signal Falling Time		TPWMF	—	—	—	50	us		
Input Impedance		Rin	—	1	—	—	MΩ		
PWM Delay Time		TPWM	—	100	—	—	ms		
BLON Delay Time		T_{on}	—	300	—	—	ms		
		T_{on1}	—	300	—	—	ms		
BLON Off Time		Toff	—	300	—	—	ms		

Note (1) The Dimming signal should be valid before backlight turns on by BLON signal. It is inhibited to change the external PWM signal during backlight turn on period.

Note (2) The power sequence and control signal timing are shown in the Fig.1. For a certain reason, the converter has a possibility to be damaged with wrong power sequence and control signal timing.

Note (3) While system is turned ON or OFF, the power sequences must follow as below descriptions:

Turn ON sequence: VBL → PWM signal → BLON

Turn OFF sequence: BLOFF → PWM signal → VBL

Note (4) When converter protective function is triggered, ERR will output open collector status. (Fig.2)

Note (5) The EPWM interface that inserts a pull up resistor to 5V in Max Duty (100%), please refers to Fig.3.

Note (6) EPWM signal have to input available frequency range.

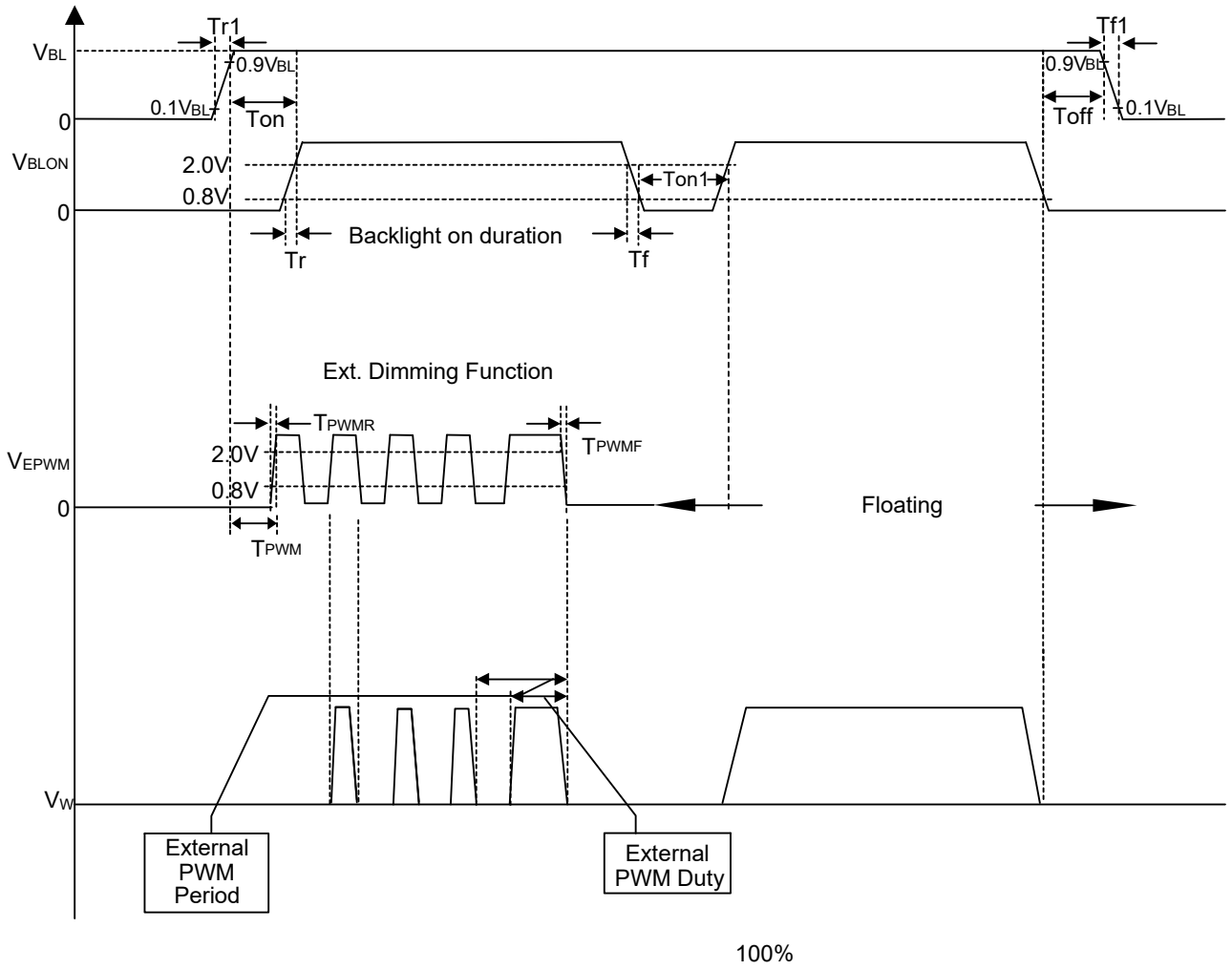


Fig. 1

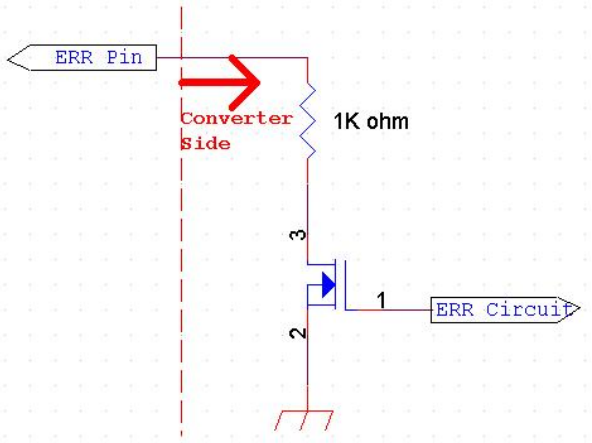


Fig. 2

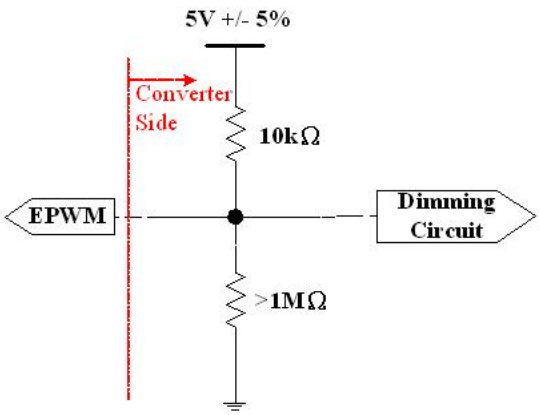


Fig. 3

4. INTERFACE PIN CONNECTION

4.1 TFT LCD MODULE

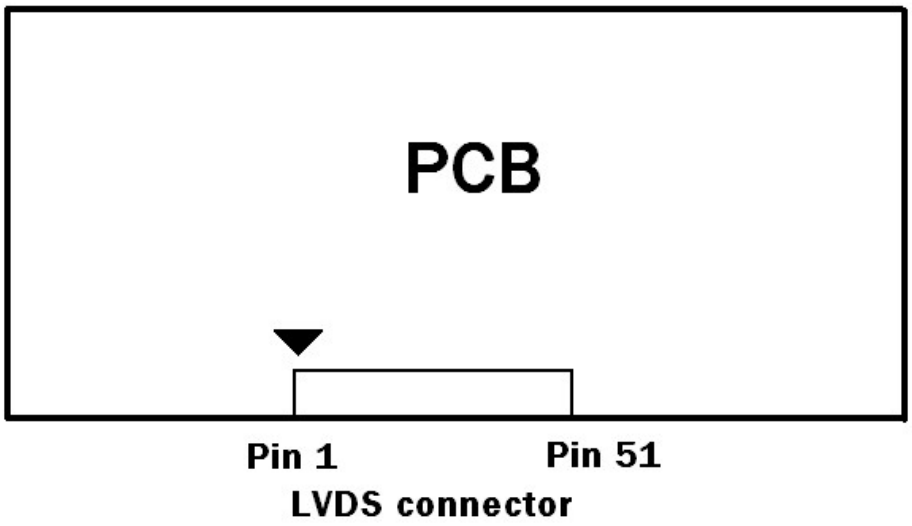
CNX11 Connector Pin Assignment: [187059-51221(P-Two), WF23-402-5133(FCN)]

Matting connector : [FI-RE51HL (JAE)]

Pin	Name	Description	Note
1	N.C.	No Connection	(2)
2	N.C.	No Connection	
3	N.C.	No Connection	
4	N.C.	No Connection	
5	N.C.	No Connection	
6	N.C.	No Connection	
7	SELLVDS	LVDS data format Selection	(3), (4)
8	N.C.	No Connection	(2)
9	N.C	No Connection	(2)
10	N.C.	No Connection	(2)
11	GND	Ground	
12	ERX0-	Even pixel Negative LVDS differential data input. Channel 0	(5)
13	ERX0+	Even pixel Positive LVDS differential data input. Channel 0	
14	ERX1-	Even pixel Negative LVDS differential data input. Channel 1	
15	ERX1+	Even pixel Positive LVDS differential data input. Channel 1	
16	ERX2-	Even pixel Negative LVDS differential data input. Channel 2	
17	ERX2+	Even pixel Positive LVDS differential data input. Channel 2	
18	GND	Ground	
19	ECLK-	Even pixel Negative LVDS differential clock input	(5)
20	ECLK+	Even pixel Positive LVDS differential clock input	
21	GND	Ground	
22	ERX3-	Even pixel Negative LVDS differential data input. Channel 3	(5)
23	ERX3+	Even pixel Positive LVDS differential data input. Channel 3	
24	N.C.	No Connection	(2)
25	N.C.	No Connection	
26	N.C.	No Connection	
27	N.C.	No Connection	

28	ORX0-	Odd pixel Negative LVDS differential data input. Channel 0	(5)
29	ORX0+	Odd pixel Positive LVDS differential data input. Channel 0	
30	ORX1-	Odd pixel Negative LVDS differential data input. Channel 1	
31	ORX1+	Odd pixel Positive LVDS differential data input. Channel 1	
32	ORX2-	Odd pixel Negative LVDS differential data input. Channel 2	
33	ORX2+	Odd pixel Positive LVDS differential data input. Channel 2	
34	GND	Ground	
35	OCLK-	Odd pixel Negative LVDS differential clock input.	(5)
36	OCLK+	Odd pixel Positive LVDS differential clock input.	
37	GND	Ground	
38	ORX3-	Odd pixel Negative LVDS differential data input. Channel 3	(5)
39	ORX3+	Odd pixel Positive LVDS differential data input. Channel 3	
40	N.C.	No Connection	(2)
41	N.C.	No Connection	
42	N.C.	No Connection	
43	N.C.	No Connection	
44	GND	Ground	
45	GND	Ground	
46	GND	Ground	
47	N.C.	No Connection	(2)
48	VCC	Power input (+12V)	
49	VCC	Power input (+12V)	
50	VCC	Power input (+12V)	
51	VCC	Power input (+12V)	

Note (1) LVDS connector pin order is defined as below.



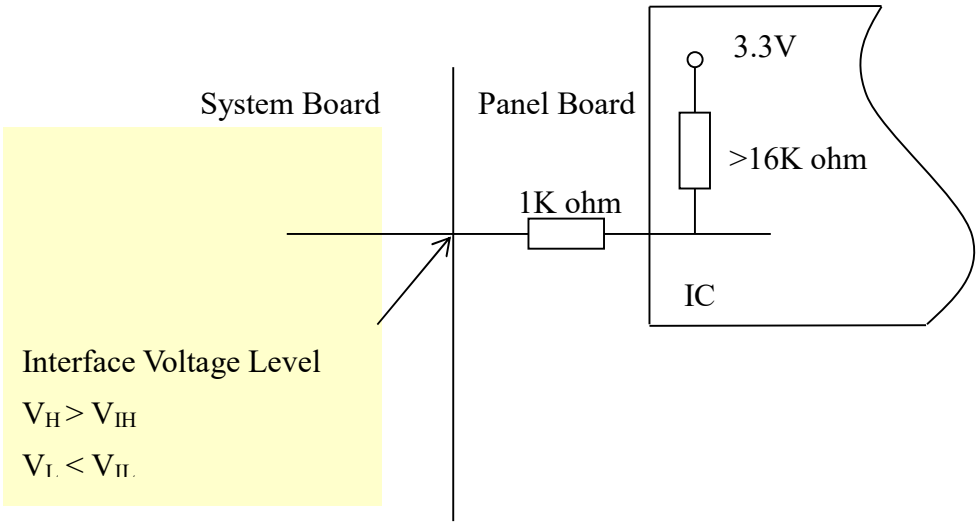
Note (2) Reserved for internal use. Please leave it open.

Note (3)

SELLVDS	Mode
L	JEIDA
H(default)	VESA

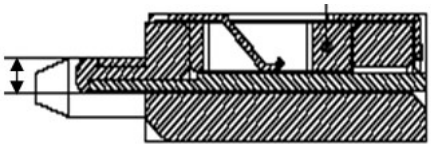
L : Connect to GND, H : Connect to Open or +3.3V

Note (4) Interface optional pin has internal scheme as following diagram. Customer should keep the interface voltage level requirement which including panel board loading as below.



Note (5) Two pixel data send into the module for every clock cycle. The first pixel of the frame is odd pixel and the second pixel is even pixel.

Note (6) LVDS connector mating dimension range request is 0.93mm~1.0mm as below



4.2 BACKLIGHT UNIT

4.2.1 LIGHT BAR UNIT

The pin configuration for the housing and lead wire is shown in the table below.

CNL01 Connector Pin Assignment: [WTB,CI1408M1VL0-NH (CviLux)]

L-side

Pin №	Symbol	Feature
1	N1	Negative of LED String
2	N2	
3	N3	
4	N4	
5	N5	
6	N6	
7	VLED+	Positive of LED String
8	VLED+	

R-side

Pin №	Symbol	Feature
1	N1	Negative of LED String
2	N2	
3	N3	
4	N4	
5	N5	
6	N6	
7	VLED+	Positive of LED String
8	VLED+	

4.2.2 CONVERTER UNIT

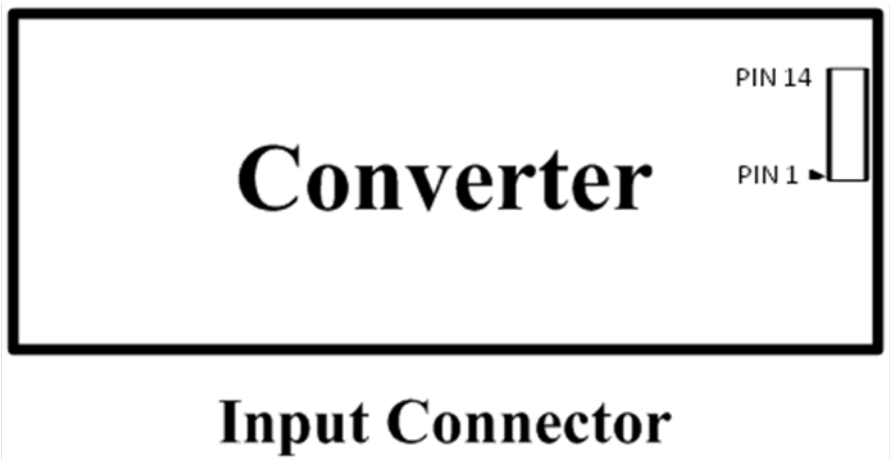
CNV04 Connector Pin Assignment: [CI0114M1HR0-LA-NH (CvilLux) , JH2-D4-143N (FCN)]
Matting connector : [PHR-14(JST)]

Pin No.	Symbol	Feature
1	VBL	+24V
2		
3		
4		
5		
6	GND	GND
7		
8		
9		
10		
11	ERR	Normal (GND) ; Abnormal (Open collector)
12	BLON	BL ON/OFF
13	NC	NC
14	E_PWM	External PWM Control

Note (1) The pin14 must be connected to EPWM simultaneously.

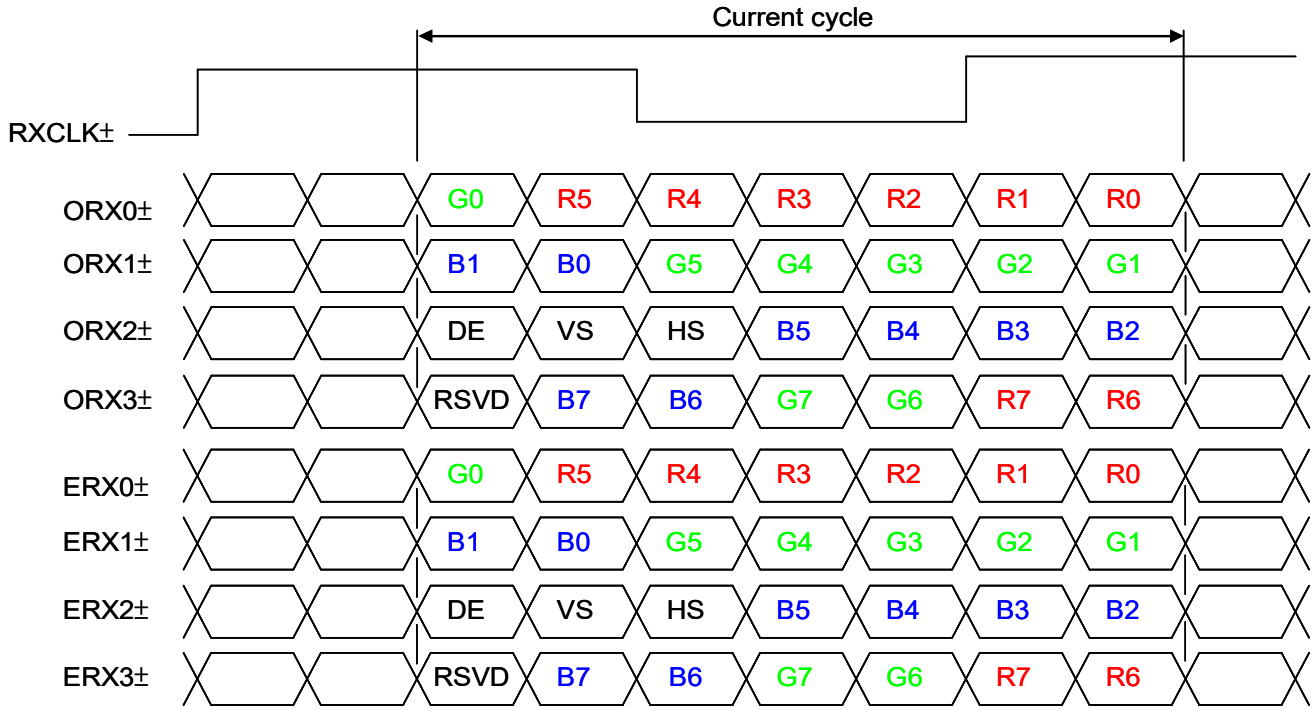
Note (2) If Pin14 is open, E_PWM is 100% duty.

Note (3) Input connector pin order defined as follows

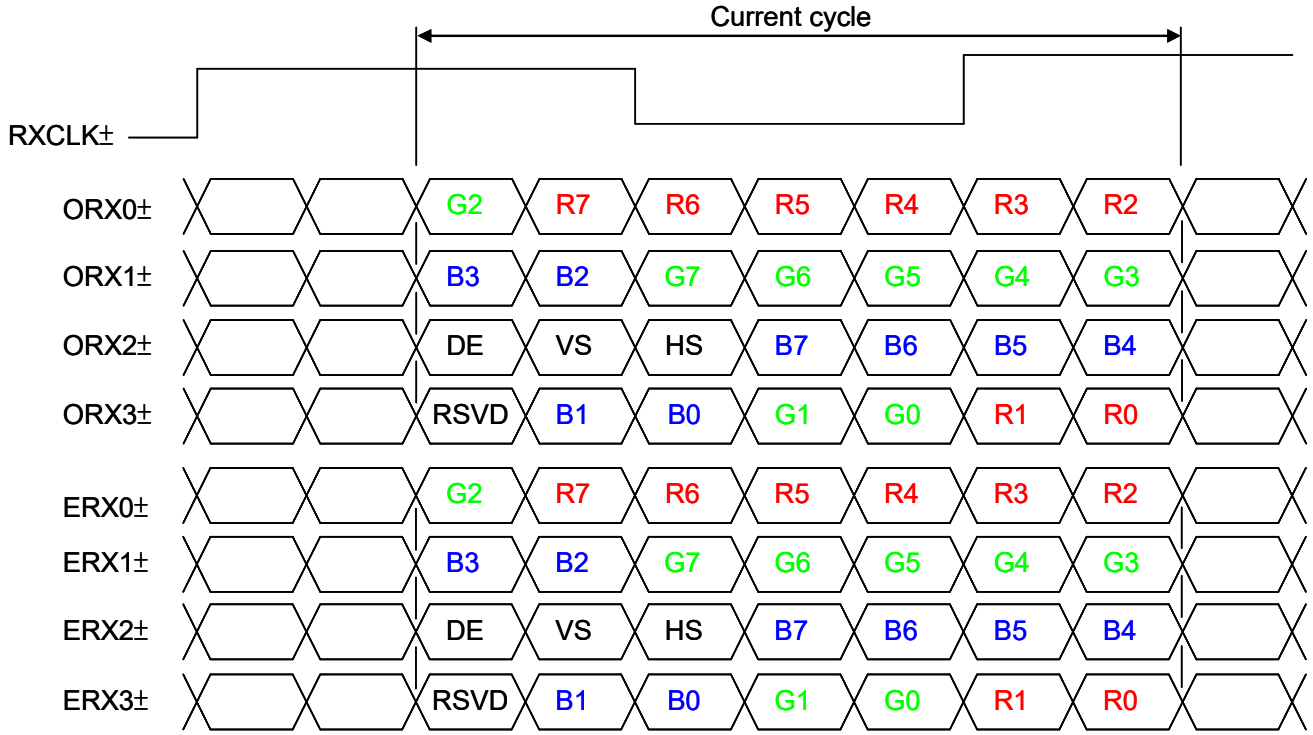


4.3 LVDS INTERFACE

VESA Format : SELLVDS = H or Open



JEIDA Format : SELLVDS = L



R0~R7	Pixel R Data (7; MSB, 0; LSB)	DE	Data enable signal
G0~G7	Pixel G Data (7; MSB, 0; LSB)	DCLK	Data clock signal
B0~B7	Pixel B Data (7; MSB, 0; LSB)		

Note (1) RSVD (reserved) pins on the transmitter shall be "H" or "L".

4.4 COLOR DATA INPUT ASSIGNMENT

The brightness of each primary color (red, green and blue) is based on the 8-bit gray scale data input for the color. The higher the binary input, the brighter the color. The table below provides the assignment of the color versus data input.

Color		Data Signal																							
		Red								Green								Blue							
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale Of Red	Red (0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (2)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red (253)	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale Of Green	Green (0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	Green (2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green (253)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	Green (254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green (255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Gray Scale Of Blue	Blue (0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue (1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue (2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue (253)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	Blue (254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue (255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

Note (1) 0: Low Level Voltage, 1: High Level Voltage

5. INTERFACE TIMING

5.1 INPUT SIGNAL TIMING SPECIFICATIONS

The input signal timing specifications are shown as the following table and timing diagram.

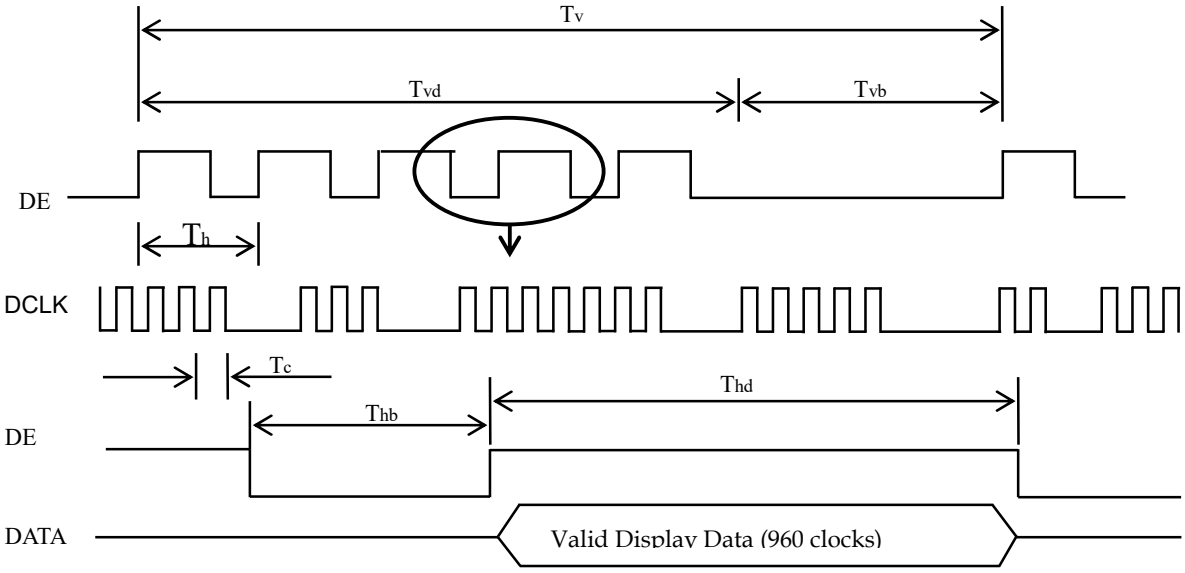
Signal	Item	Symbol	Min.	Typ.	Max.	Unit	Note
LVDS Receiver Clock	Frequency	F_{clkin} (=1/TC)	60	74.25	80	MHz	
	Input cycle to cycle jitter	T_{rcl}	-	—	200	ps	(3)
	Spread spectrum modulation range	F_{clkin_mod}	F_{clkin}-2%	—	F_{clkin}+2%	MHz	(4)
	Spread spectrum modulation frequency	F_{SSM}	—	—	200	KHz	
LVDS Receiver Data	Receiver Skew Margin	T_{RSKM}	-400	—	400	ps	(5)
Frame Rate		F_{r5}	47	50	53	Hz	
		F_{r6}	57	60	63	Hz	
Horizontal Frequency		Fh	45.3	67.5	74.2	KHz	
Vertical Active Display Term	Total	Tv	1100	1125	1480	Th	Tv=Tvd+Tvb
	Display	Tvd	1080	1080	1080	Th	(6)
	Blank	Tvb	20	45	400	Th	—
Horizontal Active Display Term	Total	Th	1030	1100	1325	Tc	Th=Thd+Thb
	Display	Thd	960	960	960	Tc	—
	Blank	Thb	70	140	365	Tc	—

Note (1) Please make sure the range of frame rate has follow the below equation：

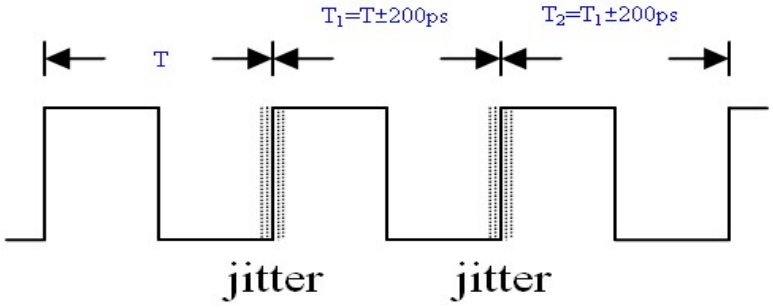
$$Fclkin(max) \geqq Fr6 \times Tv \times Th$$

$$Fr5 \times Tv \times Th \geqq Fclkin(min)$$

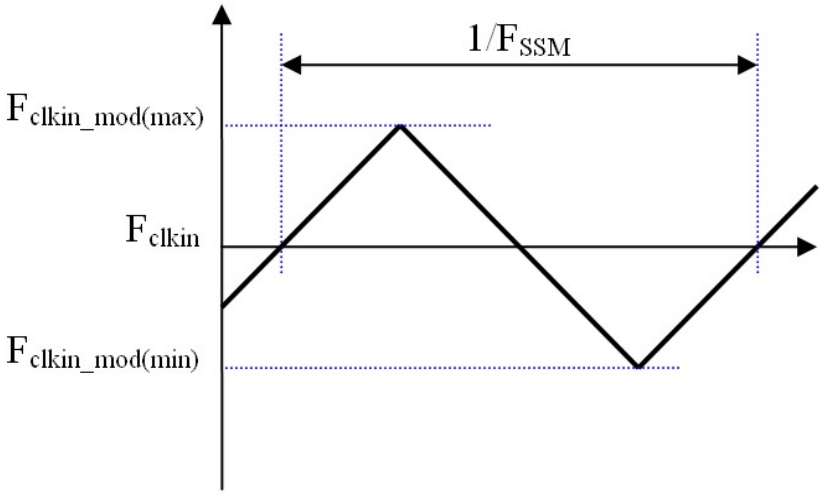
Note (2) This module is operated in DE only mode and please follow the input signal timing diagram as below :



Note (3) The input clock cycle-to-cycle jitter is defined as below figures. Trcl = | T_1 – T |

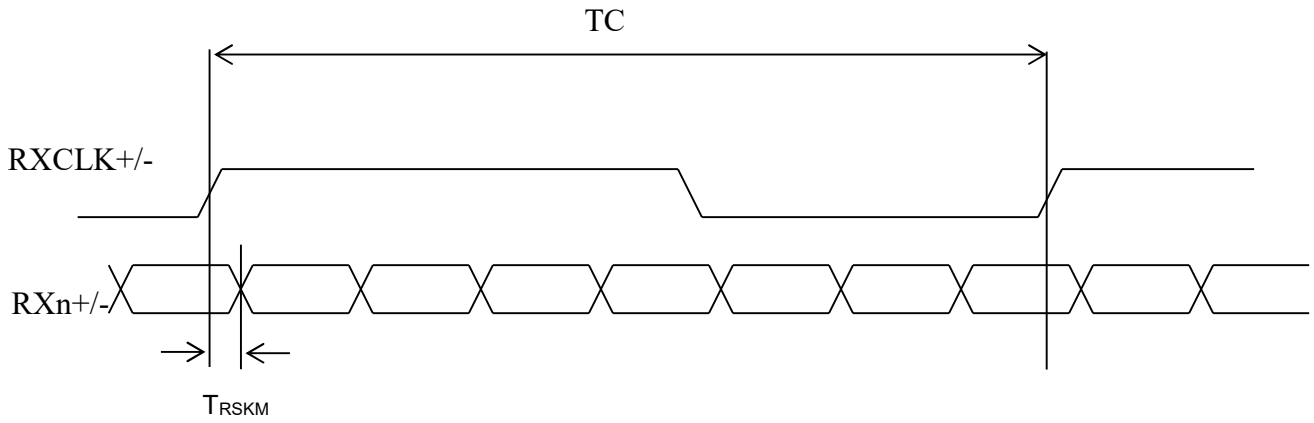


Note (4) The SSCG (Spread spectrum clock generator) is defined as below figures.



Note (5) The LVDS timing diagram and the receiver skew margin is defined and shown in following figure.

LVDS RECEIVER INTERFACE TIMING DIAGRAM

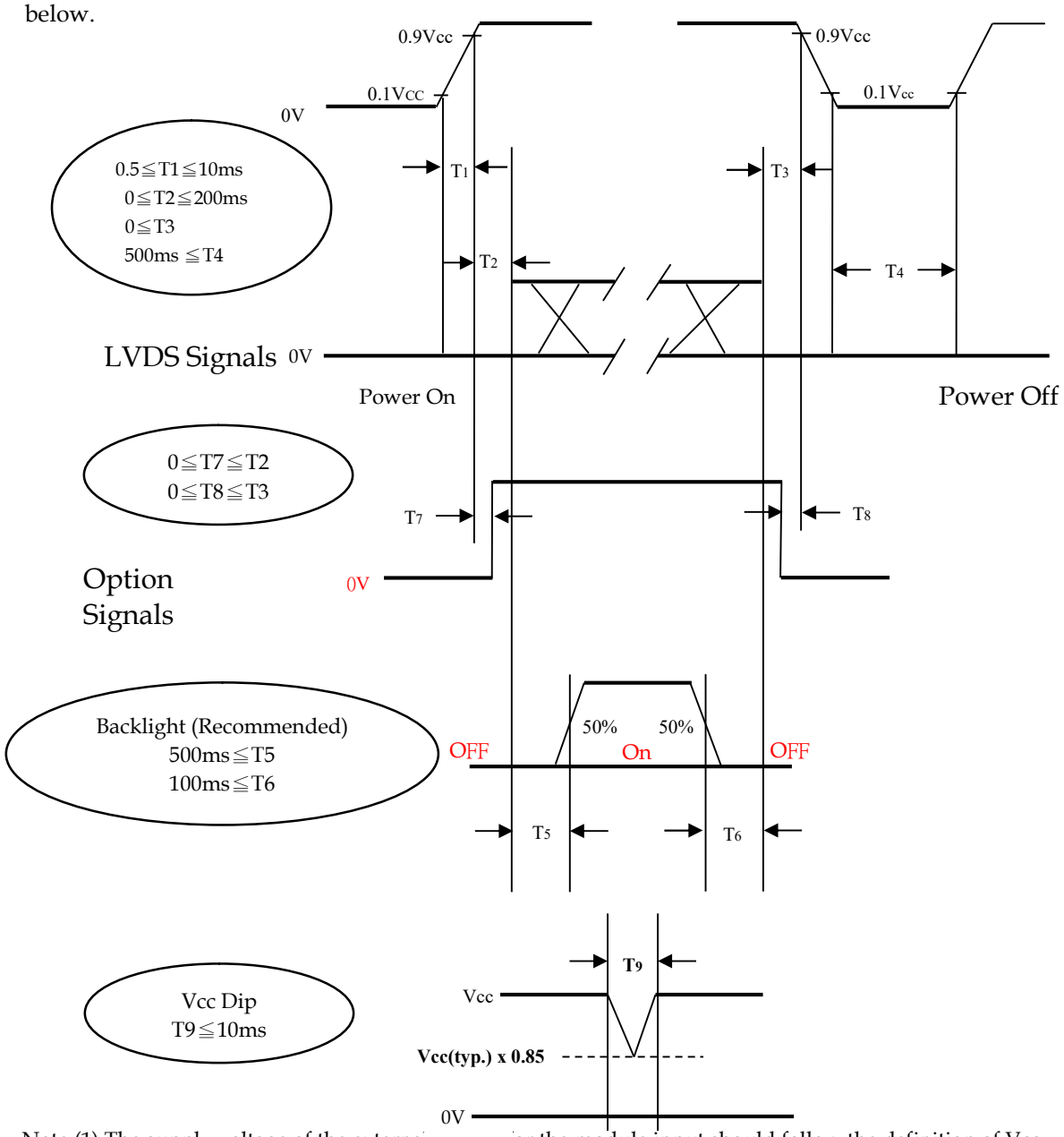


Note (6) For primitive resolution 1920*290, typical Tvd should be 1080 TH , image display at Tvd 291 to 1080 lines is invalid .

5.2 POWER ON/OFF SEQUENCE

(Ta = 25 ± 2 °C)

To prevent a latch-up or DC operation of LCD module, the power on/off sequence should be as the diagram below.



Note (1) The supply voltage of the externa or the module input should follow the definition of Vcc.

Note (2) Apply the LED voltage within the LCD operation range. When the backlight turns on before the LCD operation or the LCD turns off before the backlight turns off, the display may momentarily become abnormal screen.

Note (3) In case of VCC is in off level, please keep the level of input signals on the low or high impedance.
If T2<0, that maybe cause electrical overstress failure.

Note (4) T4 should be measured after the module has been fully discharged between power off and on period.

Note (5) Interface signal shall not be kept at high impedance when the power is on.

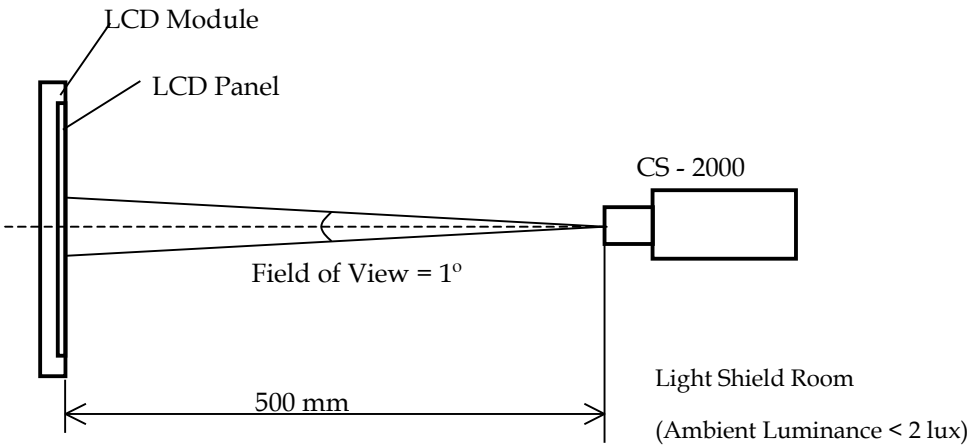
Note (6) Vcc must decay smoothly when power-off.

6. OPTICAL CHARACTERISTICS

6.1 TEST CONDITIONS

Item	Symbol	Value	Unit
Ambient Temperature	Ta	25±2	°C
Ambient Humidity	Ha	50±10	%RH
Supply Voltage	VCC	12±1.2	V
Input Signal	According to typical value in "3. ELECTRICAL CHARACTERISTICS"		
Vertical Frame Rate	Fr	60	Hz

The LCD module should be stabilized at given temperature for 1 hour to avoid abrupt temperature change during measuring in a windless room.



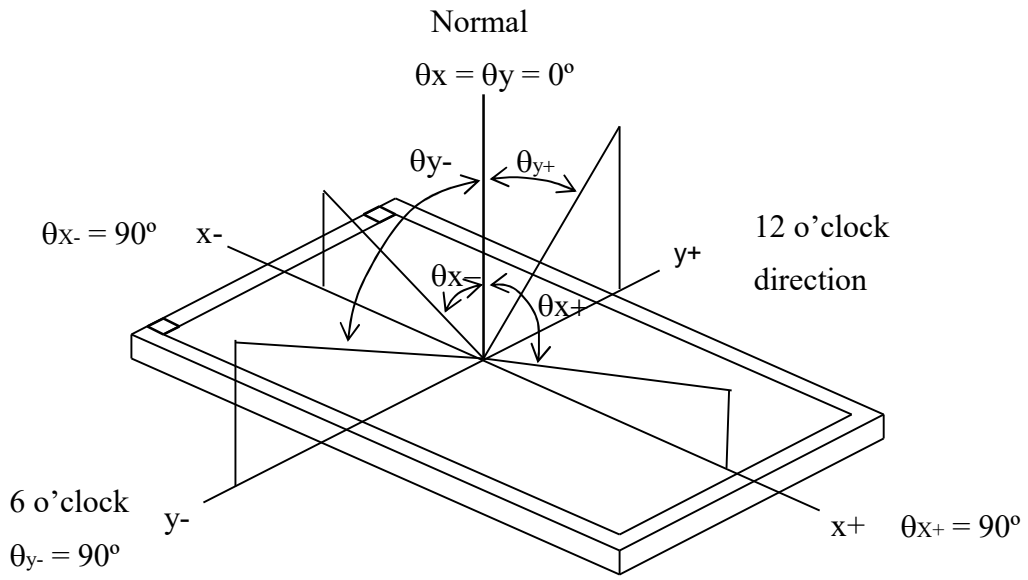
6.2 OPTICAL SPECIFICATIONS

The relative measurement methods of optical characteristics are shown in 6.2. The following items should be measured under the test conditions described in 6.1 and stable environment shown in 6.1.

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Contrast Ratio		CR	θ_x=0°, θ_Y =0° Viewing angle at normal direction	3500	5000	-	-	(2)
Response Time		Gray to gray			11	22	ms	(3)
Center Luminance of White		L_C		800	1000	-	cd/m²	(4)
White Variation		δW				1.3	-	(6)
Cross Talk		CT		-		4	%	(5)
Color Chromaticity	Red	Rx		Typ.-0.03	0.640	Typ.+0.03	-	
		Ry			0.338		-	
	Green	Gx			0.309		-	
		Gy			0.615		-	
	Blue	Bx			0.152		-	
		By			0.057		-	
	White	Wx			0.285		-	
		Wy			0.300		-	
	Correlated color temperature			-	8500	-	K	
	Color Gamut	C.G.		-	72	-	%	NTSC
Viewing Angle	Horizontal	θ_x+	CR≥10	80	89	-	Deg.	(1)
		θ_x-		80	89	-		
	Vertical	θ_Y+		80	89	-		
		θ_Y-		80	89	-		

Note (1) Definition of Viewing Angle (x, y) :

Viewing angles are measured by Autronic Conoscope Cono-80 (or Eldim EZ-Contrast 160R).



Note (2) Definition of Contrast Ratio (CR) :

The contrast ratio can be calculated by the following expression.

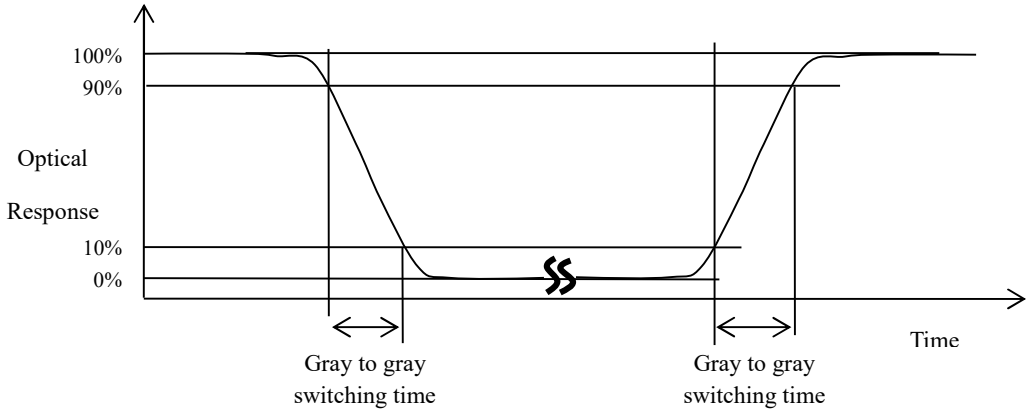
$$\text{Contrast Ratio (CR)} = \frac{\text{Surface Luminance of L255}}{\text{Surface Luminance of L0}}$$

L255: Luminance of gray level 255

L 0: Luminance of gray level 0

CR = CR (5), where CR (X) is corresponding to the Contrast Ratio of the point X at the figure in Note (6).

Note (3) Definition of Gray-to-Gray Switching Time :



The driving signal means the signal of gray level 0, 31, 63, 95, 127, 159, 191, 223 and 255.

Gray to gray average time means the average switching time of gray level 0, 31, 63, 95, 127, 159, 191, 223 and 255 to each other.

Note (4) Definition of Luminance of White (L_C, L_{AVE}) :

Measure the luminance of gray level 255 at center point and 5 points

LC = L (5), where L (X) is corresponding to the luminance of the point X at the figure in Note (6).

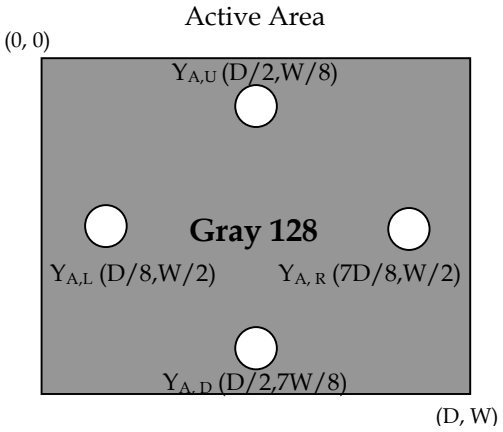
Note (5) Definition of Cross Talk (CT) :

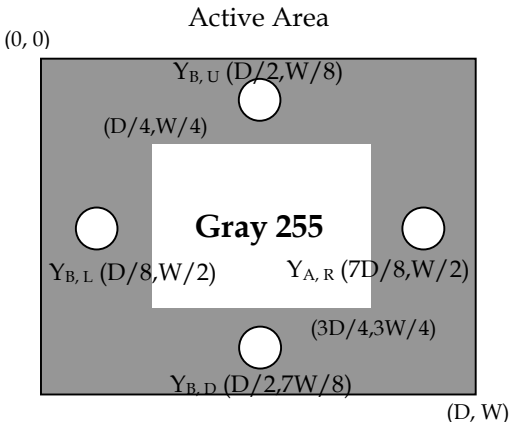
CT = | YB – YA | / YA 100 (%)

Where:

Y_A = Luminance of measured location without gray level 255 pattern (cd/m2)

Y_B = Luminance of measured location with gray level 255 pattern (cd/m2)

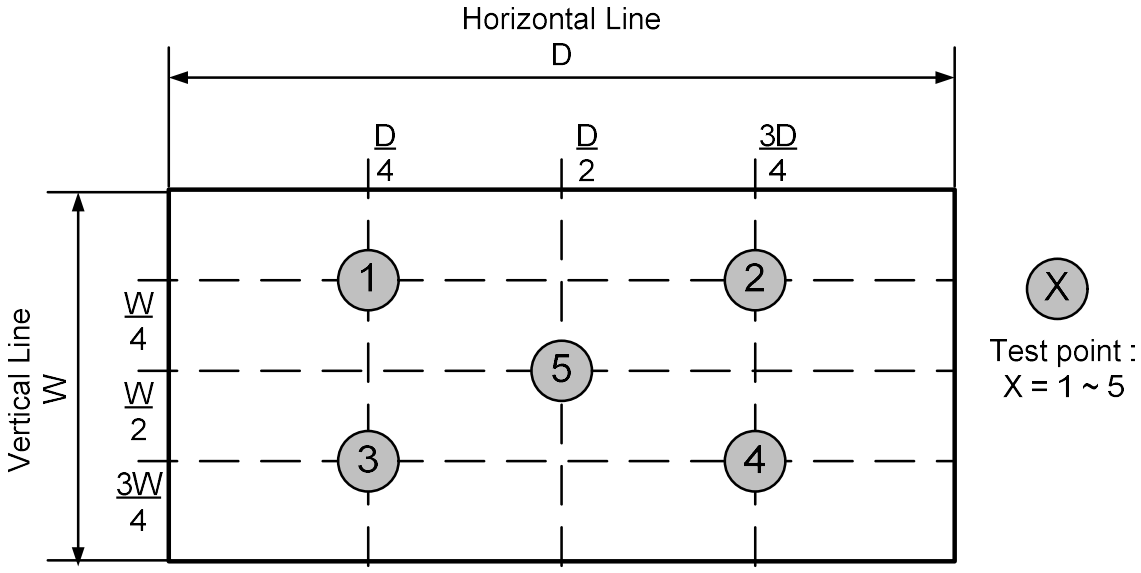




Note (6) Definition of White Variation (W) :

Measure the luminance of gray level 255 at 5 points

$$W = \frac{\text{Maximum [L (1), L (2), L (3), L (4), L (5)]}}{\text{Minimum [L (1), L (2), L (3), L (4), L (5)]}}$$



7. PRECAUTIONS

7.1 ASSEMBLY AND HANDLING PRECAUTIONS

[1] Do not apply rough force such as bending or twisting to the module during assembly.

[2] Do not apply pressure or impulse to the module to prevent the damage of LCD panel and Backlight.

[3] Bezel of Set can not press or touch the panel surface. It will make light leakage or scrape.

[4] It should be attached to the system firmly using all mounting holes.

[5] It is recommended to assemble or to install a module into the user's system in clean working areas. The dust and oil may cause electrical short or worsen the polarizer, do not press or scratch the surface harder than a HB pencil lead.

[6] Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.

[7] Protection film for polarizer on the module should be slowly peeled off just before use so that the electrostatic charge can be minimized.

[8] Do not disassemble the module.

[9] Always follow the correct power-on sequence when the LCD module is turned on. This can prevent the damage and latch-up of the CMOS LSI chips.

[10] Do not plug in or pull out the I/F connector while the module is in operation, pins of I/F connector should not be touched directly with bare hands. Do not adjust the variable resistor located on the module.

[11] Moisture can easily penetrate into LCD module and may cause the damage during operation.

[12] When storing modules as spares for a long time, the following precaution is necessary.

[12.1] Do not leave the module in high temperature, and high humidity for a long time. It is highly recommended to store the module with temperature from 0 to 35℃ at normal humidity (under 70%) without condensation.

[12.2] The module shall be stored in dark place. Do not store the TFT-LCD module in direct sunlight or fluorescent light.

[13] When ambient temperature is lower than 10°C, the display quality might be reduced. For example, the response time will become slow, and the starting voltage of LED will be higher than that of room temperature.

[14] Use a soft dry cloth without chemicals and Ethyl Alcohol for cleaning, because the surface of polarizer is very soft and easily scratched. Do not use Ketone type materials (ex. Acetone), Toluene, Ethyl acid or Methyl chloride, these chemical solvent might permanently damage the polarizer due to chemical action.

7.2 SAFETY PRECAUTIONS

To optimize PID module's lifetime and functions, operating conditions should be followed as below

[1] Normal operating condition

[1.1] Well-ventilated place is suggested to set up PID module and system.

[1.2] Display pattern: regular switched patterns or moving pictures.

[2] Operation usage to protect against image sticking due to long-term static display.

[2.1] Liquid Crystal refresh time is required. Cycling display between 5 minutes' information (static) display and 10 seconds' moving image.

[2.2] Periodical display contents should be changed from static image to moving picture.

[2.2.1] Different background and image colors changed respectively, and changed colors periodically.

[2.2.2] Background and image with large different luminance displayed at the same time should be avoided.

[2.2.3] Periodical power-off the system for a while or screen saver is needed after long-term static display.

[2.2.4] Moving picture or black pattern is strongly recommended for screen saver.

[3] The startup voltage of a Backlight may cause an electrical shock while assembling with the converter. Do not disassemble the module or insert anything into the Backlight unit.

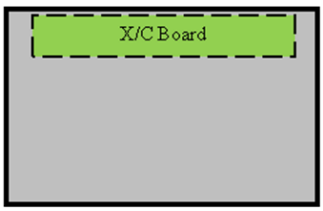
[4] Do not connect or disconnect the module in the "Power On" condition.

[5] Do not exceed the absolute maximum rating value. (supply voltage variation, input voltage variation, variation in part contents and environmental temperature…) Otherwise the module may be damaged.

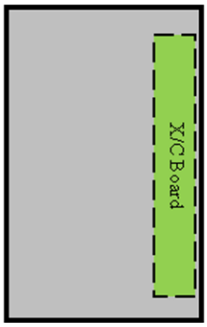
[6] If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, skin or clothes, it has to be washed away thoroughly with soap.

[7] Module should be turned clockwise (front view perspective) when used in portrait mode.

Landscape (Front view)



Portrait (Front view)



[8] Ultra-violet ray filter is necessary for outdoor operation.

[9] Only when PID module is operated under right operating conditions, lifetime in this spec can be guaranteed. After the module's end of life, it is not harmful in case of normal operation and storage.

[10] LCD system is required to place in well-ventilated environment. Adapting active cooling system is highly recommended.

[11] Product reliability and functions are only guaranteed when the product is used under right operation usages. If product will be used in extreme conditions, such as high temperature/humidity, display stationary patterns, or long operation time etc…, it is strongly recommended to contact INX for filed application engineering advice. Otherwise, its reliability and function may not be guaranteed. Extreme conditions are commonly found at airports, transit stations, banks, stock market and controlling systems.

7.3 SAFETY STANDARDS

The LCD module should be certified with safety regulations as follows:

Regulatory	Item	Standard
Audio/video, Information and Communication Technology Equipment	UL	UL 62368-1, 3rd Edition, 2019-12-13
	cUL	CAN/CSA C22.2 No. 62368-1-19, 3rd Edition, 2019-12-13
	CB	IEC 62368-1:2018

If the module displays the same pattern for a long period of time, the phenomenon of image sticking may be occurred.

7.4 DUST RESIST

[1] INX module dust test is conducted with marked holes (see Figure 7.4,marked with red circle) sealed to comply with JIS D 0207.

[2] Module users should design set with these holes used/sealed (if not used) or covered by set mechanism to prevent dust from entering. The INX testing procedure cannot replicate all different real world scenarious,module users should apply set dust resistance solution to meet user's requirement.

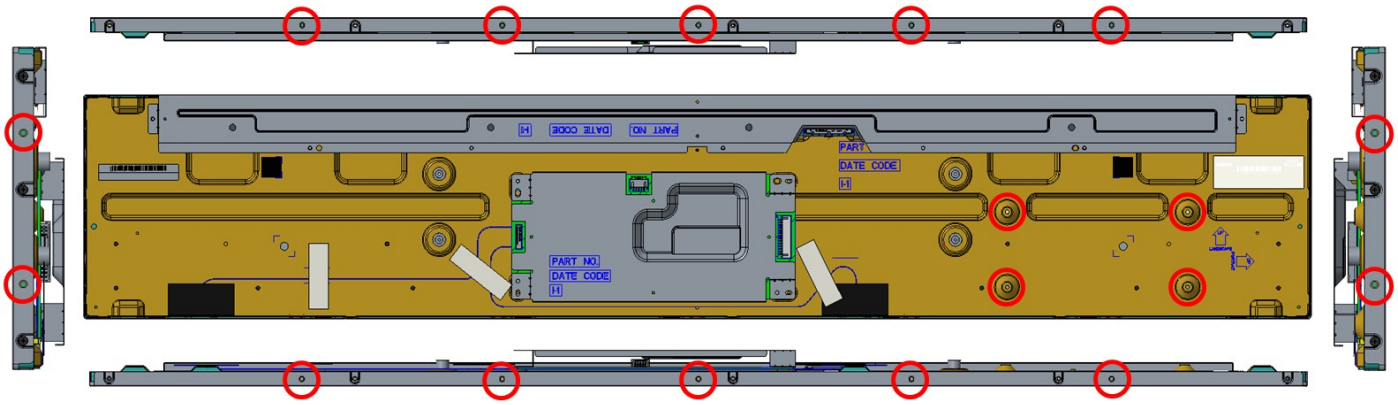


Figure 7.4

7.5 PRECAUTIONS FOR STRONG LIGHT EXPOSURE

[1] Strong light exposure causes degradation of polarizer and color filter. Since the product design is not protected by CG(cover glass), the deterioration of the polarizer due to sun exposure or water drenching is not guarantee.

[2] To keep display function well as a digital signage application, especially the component of TFT is very sensitive to sunlight, it is necessary to set up blocking device protecting panel from radiation of ambient environment.

8. DEFINITION OF LABELS

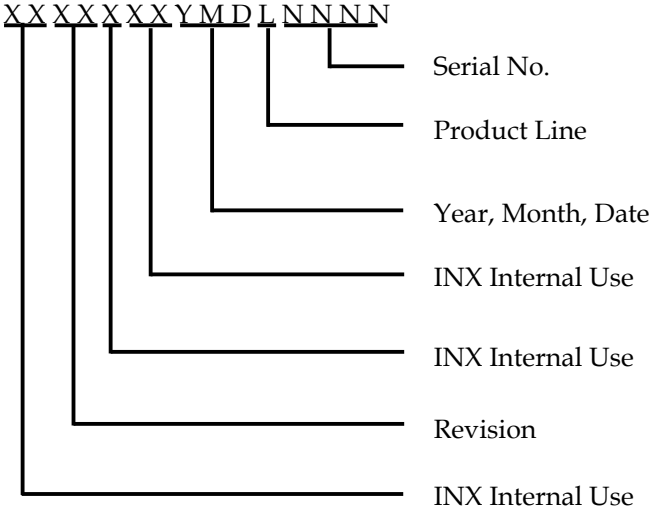
8.1 MODULE LABEL

The barcode nameplate is pasted on each module as illustration, and its definitions are as following explanation.

(a) Model Name: S366AJ1-LE1

(b) Revision: Rev. XX, for example: A0, A1… B1, B2… or C1, C2…etc.

(c) Serial ID: X X X X X X Y M D L N N N N



Serial ID includes the information as below:

(a) Manufactured Date: Year: 0~9, for 2010~2019

Month: 1~9, A~C, for Jan. ~ Dec.

Day: 1~9, A~Y, for 1st to 31st, exclude I ,O, and U.

(b) Revision Code: Cover all the change

(c) Serial No.: Manufacturing sequence of product

(d) Product Line: 1 → Line1, 2 → Line 2, …etc.

8.2 CARTON LABEL

The barcode nameplate is pasted on each box as illustration, and its definitions are as following explanation.

PO. NO._____

Part ID._____

Model Name _S366AJ1-LE1___Rev.XX_____

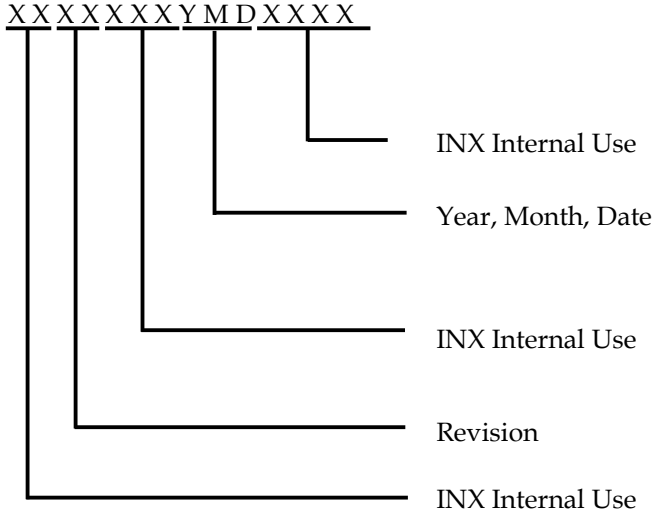
Carton ID._____ Quantities ___

XXXXXXXXXXXXXX

Made In Taiwan (Made In China) RoHS

Model Name: S366AJ1-LE1

Carton ID: X X X X X X Y M D X X X X



Serial ID includes the information as below :

Manufactured Date:

Year: 2010=0, 2011=1, 2012=2…etc.

Month: 1~9, A~C, for Jan. ~ Dec.

Day: 1~9, A~Y, for 1st to 31st, exclude I ,O, and U.

Revision Code: Cover all the change

9.PACKAGING

9.1 PACKAGING SPECIFICATIONS

(1) 10 LCD TV modules / 1 Box

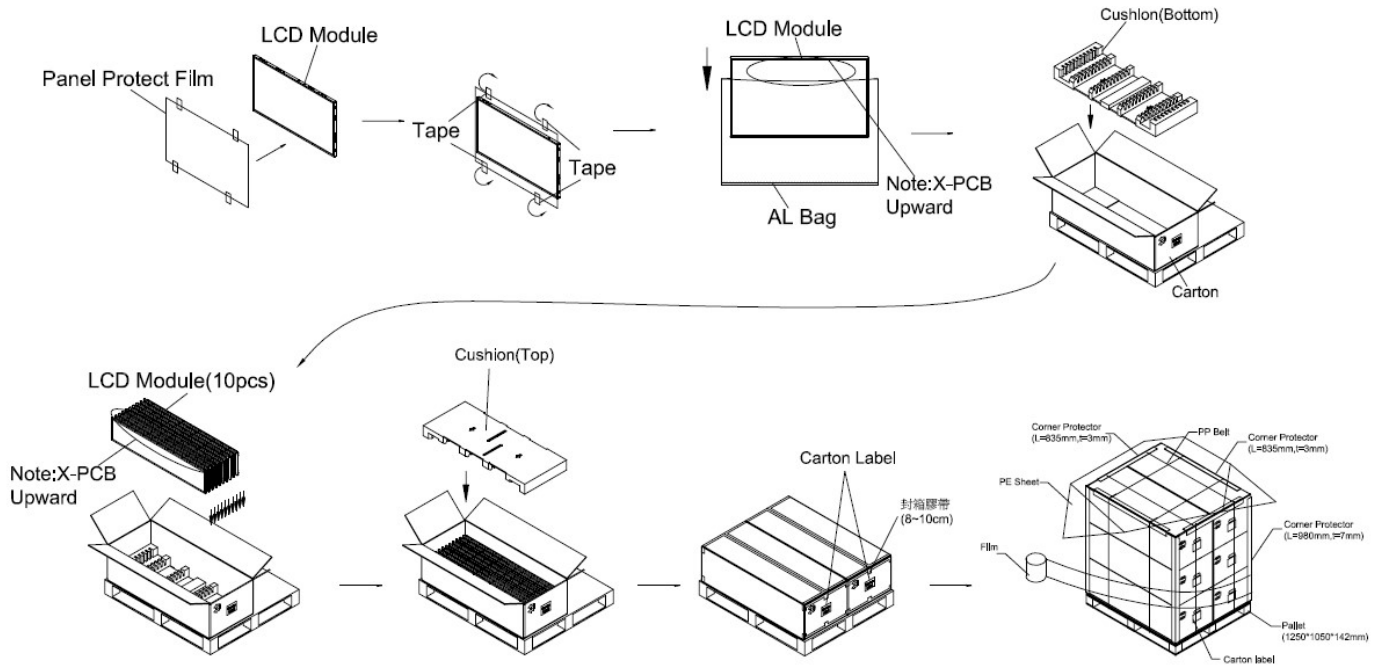
(2) Box dimensions: 1070(L) mmx590(W) mmx302(H) mm

(3) Weight: approximately 34 Kg (10 modules per box)

9.2 PACKAGING METHOD

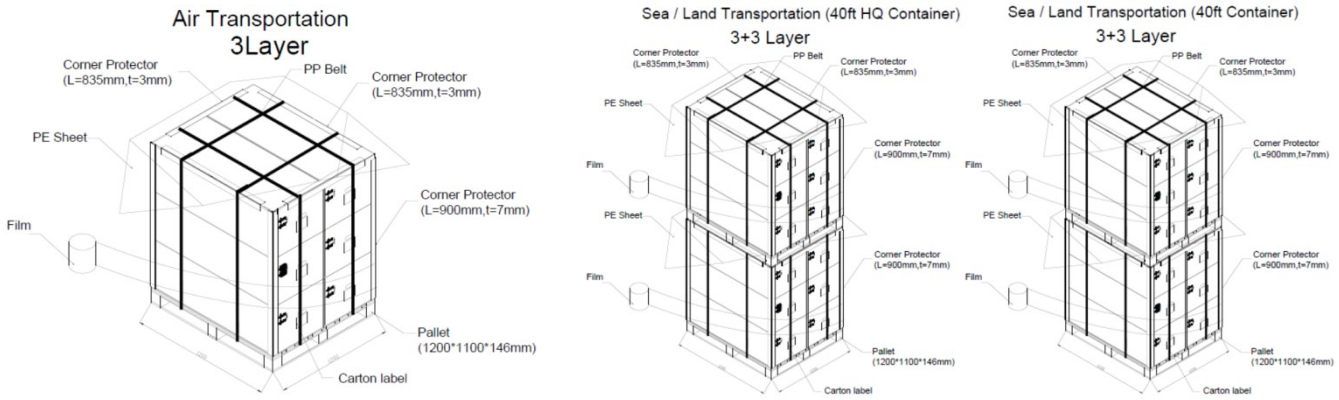
Notice : 1. When the products are stored, the package status must be kept as the same as shipping type.

2. Stack is forbidden when the products are stocked over one month.



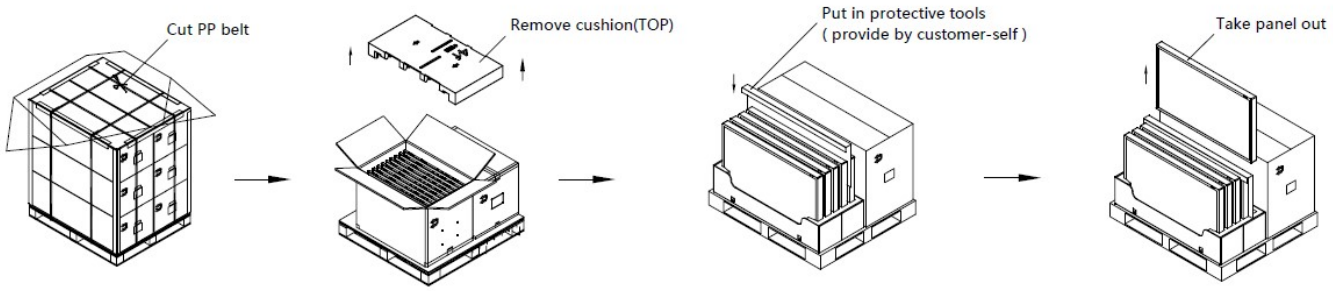
1.Carton dimensions: 1070(L)x590(W)x302(H)mm

2. 10 modules / carton

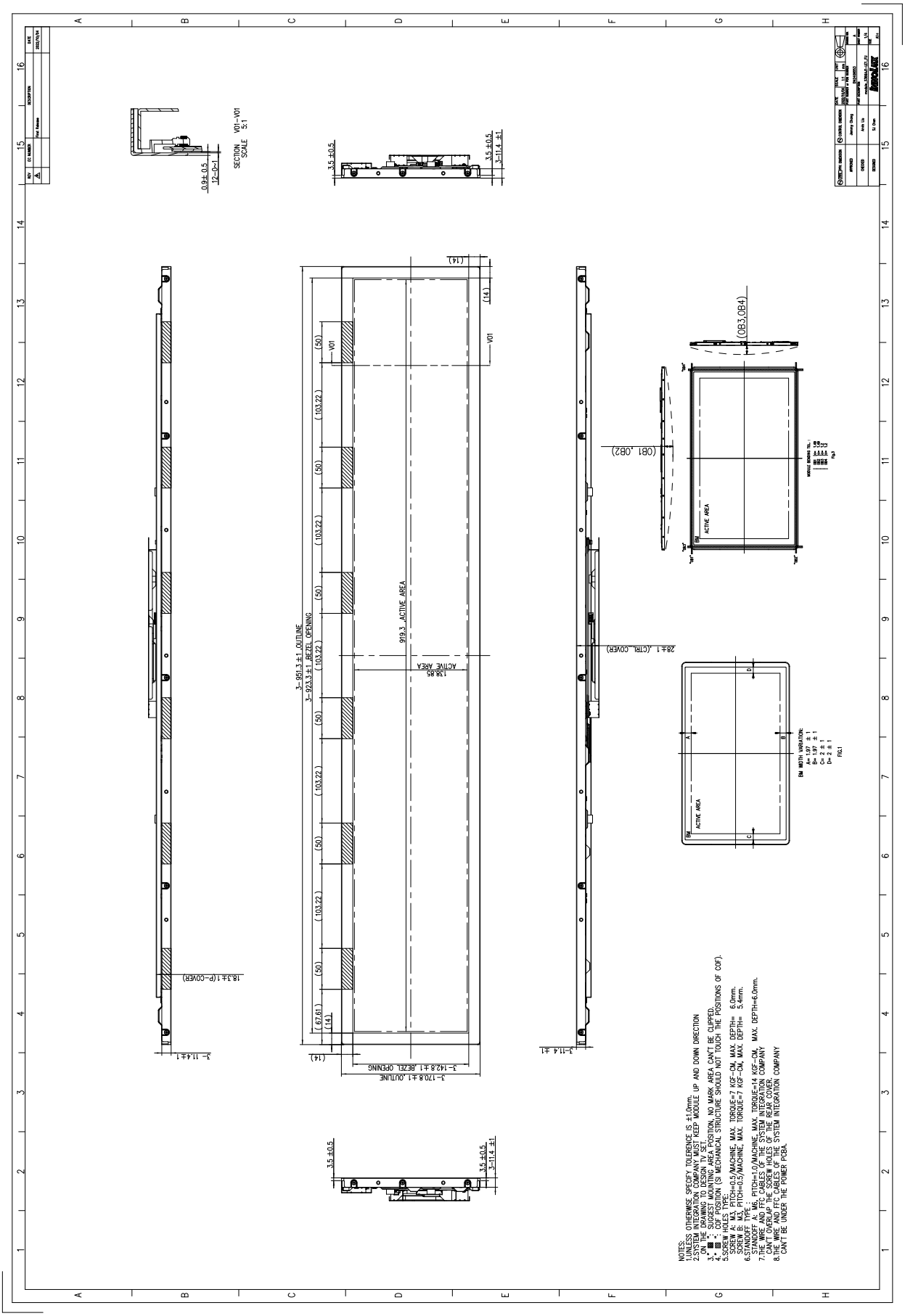


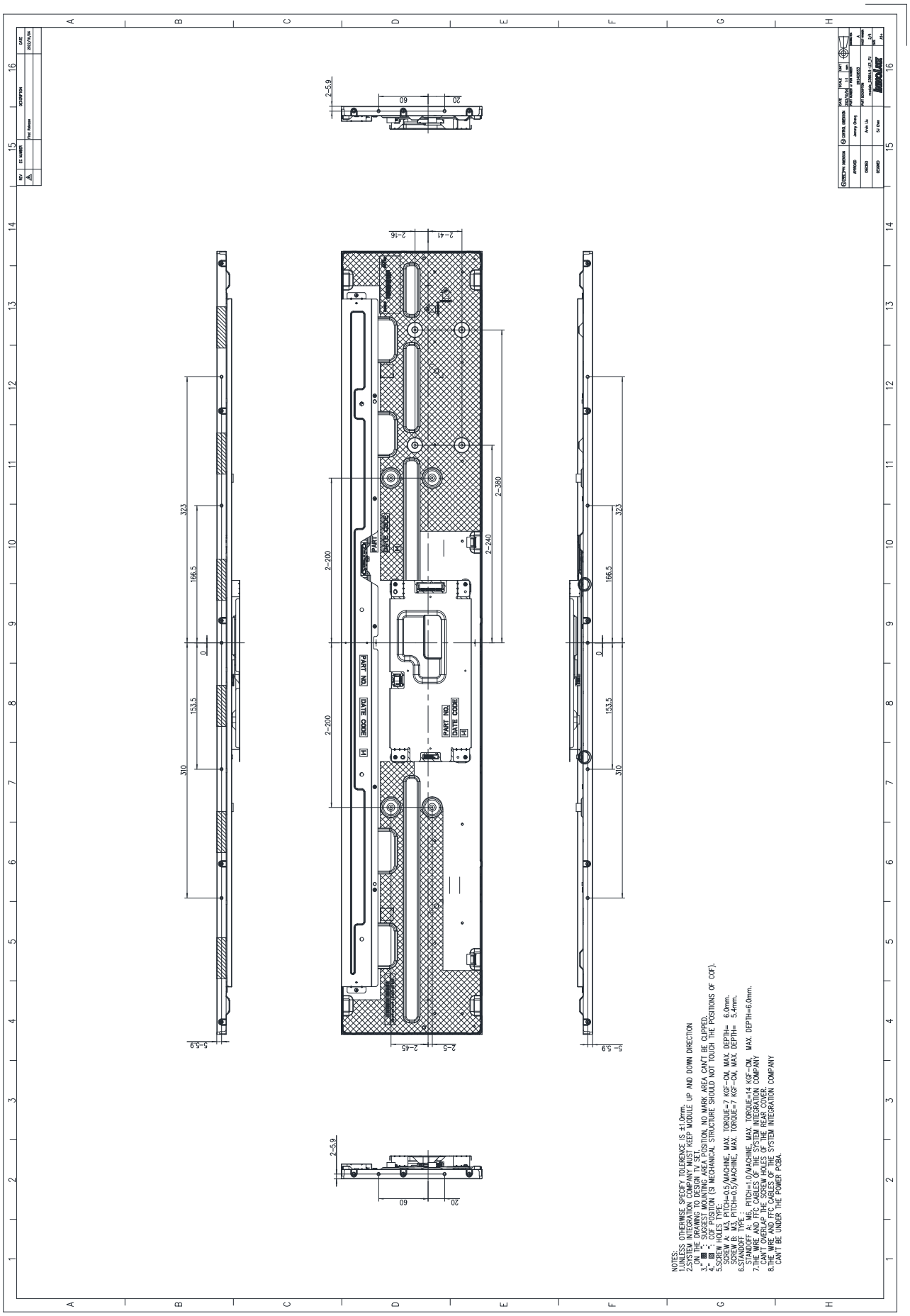
9.3 UN-PACKAGING METHOD

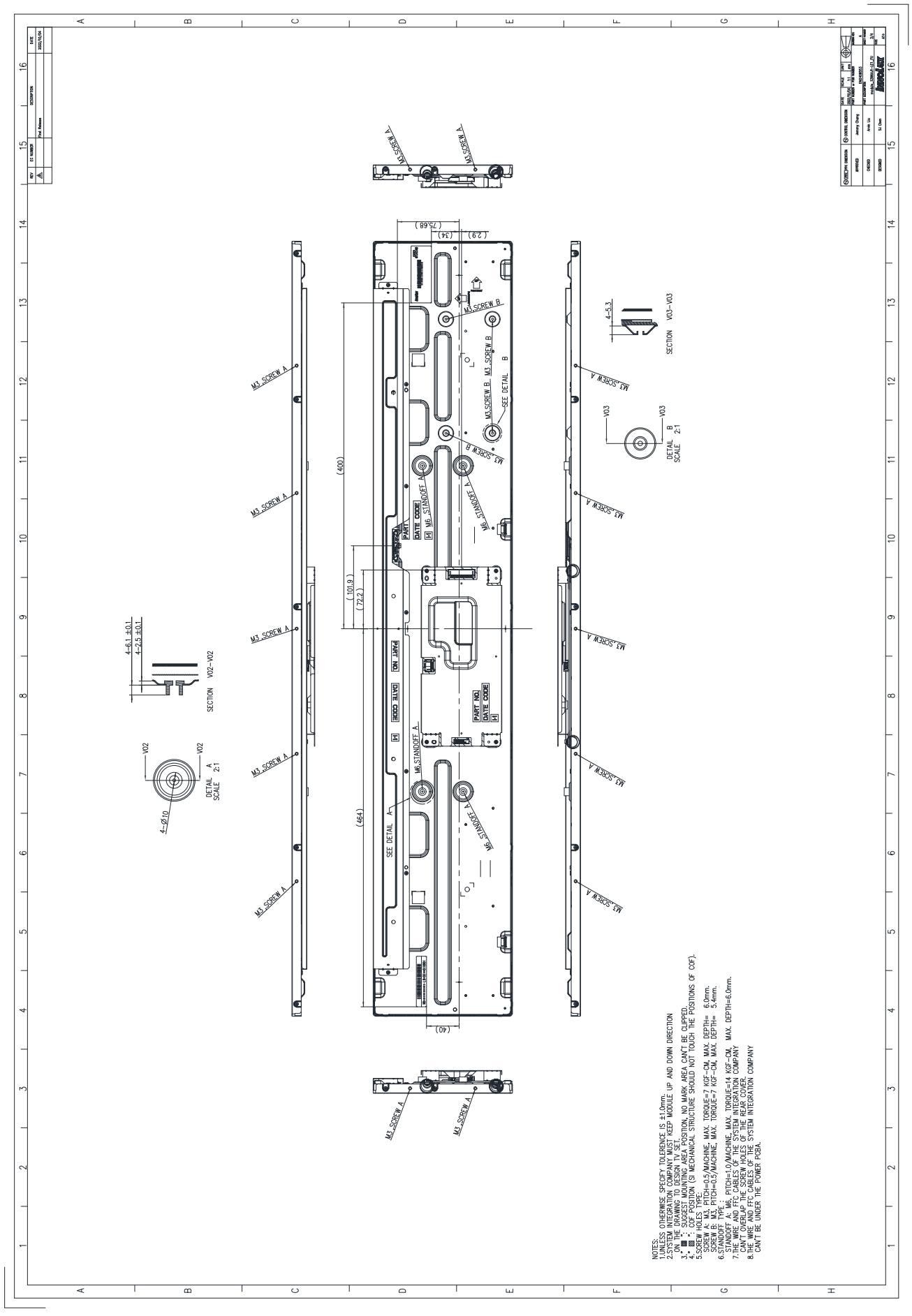
Un-packaging method is shown in following Figure.9-3.

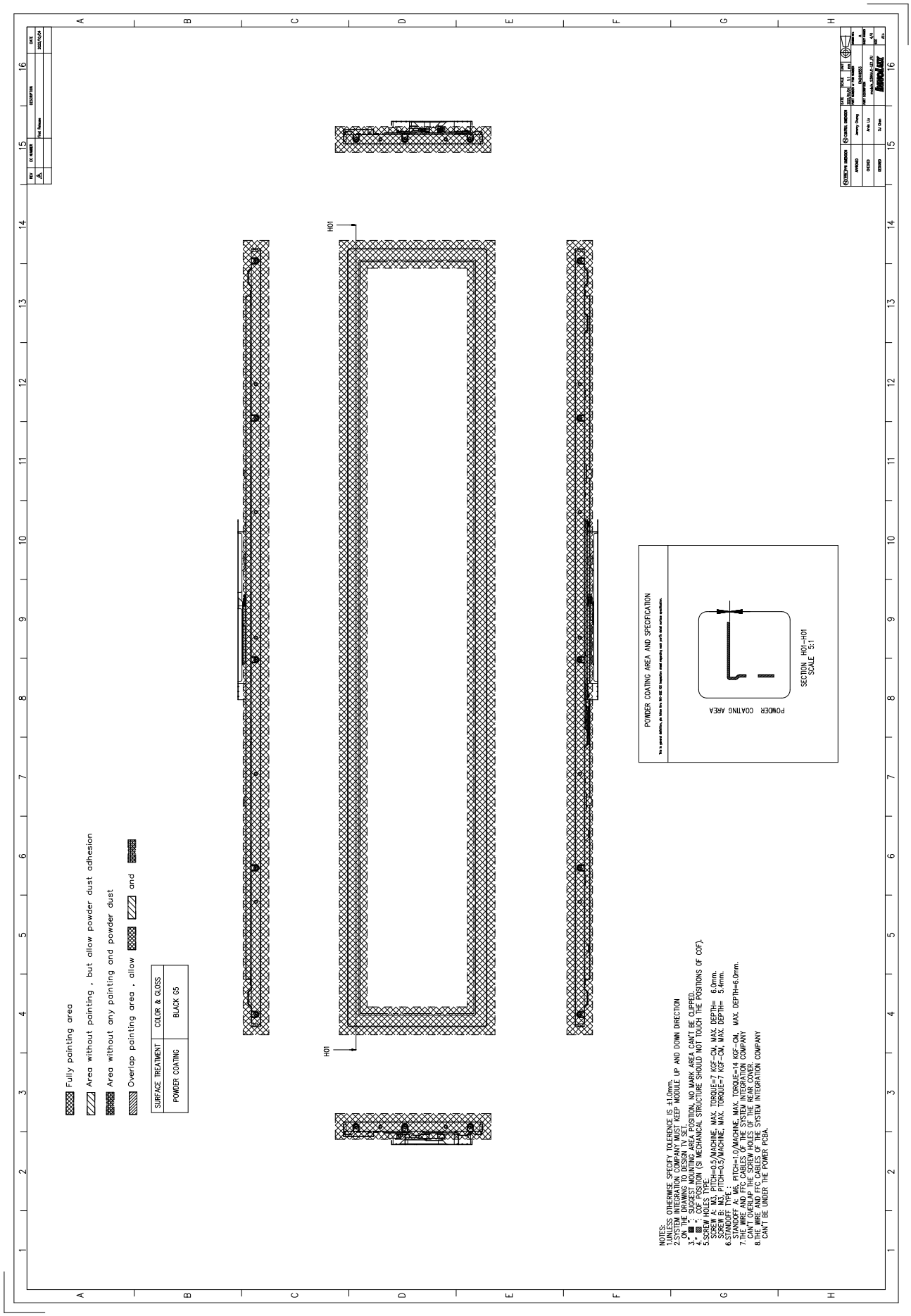


10. MECHANICAL CHARACTERISTIC









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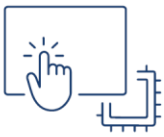
Passion Displayed



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