



SPECIFICATION



HSD070GFW4-B00-P

7" - WVGA - LVDS

Version: 1.1

Date: 17.04.2024

Note: This specification is subject to change without prior notice



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TO: DATA MODUL

Date: Apr.17.2024

HannStar Product Information

(Preliminary)

7" Color TFT-LCD Module

Model: HSD070GFW4-B00-P

Note: (1) The information contained herein is tentative and may be changed without prior notices.

- (2) Please contact HannStar Display Corp. before designing your product based on this module specification.
- (3) The information contained herein is presented merely to indicate the characteristics and performance of our products. No responsibility is assumed by HannStar for any intellectual property claims or other problems that may result from application based on the module described herein.



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1.0 GENERAL DESCRIPTION

1.1 Introduction

HannStar Display model HSD070GFW4-B00-P is a color active matrix thin film transistor (TFT) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching device. This model is composed of a TFT LCD panel, a driving circuit and a back- light system. This TFT LCD has a 7.0 inch (16:9) diagonally measured active display area with WVGA (1024 horizontal by 600 vertical pixel) resolution.

1.2 Features

- 7.0 (16:9 diagonal) inch configuration
- 16.7M color by 8bit R.G.B
- ROHS / Halogen Free Compliance

1.3 Applications

- TFT LCD Monitor
- Industrial Application
- Amusement
- Vehicle

1.4 General information

Item		Specification	Unit
LCM outline dim	ension	164.9(W) x 100(H) x 2.8(D)	mm
Display area		154.21(W) x 85.92(H)	mm
Number of Pixel		1024RGB x 600	pixels
Pixel pitch		0.1506(W) x 0.1432(H)	mm
Pixel arrangeme	nt	RGB Vertical Stripe	
Display mode		Normally Black	
Interface		LVDS	
NTSC		50 (Typ.)	%
Surface treatment		HC	
Weight		TBD	g
Power	Logic System	TBD	W
Consumption	B/L System	(1.92)Typ.	W



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2.0 ABSOLUTE MAXIMUM RATINGS

2.1 Electrical Absolute Rating

Item	Symbol	Min.	Max.	Unit	Note
Supply voltage	VDD	-0.3	3.96	V	GND=0
Analog power	AVDD	-0.3	14.85	V	GND=0
Positive power for TFT	VGH	-0.3	42	V	GND=0
Negative power for TFT	VGL	VGH-42	+0.3	V	GND=0
Logic Input voltage	Vin	-0.3	VDD+0.3	V	GND=0

Note

- (1) Permanent damage may occur to the LCD module if beyond this specification. Functional operation should be restricted to the conditions described under normal operating conditions.
- (2) Ta =25±2°C

2.2 Environment Absolute Rating

Item	Symbol	Min.	Max.	Unit	Note
Operating Temperature	T_{opa}	-20	70	$^{\circ}\!\mathbb{C}$	
Storage Temperature	T_{stg}	-30	80	$^{\circ}\!\mathbb{C}$	

Note 1:

If Ta below 50°C, the maximal humidity is 90%RH, if Ta over 50°C, absolute humidity should be less than 60%RH.

Note 2:

The response time will be extremely slow when the operating temperature is around -10 $^{\circ}$ C, and the back ground will become darker at high temperature operating.



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3.0 OPTICAL CHARACTERISTICS

3.1 Optical specification

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Item		Symbol	Condition	Min.	Тур.	Max.	Unit	Note
Contrast		CR		600	800	_		(1)(2)
Deepense tim		Tr		1	13	20	msec	(1)(3)
Response tim	1e	Tf		_	15	25	msec	(1)(3)
White lumina	nce	YL		=	500	-	cd/m ²	(1)(4)
Color Gamut		S(%)		1	50	_	%	
	\A/I '1	Wx	⊖=0		(0.31)	_		
	White	Wy	Normal	_	(0.33)	_		
		Rx	viewing	_	_	_		
Color	Red	Ry	angle -	_	_	_		
chromaticity (CIE1931)		Gx		_	_	_		(1)(4)
(OIL 1331)	Green	Gy		_	_	_		
		B _x		_	_	_		
	Blue	Ву		_	_	_		
		θL		_	85	_		
Viewing	Hor.	θR		_	85	_	•	(1)(2) (1)(3) (1)(3) (1)(4)
angle		θυ	CR>10	_	85	_		
	Ver.	θр		_	85	_		
Brightness Uniformity			Θ=0	_	80	_	%	
Optima View Direction			<u> </u>	AL	.L			

3.2 Measuring Condition

■ Measuring surrounding : dark room■ Ambient temperature : 25±2°C

■ 15min. warm-up time.

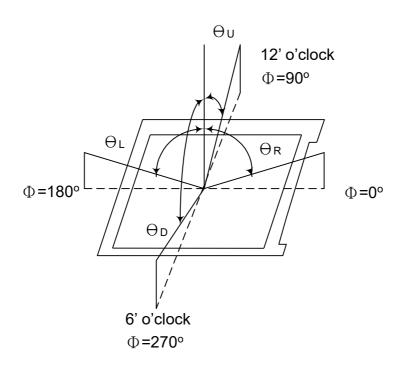


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3.3 Measuring Equipment

- FPM520 of Westar Display technologies, INC., which utilized SR-3 for Chromaticity and BM-7A for other optical characteristics.
- Measuring spot size: 20 ~ 21 mm

Note (1) Definition of Viewing Angle:



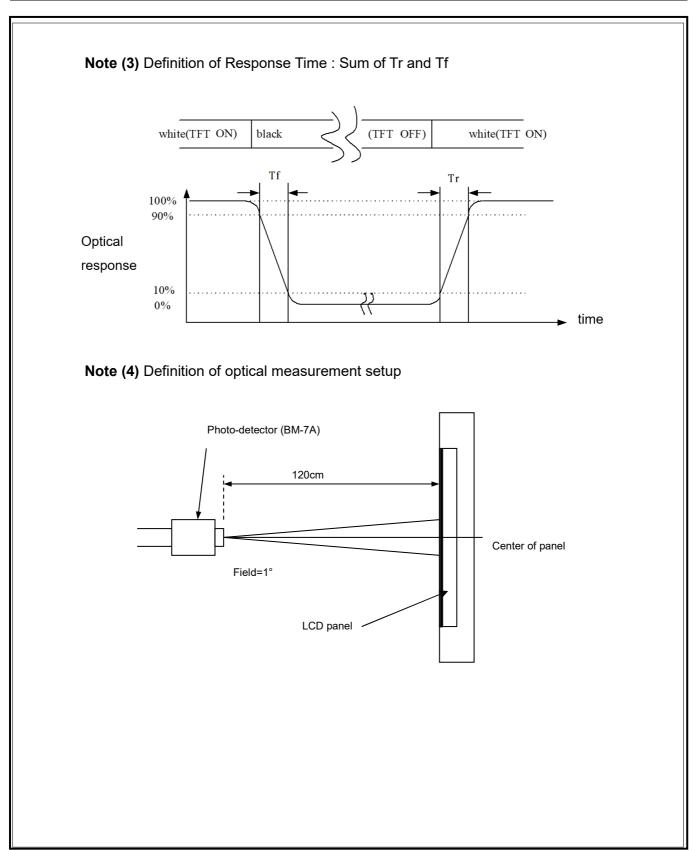
Note (2) Definition of Contrast Ratio (CR) : measured at the center point of panel

CR = Luminance with all pixels white

Luminance with all pixels black



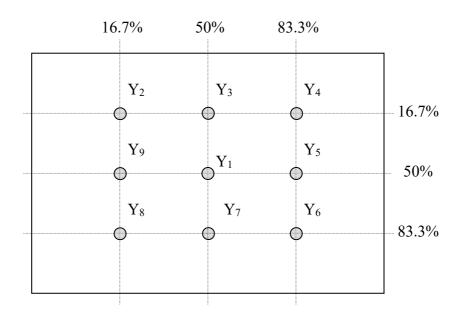
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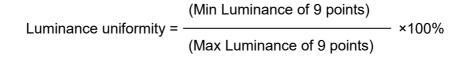




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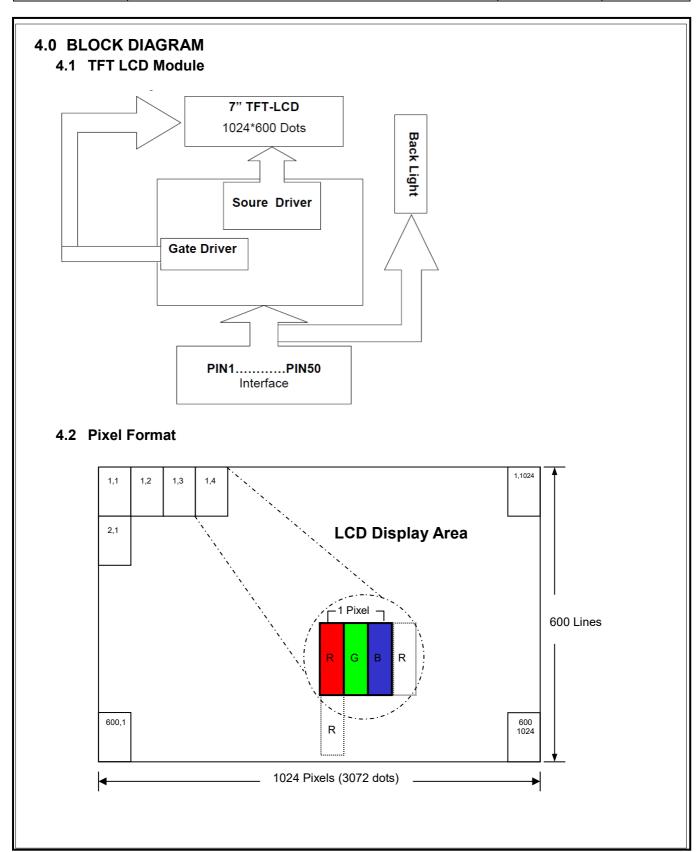
Note (5) Definition of brightness uniformity







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5.0 INTERFACE PIN CONNECTION

5.1 FPC Pin Assignment

Terminal no.	FPC PIN ASSIGNMENT									
2		Symbol	I/O	Function						
3	1	VCOM	Р	Common voltage						
4	2	VDD	Р	Power Supply +3.3V						
Standby mode. Normally pull high.	3	VDD	Р	Power Supply +3.3V						
Standby mode. Normally pull high. STBYB = 0, Standby mode STBYB = 0, Standby mode STBYB = 1, normal operation.	4	NC	-	No Connection						
STBYB STBYB 1 STBYB 1, normal operation.	5	Reset	I							
6 STBYB I STBYB = 1, normal operation. 7 GND I Ground 8 RIN0- I -LVDS differential data input 9 RIN0+ I +LVDS differential data input 10 GND I Ground 11 RIN1- I -LVDS differential data input 12 RIN1+ I +LVDS differential data input 13 GND I Ground 14 RIN2- I -LVDS differential data input 15 RIN2+ I +LVDS differential clock input 16 GND I Ground 17 CLKIN- I +LVDS differential clock input 18 CLKIN+ I +LVDS differential data input 19 GND I Ground 20 RIN3- I -LVDS differential data input 21 RIN3+ I +LVDS differential data input 22 GND I Ground <t< td=""><td></td><td></td><td></td><td>Standby mode. Normally pull high.</td></t<>				Standby mode. Normally pull high.						
7 GND I Ground 8 RINO- I -LVDS differential data input 9 RINO+ I +LVDS differential data input 10 GND I Ground 11 RIN1- I -LVDS differential data input 12 RIN1+ I +LVDS differential data input 13 GND I Ground 14 RIN2- I -LVDS differential data input 15 RIN2+ I +LVDS differential data input 16 GND I Ground 17 CLKIN- I -LVDS differential clock input 18 CLKIN+ I +LVDS differential clock input 19 GND I Ground 20 RIN3- I -LVDS differential data input 21 RIN3- I -LVDS differential data input 22 GND I Ground 23 NC - No Connection 24										
8 RINO- I -LVDS differential data input 9 RINO+ I +LVDS differential data input 10 GND I Ground 11 RIN1- I -LVDS differential data input 12 RIN1+ I +LVDS differential data input 13 GND I Ground 14 RIN2- I -LVDS differential data input 15 RIN2+ I +LVDS differential data input 16 GND I Ground 17 CLKIN- I -LVDS differential clock input 18 CLKIN+ I +LVDS differential clock input 19 GND I Ground 20 RIN3- I -LVDS differential data input 21 RIN3- I +LVDS differential data input 22 GND I Ground 23 NC - No Connection 24 NC - No Connection 25				·						
9 RIN0+ I +LVDS differential data input 10 GND I Ground 11 RIN1- I -LVDS differential data input 12 RIN1+ I +LVDS differential data input 13 GND I Ground 14 RIN2- I -LVDS differential data input 15 RIN2+ I +LVDS differential data input 16 GND I Ground 17 CLKIN- I -LVDS differential clock input 18 CLKIN+ I +LVDS differential clock input 19 GND I Ground 20 RIN3- I -LVDS differential data input 21 RIN3- I +LVDS differential data input 22 GND I Ground 23 NC - No Connection 24 NC - No Connection 25 GND I Ground 26 NC										
10			l							
11 RIN1- I -LVDS differential data input 12 RIN1+ I +LVDS differential data input 13 GND I Ground 14 RIN2- I -LVDS differential data input 15 RIN2+ I +LVDS differential data input 16 GND I Ground 17 CLKIN- I -LVDS differential clock input 18 CLKIN+ I +LVDS differential clock input 19 GND I Ground 20 RIN3- I -LVDS differential data input 21 RIN3+ I +LVDS differential data input 22 GND I Ground 23 NC - No Connection 24 NC - No Connection 25 GND I Ground 26 NC - No Connection 27 NC - No Connection 31 VLED_GND <t< td=""><td></td><td></td><td>l</td><td>'</td></t<>			l	'						
12			l							
13			ı							
14 RIN2- I -LVDS differential data input 15 RIN2+ I +LVDS differential data input 16 GND I Ground 17 CLKIN- I -LVDS differential clock input 18 CLKIN+ I +LVDS differential clock input 19 GND I Ground 20 RIN3- I -LVDS differential data input 21 RIN3- I +LVDS differential data input 22 GND I Ground 23 NC - No Connection 24 NC - No Connection 25 GND I Ground 26 NC - No Connection 27 NC - No Connection 28 SELB I Selection for either 6bit or 8bit LVDS input: 29 AVDD P Analog power 30 GND I LED Ground 31 VLED GND <			l							
15			l							
16			ı							
17 CLKIN- I -LVDS differential clock input 18 CLKIN+ I +LVDS differential clock input 19 GND I Ground 20 RIN3- I -LVDS differential data input 21 RIN3+ I +LVDS differential data input 22 GND I Ground 23 NC - No Connection 24 NC - No Connection 25 GND I Ground 26 NC - No Connection 27 NC - No Connection 28 SELB I Selection for either 6bit or 8bit LVDS input: 29 AVDD P Analog power 30 GND I Ground 31 VLED_GND I LED Ground 32 VLED_GND I Left / right Display control 34 UPDN I Up / Down Display Control 35 VGL			- 1							
18 CLKIN+ I +LVDS differential clock input 19 GND I Ground 20 RIN3- I -LVDS differential data input 21 RIN3+ I +LVDS differential data input 22 GND I Ground 23 NC - No Connection 24 NC - No Connection 25 GND I Ground 26 NC - No Connection 27 NC - No Connection 28 SELB I Selection for either 6bit or 8bit LVDS input: 29 AVDD P Analog power 30 GND I Ground 31 VLED_GND I LED Ground 32 VLED_GND I LED Ground 33 SHLR I Left / right Display control 34 UPDN I Up / Down Display Control 35 VGL P Neg			- 1							
19 GND I Ground 20 RIN3- I -LVDS differential data input 21 RIN3+ I +LVDS differential data input 22 GND I Ground 23 NC - No Connection 24 NC - No Connection 25 GND I Ground 26 NC - No Connection 27 NC - No Connection 28 SELB I Selection for either 6bit or 8bit LVDS input: 29 AVDD P Analog power 30 GND I Ground 31 VLED_GND I LED Ground 32 VLED_GND I LED Ground 33 SHLR I Left / right Display control 34 UPDN I Up / Down Display Control 35 VGL P Negative power for TFT 36 NC - No Connection </td <td></td> <td></td> <td>I</td> <td></td>			I							
20 RIN3- I -LVDS differential data input 21 RIN3+ I +LVDS differential data input 22 GND I Ground 23 NC - No Connection 24 NC - No Connection 25 GND I Ground 26 NC - No Connection 27 NC - No Connection 28 SELB I Selection for either 6bit or 8bit LVDS input: 29 AVDD P Analog power 30 GND I Ground 31 VLED_GND I LED Ground 32 VLED_GND I LED Ground 33 SHLR I Left / right Display control 34 UPDN I Up / Down Display Control 35 VGL P Negative power for TFT 36 NC - No Connection 37 NC - No Connection 38 VGH I Positive power for TFT 39 VLED I LED Power Supply	18	CLKIN+	I	+LVDS differential clock input						
21 RIN3+ I +LVDS differential data input 22 GND I Ground 23 NC - No Connection 24 NC - No Connection 25 GND I Ground 26 NC - No Connection 27 NC - No Connection 28 SELB I Selection for either 6bit or 8bit LVDS input: 29 AVDD P Analog power 30 GND I Ground 31 VLED GND I LED Ground 32 VLED GND I LED Ground 33 SHLR I Left / right Display control 34 UPDN I Up / Down Display Control 35 VGL P Negative power for TFT 36 NC - No Connection 37 NC - No Connection 38 VGH I Positive power for TFT	19	GND	I	Ground						
22 GND I Ground 23 NC - No Connection 24 NC - No Connection 25 GND I Ground 26 NC - No Connection 27 NC - No Connection 28 SELB I Selection for either 6bit or 8bit LVDS input: 29 AVDD P Analog power 30 GND I Ground 31 VLED GND I LED Ground 32 VLED_GND I LED Ground 33 SHLR I Left / right Display control 34 UPDN I Up / Down Display Control 35 VGL P Negative power for TFT 36 NC - No Connection 37 NC - No Connection 38 VGH I Positive power for TFT 39 VLED I LED Power Supply <td>20</td> <td>RIN3-</td> <td>I</td> <td>-LVDS differential data input</td>	20	RIN3-	I	-LVDS differential data input						
NC	21	RIN3+	I	+LVDS differential data input						
24 NC - No Connection 25 GND I Ground 26 NC - No Connection 27 NC - No Connection 28 SELB I Selection for either 6bit or 8bit LVDS input: 29 AVDD P Analog power 30 GND I Ground 31 VLED_GND I LED Ground 32 VLED_GND I LED Ground 33 SHLR I Left / right Display control 34 UPDN I Up / Down Display Control 35 VGL P Negative power for TFT 36 NC - No Connection 37 NC - No Connection 38 VGH I Positive power for TFT 39 VLED I LED Power Supply	22	GND		Ground						
25 GND I Ground 26 NC - No Connection 27 NC - No Connection 28 SELB I Selection for either 6bit or 8bit LVDS input: 29 AVDD P Analog power 30 GND I Ground 31 VLED_GND I LED Ground 32 VLED_GND I LED Ground 33 SHLR I Left / right Display control 34 UPDN I Up / Down Display Control 35 VGL P Negative power for TFT 36 NC - No Connection 37 NC - No Connection 38 VGH I Positive power for TFT 39 VLED I LED Power Supply	23	NC	-	No Connection						
26 NC - No Connection 27 NC - No Connection 28 SELB I Selection for either 6bit or 8bit LVDS input: 29 AVDD P Analog power 30 GND I Ground 31 VLED_GND I LED Ground 32 VLED_GND I LED Ground 33 SHLR I Left / right Display control 34 UPDN I Up / Down Display Control 35 VGL P Negative power for TFT 36 NC - No Connection 37 NC - No Connection 38 VGH I Positive power for TFT 39 VLED I LED Power Supply	24	NC	-	No Connection						
27 NC - No Connection 28 SELB I Selection for either 6bit or 8bit LVDS input: 29 AVDD P Analog power 30 GND I Ground 31 VLED_GND I LED Ground 32 VLED_GND I LED Ground 33 SHLR I Left / right Display control 34 UPDN I Up / Down Display Control 35 VGL P Negative power for TFT 36 NC - No Connection 37 NC - No Connection 38 VGH I Positive power for TFT 39 VLED I LED Power Supply	25	GND		Ground						
28 SELB I Selection for either 6bit or 8bit LVDS input: 29 AVDD P Analog power 30 GND I Ground 31 VLED_GND I LED Ground 32 VLED_GND I LED Ground 33 SHLR I Left / right Display control 34 UPDN I Up / Down Display Control 35 VGL P Negative power for TFT 36 NC - No Connection 37 NC - No Connection 38 VGH I Positive power for TFT 39 VLED I LED Power Supply	26	NC	-	No Connection						
29 AVDD P Analog power 30 GND I Ground 31 VLED_GND I LED Ground 32 VLED_GND I LED Ground 33 SHLR I Left / right Display control 34 UPDN I Up / Down Display Control 35 VGL P Negative power for TFT 36 NC - No Connection 37 NC - No Connection 38 VGH I Positive power for TFT 39 VLED I LED Power Supply	27	NC	-	No Connection						
30	28	SELB	Ī	Selection for either 6bit or 8bit LVDS input:						
31 VLED_GND I LED Ground 32 VLED_GND I LED Ground 33 SHLR I Left / right Display control 34 UPDN I Up / Down Display Control 35 VGL P Negative power for TFT 36 NC - No Connection 37 NC - No Connection 38 VGH I Positive power for TFT 39 VLED I LED Power Supply	29		Р	Analog power						
32 VLED_GND I LED Ground 33 SHLR I Left / right Display control 34 UPDN I Up / Down Display Control 35 VGL P Negative power for TFT 36 NC - No Connection 37 NC - No Connection 38 VGH I Positive power for TFT 39 VLED I LED Power Supply	30	GND	I	Ground						
33 SHLR I Left / right Display control 34 UPDN I Up / Down Display Control 35 VGL P Negative power for TFT 36 NC - No Connection 37 NC - No Connection 38 VGH I Positive power for TFT 39 VLED I LED Power Supply	31	VLED_GND	ı	LED Ground						
34 UPDN I Up / Down Display Control 35 VGL P Negative power for TFT 36 NC - No Connection 37 NC - No Connection 38 VGH I Positive power for TFT 39 VLED I LED Power Supply	32	VLED_GND	I							
34 UPDN I Up / Down Display Control 35 VGL P Negative power for TFT 36 NC - No Connection 37 NC - No Connection 38 VGH I Positive power for TFT 39 VLED I LED Power Supply	33	SHLR	Ī							
36 NC - No Connection 37 NC - No Connection 38 VGH I Positive power for TFT 39 VLED I LED Power Supply	34	UPDN	Ī							
37 NC - No Connection 38 VGH I Positive power for TFT 39 VLED I LED Power Supply	35	VGL	Р	Negative power for TFT						
38 VGH I Positive power for TFT 39 VLED I LED Power Supply	36		-	No Connection						
39 VLED I LED Power Supply	37	NC	-	No Connection						
'''	38	VGH	Ī	Positive power for TFT						
40 VLED I LED Power Supply	39	VLED	I	LED Power Supply						
	40	VLED	I	LED Power Supply						

Notes: 1. NC pin must be retained; this pin can't contact GND or other signal 2.GND pin must ground contact, cannot be floating



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5.2 Relationship Between Displayed Color and Input

	-	MS	SB					L	SB	MS	SB					- L:	SB	MS	В					L	SB	Gray scale
	Display			R5	R4	R3	R2					G5	G4	G3	G2			B7 I		B5	В4	вз	B2			Level
	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	-
	Blue	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	Н	H	Н	Н	Н	Н	Н	Н	-
	Green	L	L	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	L	-
Basic	Light Blue	L	L	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	-
color	Red	Н	Н	Н	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	-
	Purple	Н	Н	Н	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	-
	Yellow	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	L	-
	White	Н	Н	Н	Н	Н	Н	Н	Н	Η	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	-
	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L0
		L	L	L	L	L	L	L	Н	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L1
	Dark	L	L	L	L	L	L	Н	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L2
Gray scale	↑				:								:								:					L3L251
of Red	↓	Н	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L252
	Light	Н	Н	Н	Н	Н	Н	L	Н	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L253
		Н	Н	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L254
	Red	Н	Н	Н	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	Red L255
	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L0
		L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	Н	Ш	L	L	L	L	L	L	L	L1
	Dark	L	L	L	L	L	L	L	L	Ш	L	L	L	L	L	Н	L	ш	L	L	L	L	L	L	L	L2
Gray scale	↑				:																:					L3L251
of Green	\downarrow	L	L	L	L	L	L	L	L	Η	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	L	L	L	L252
	Light	L	L	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	L	Н	L	L	L	L	L	L	L	L	L253
		L	L	L	L	L	L	L	L	Ι	Н	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	L	L	L254
	Green	L	L	L	L	L	L	L	L	Η	Н	Н	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	L	Green L255
	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L0
		L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	Н	L1
	Dark	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	Н	L	L2
Gray scale	↑				:																:					L3…L251
of Blue	\downarrow	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	L	L	L252
	Light	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	L	Н	L253
		L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	L	L254
	Blue	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	Ι	Н	Н	Н	Н	Н	Н	Н	Blue L255
	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L0
		L	L	L	L	L	L	L	Н	L	L	L	L	L	L	L	Н	L	L	L	L	L	L	L	Н	L1
	Dark	L	L	L	L	L	L	Н	L	L	L	L	L	L	L	Н	L	L	L	L	L	L	L	Н	L	L2
Gray scale	↑				:																:					L3…L251
of White & Black	↓	Н	Н	Н	Н	Н	Н	L	L	Н	Н	Н	Н	Н	Н	L	L	Н	Н	Н	Н	Н	Н	L	L	L252
	Light	Н	Н	Н	Н	Н	Н	L	Н	Н	Н	Н	Н	Н	Н	L	Н	Н	Н	Н	Н	Н	Н	L	Н	L253
		Н	Н	Н	Н	Н	Н	Н	L	Н	Н	Н	Н	Н	Н	Н	L	Н	Н	Н	Н	Н	Н	Н	L	L254
	White	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	H	Н	Н	Н	Н	Н	Н	White L255



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6.0 ELECTRICAL CHARACTERISTICS

6.1 TFT LCD Module

Item	Symbol	Min.	Тур.	Max.	Unit	Note
	VDD	3.0	3.3	3.6	V	
Supply voltage	AVDD	10.5	11	11.5	٧	
Supply voltage	VGH	17	18	19	٧	
	VGL	-11	-10	-9	٧	
Common voltage	VCOM		(4.35)		V	Note1
	I_{VDD}		20	30	٧	VDD=3.3V @White pattern
Current of power	I _{AVDD}		25	35	٧	AVDD=11V @White pattern
supply	I_{VGH}		0.5	1	٧	VGH=18V
	I_{VGL}		1	1.5	٧	VGL=-10V
Logic input	VIH	0.7*VDD	-	VDD	V	
voltage	VIL	GND	-	0.3*VDD	V	

Note1: Vcom must be adjusted to optimize display quality crosstalk, Contrast Ratio and etc.

6.2 Backlight Unit

Parameter	Symbol	Min	Тур	Max	Units	Condition
LED Current	IL		200		mA	Ta=25°ℂ
LED Voltage	V _F	9.3	9.6	9.9	Volt	Ta=25°ℂ
LED Life-Time	N/A	30000			Hour	Ta=25°ℂ
LLD Life-Tillie	IN/A	30000			rioui	Note (2)

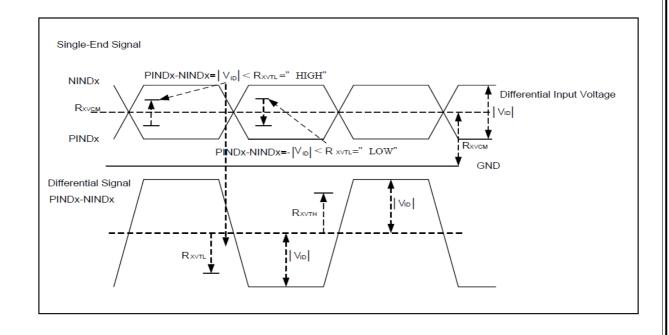
- Note (1) LED life time (Hr) can be defined as the time in which it continues to operate under the condition: Ta=25±3 °C, typical IL value indicated in the above table until the brightness becomes less than 50%.
- Note (2) The "LED life time" is defined as the module brightness decrease to 50% original brightness at Ta=25°C. and IL=200 mA LED lifetime could be decreased if operating IL is larger than 200 mA The constant current driving method is suggested.



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6.3 LVDS DC electrical characteristics

Parameter	Symbol	Min	Тур	Max	Units	Condition				
Differential input high threshold voltage	R _{XVTH}	-	-	+0.1	V	R _{XVCM} =1.2V				
Differential input low threshold voltage	R _{XVTL}	-0.1	-	-	V	R _{XVCM} =1.2V				
Input voltage range (Singled-end)	R _{XVIN}	0	-	2.4	V					
Differential input common mode voltage	R _{XVCM}	V _{ID} /2	1.2	2.4- V _{ID} /2	V					
Differential input Voltage	V _{ID}	0.2	-	0.6	V					

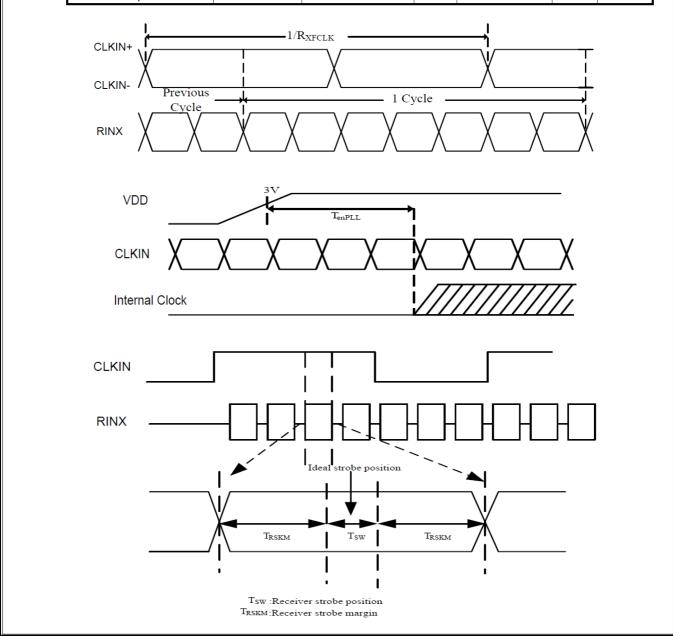




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6.4 LVDS AC electrical characteristics

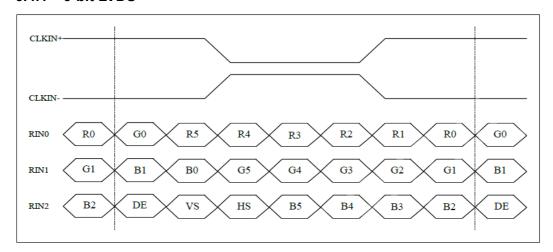
Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit
Clock Frequency	Rxfclk		20	-	71	MHz
Input data skew margin	Trskm	VID =400mV RxVCM=1.2V RxFCLK=71MHz	500			ps
Clock High Time	TLVCH			4/(7* RxFCLK)		ns
Clock Low Time	TLVCL			3/(7* RxFCLK)		ns ns
PLL wake-up-time	TenPLL				150	us



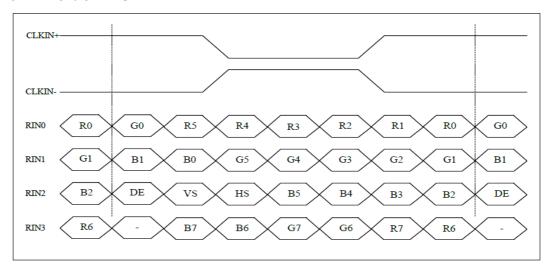
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6.5 Data input format for LVDS 6.4.1 6-bit LVDS



6.4.2 8-bit LVDS



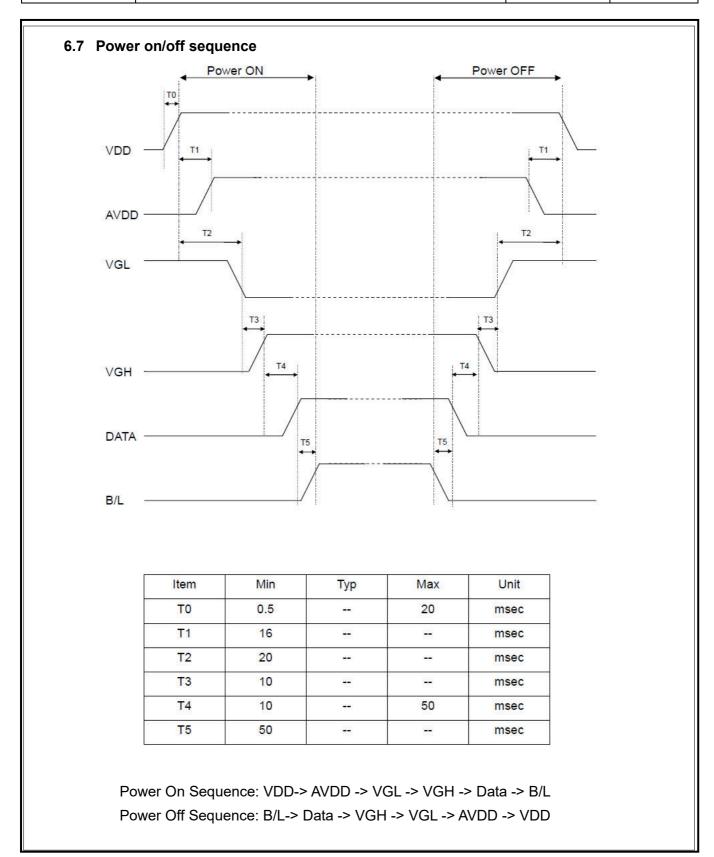
6.6 Input timing

DE mode

Parameter	Symbol		Unit		
Faiailletei	Syllibol	Min.	Тур.	Max.	Onic
DCLK Frequency	fclk	40.8	51.2	67.2	MHz
Horizontal Display Area	thd		DCLK		
HSD Period	th	1114	1344	1600	DCLK
HSD Blanking	thb+ thfp	90 320		376	DCLK
Vertical Display Area	tvd		600		T _H
VSD Period	tvbp	610 635		800	T _H
VSD Blanking	tvbp+ tvfp	10 35		200	T _H



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7.0 RELIABILTY TEST ITEMS.

7.1 Test condition

No.	Item	Conditions	Remark
1	High Temperature Storage	Ta=+80℃, 240hrs	1,2,3
2	Low Temperature Storage	Ta=-30°ℂ , 240hrs	1,2,3
3	High Temperature Operation	Ta=+70℃, 240hrs	1,2,3
4	Low Temperature Operation	Ta=-20°ℂ , 240hrs	1,2,3
5	High Temperature and High Humidity (operation)	Ta=+40℃, 90%RH, 240hrs	1,2,3

Note1: There is no display function NG issue occurred, all the cosmetic specification is judged before the reliability stress.

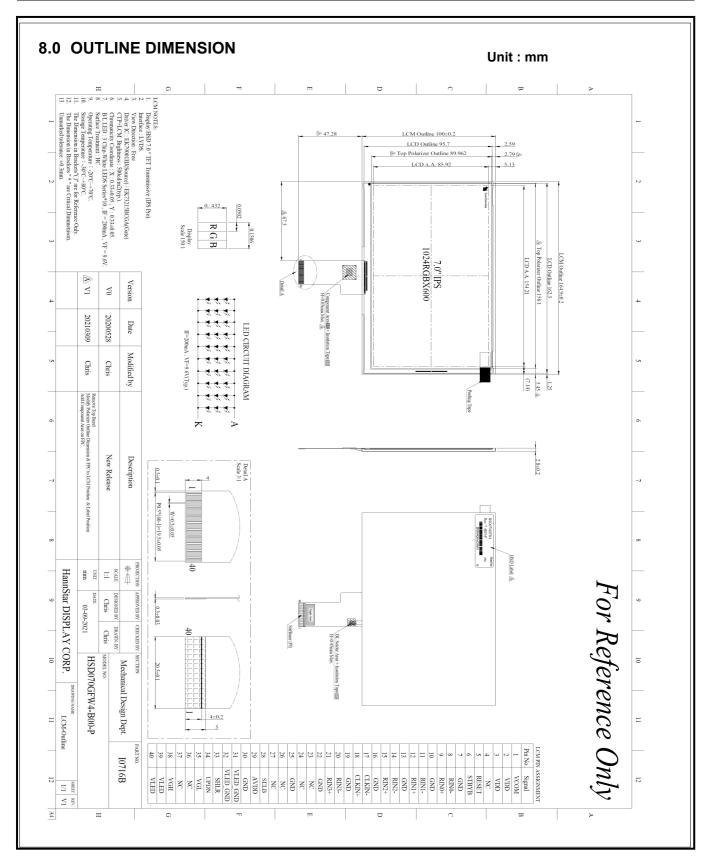
Note2: All of the function & cosmetic Judgment basis base on room temperature.

(The tested module must have enough recovery time at least 2 hours at room temperature.)

Note3: The test condition definition panel's surface temperature.



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9.0 LOT MARK

9.1 Lot Mark

 1
 2
 3
 4
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 15

Code 1,2,3,4,5,6: HannStar internal flow control code.

Code 7: production location.

Code 8: production year.

Code 9: production month.

Code 10,11,12,13,14,15: serial number.

Note (1) Production Year: Code 8 is defined by the last number of the year, for example

Year	2016	2017	2018	2019	2020	2021	2022	2023	2024	2025	2026
Mark	6	7	8	9	0	1	2	3	4	5	6

Note (2) Production Month

Month	Jan.	Feb.	Mar.	Apr.	Мау.	Jun.	Jul.	Aug.	Sep.	Oct	Nov.	Dec.
Mark	1	2	3	4	5	6	7	8	9	Α	В	С

9.2 Detail of Lot Mark

- (1) Below label is attached on the backside of the LCD module. See Section 8.0: Outline Dimension.
- (2) The detail of Lot Mark is attached as below.
- (3) This is subject to change without prior notice.





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10.0 PACKAGE SPECIFICATION 10.1 Packing form
TBD
10.2 Pallet Drawing
TBD



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11.0 GENERAL PRECAUTION

11.1 Use Restriction

This product is not authorized for use in life supporting systems, aircraft navigation control systems, military systems and any other application where performance failure could be life-threatening or otherwise catastrophic.

11.2 Disassembling or Modification

Do not disassemble or modify the module. It may damage sensitive parts inside LCD module, and may cause scratches or dust on the display. HannStar does not warrant the module, if customers disassemble or modify the module.

11.3 Breakage of LCD Panel

- 11.3.2. If LCD panel is broken and liquid crystal spills out, do not ingest or inhale liquid crystal, and do not contact liquid crystal with skin.
- 11.3.3. If liquid crystal contacts mouth or eyes, rinse out with water immediately.
- 11.3.4. If liquid crystal contacts skin or cloths, wash it off immediately with alcohol and rinse thoroughly with water.
- 11.3.5. Handle carefully with chips of glass that may cause injury, when the glass is broken.

11.4 Electric Shock

- 11.4.1. Disconnect power supply before handling LCD module.
- 11.4.2. Do not pull or fold the LED cable.
- 11.4.3. Do not touch the parts inside LCD modules and the fluorescent LED's connector or cables in order to prevent electric shock.

11.5 Absolute Maximum Ratings and Power Protection Circuit

- 11.5.1. Do not exceed the absolute maximum rating values, such as the supply voltage variation, input voltage variation, variation in parts' parameters, environmental temperature, etc., otherwise LCD module may be damaged.
- 11.5.2. Please do not leave LCD module in the environment of high humidity and high temperature for a long time.
- 11.5.3. It's recommended to employ protection circuit for power supply.

11.6 Operation

- 11.6.1 Do not touch, push or rub the polarizer with anything harder than HB pencil lead.
- 11.6.2 Use fingerstalls of soft gloves in order to keep clean display quality, when persons handle the LCD module for incoming inspection or assembly.
- 11.6.3 When the surface is dusty, please wipe gently with absorbent cotton or other soft material.



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- 11.6.4 Wipe off saliva or water drops as soon as possible. If saliva or water drops contact with polarizer for a long time, they may causes deformation or color fading.
- 11.6.5 When cleaning the adhesives, please use absorbent cotton wetted with a little petroleum benzine or other adequate solvent.

11.7 Mechanism

Please mount LCD module by using mounting holes arranged in four corners tightly.

11.8 Static Electricity

- 11.8.1 Protection film must remove very slowly from the surface of LCD module to prevent from electrostatic occurrence.
- 11.8.2 Because LCD module use CMOS-IC on circuit board and TFT-LCD panel, it is very weak to electrostatic discharge. Please be careful with electrostatic discharge. Persons who handle the module should be grounded through adequate methods.

11.9 Strong Light Exposure

The module shall not be exposed under strong light such as direct sunlight. Otherwise, display characteristics may be changed.

11.10 Disposal

When disposing LCD module, obey the local environmental regulations.





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