



SPECIFICATION



TCG057QVLCSAFC-GA00

5.7" - QVGA - RGB

Version: Date: 01.02.2023

Note: This specification is subject to change without prior notice

www.data-modul.com

Spec No. TQ3C-8EAF0-E1YAN63-00 Date February 1, 2023 Date February 1, 2023 Spec No. Type: Tecgo57Qvicsafc-cgaoo < 5.7 inch QVGA transmissive color TFT with LED backlight and touch panel >								
 CONTENTS Application Construction and outline Mechanical specifications Absolute maximum ratings Electrical characteristics Optical characteristics Optical characteristics Interface signals Input timing characteristics Design guidance for analog touch panel Let number identification Warranty Precautions for use Reliability test data Outline drawing 								
KYOCERA CORPORATION This specification is subject to change without notice. Consult Kyocera before ordering.								
Original Issue Date		Engineering dep		Confirmed by:				
Issue Date	Prepared	Checked	Approved	Checked	Approved			
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CONFIDENTIAL					Kyocera			

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Warning

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.
- 2. Customer agrees to indemnity, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs, and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

Caution

- 1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.
- 2. Please note that we may not be able to respond to new environmental regulations after receiving the final mass production order for this product.

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)-E1YAN63-00	TCG057QVLCS	SAFC-GA00	
		R	evision 1				1
		Designed by	: Engineering	dept.	Confirmed by	v: QA dept.	
Ι	Date	Prepared	Checked	Approved	Checked	Approve	
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1. Application

This document defines the specification of TCG057QVLCSAFC-GA00. (RoHS Compliant)

2. Construction and outline

LCD	[:] Transmissive color dot matrix type TFT
Backlight system	: LED
Polarizer	: Glare treatment
Additional circuit	[:] Timing controller, Power supply (3.3V input)
	(without constant current circuit for LED backlight)
Touch panel	: Analog type, Non-Glare treatment

3. Mechanical specifications

3-1. LCD

Item		Specification	Unit
Outline dimensions 1	L)	127.2 (W)× 100.4 (H) ×6.75 (D)	mm
Active area		115.2 (W) × 86.4 (H) (14.4cm / 5.7 inch (Diagonal))	mm
Dot format		$320 \times (B,G,R)$ (W) $\times 240$ (H)	dot
Dot pitch		$0.12 (W) \times 0.36 (H)$	mm
Base color	2)	Normally White	-
Mass		135	g

1) Projection not included. Please refer to outline for details.

2) Due to the characteristics of the LCD material, the color varies with environmental temperature.

3-2. Touch panel

Item	Specification	Unit
Input	Radius-0.8 stylus or Finger	-
Actuation Force	$0.05{\sim}0.8$	Ν
Transmittance	Тур. 79	%
Surface hardness	Pencil hardness 2H or more according	-
Anti newton's ring treatment	None	-



4. Absolute maximum ratings

4-1. Electrical absolute maximum ratings

Item	Symbol	Min.	Max.	Unit
Supply voltage for logic	$V_{\rm DD}$	0	4.0	V
Input signal voltage 1)	$V_{\rm IN}$	-0.3	6.0	V
LED forward current 2)	3) IF	-	30	mA
Supply voltage for touch panel	VTP	0	6.0	V
Input current of touch panel	ITP	0	0.5	mA

- 1) Input signal : CK, R0~R5, G0~G5, B0~B5, Hsync, Vsync, ENAB, R/L, U/D
- 2) For each "AN-CA"
- 3) Do not apply reversed voltage.

4-2. Environmental absolute maximum ratings

Item		Symbol	Min.	Max.	Unit
Operating temperature	1)	TOP	-20	70	°C
Storage temperature	2)	Tsto	-30	80	°C
Operating humidity	3)	Нор	10	4)	%RH
Storage humidity	3)	$\mathrm{H}_{\mathrm{STO}}$	10	4)	%RH
Vibration		-	5)	5)	-
Shock		-	6)	6)	-

- 1) Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25°C, another temperature range should be confirmed.
- 2) Temp. = -30°C ${<}\,48h$, Temp. = 80°C ${<}\,168h$

Store LCD panels at normal temperature/humidity. Keep them free from vibration and shock. An LCD panel that is kept at a low or a high temperature for a long time can be defective due to other conditions, even if the low or high temperature satisfies the standard.

(Please refer to "Precautions for Use" for details.)

- 3) Non-condensing
- 4) Temp.≦40°C, 85%RH Max.
- Temp.>40°C, Absolute humidity shall be less than 85%RH at 40°C.

5)

$10\sim 55 \text{ Hz}$	Acceleration value
0.15mm	$(0.3 \sim 9 \text{ m/s}^2)$
10-55-10) Hz 1 minute

2 hours in each direction X, Y, Z (6 hours total) EIAJ ED-2531

6) 6 Acceleration: 490 m/s², Pulse width: 11 ms 3 times in each direction: ±X, ±Y, ±Z EIAJ ED-2531



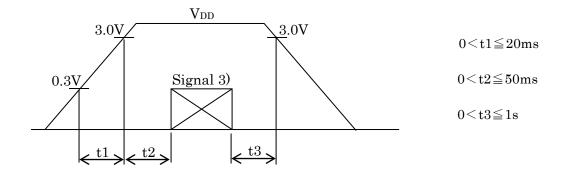
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5. Electrical characteristics

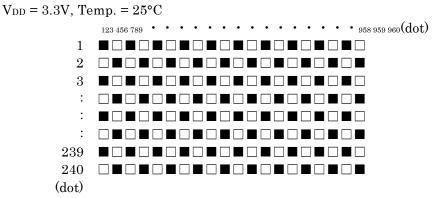
5-1. LCD

					Temp. = -2	$0\sim$ 70°C
Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Supply voltage 1)	VDD	-	3.0	3.3	3.6	V
Current consumption	Idd	2)	-	60	80	mA
Permissive input ripple voltage	V_{RP}	-	-	-	100	mVp-p
	V _{IL}	"Low" level	0	-	$0.3 V_{DD}$	V
Input signal voltage 3)	VIH	"High" level	$0.7 V_{DD}$	-	V _{DD}	V

1) V_{DD} -turn-on conditions



2) Display pattern:



3) Input signal : CK, R0~R5, G0~G5, B0~B5, H_{SYNC}, V_{SYNC}, ENAB, R/L, U/D

5-2. Touch panel

Item	Specification		
Supply voltage for touch panel	$5.0\mathrm{V}$		
	$xL\sim xR$: 200 Ω \sim 1,000 Ω		
Terminal resistance	$yU \sim yL$: 200 $\Omega \sim 800\Omega$		
Linearity	less than $\pm 2.0\%$ (when calibrated with 4 points)		
Insulation resistance	$100 \mathrm{M}\Omega$ or more at $\mathrm{DC25V}$		



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Measuring spot = ϕ 6.0mm, Temp. = 23							emp. = 25°C	
Item		Symbol	Condition	Min.	Тур.	Max.	Unit	
Descretions	Rise	τr	$\theta = \phi = 0^{\circ}$	-	10	-	ms	
Response time	Down	τd	$\theta = \phi = 0^{\circ}$	-	25	-	ms	
T7 ¹ 1		heta upper		-	80	-	1	
Viewing angle View direction	-	θ lower	$CR \ge 5$	-	80	-	deg.	
: 12 o'clo		ϕ left	$CR \leq 5$	-	80	-	1	
(Gray in	version)	ϕ right		-	80	-	deg.	
Contrast ratio		CR	$\theta = \phi = 0^{\circ}$	300	500	-	-	
Brightness		L	IF=15mA/Line	165	240	-	cd/m^2	
		x	0 - 1 - 00	0.57	0.62	0.67		
	Red	У	$\theta = \phi = 0^{\circ}$	0.32	0.37	0.42		
	0	x	$\theta = \phi = 0^{\circ}$	0.28	0.33	0.38		
Chromaticity	Green	У	$\theta = \phi = 0^{\circ}$	0.54	0.59	0.64		
coordinates	וח	x	0 - 1 -08	0.09	0.14	0.19	-	
	Blue	У	$\theta = \phi = 0^{\circ}$	0.04	0.09	0.14		
	White	х	$\theta = \phi = 0^{\circ}$	0.28	0.33	0.38		
	White	У	$v - \varphi = 0$	0.30	0.35	0.40		

6. Optical characteristics

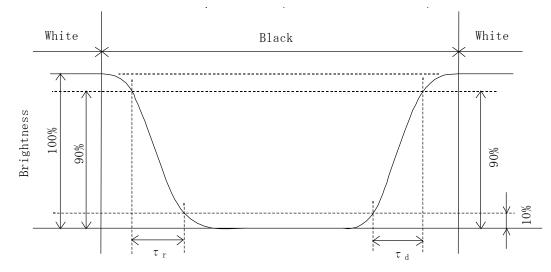
6-1. Definition of contrast ratio

CR(Contrast ratio) =

Brightness with all pixels "White"

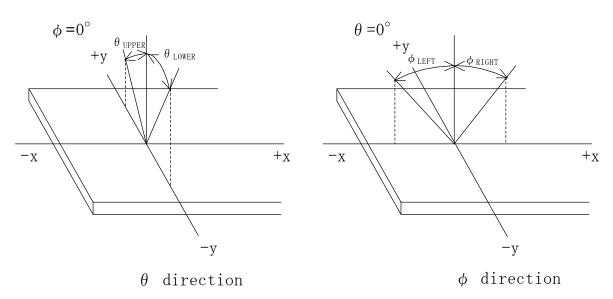
Brightness with all pixels "Black"

6-2. Definition of response time

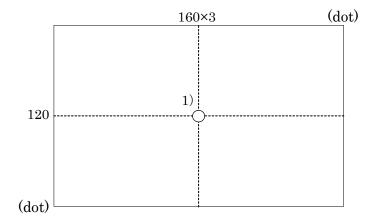




6-3. Definition of viewing angle



6-4. Brightness measuring point



- 1) Rating is defined as the white brightness at center of display screen.
- 2) Measured 5 minutes after the LED is powered on. (Ambient temp. = 25°C)



7. Interface signals

7-1. LCD

No.	Symbol	Description	I/O	Note
1	GND	GND	-	
2	СК	Clock signal for sampling each data signal	Ι	
3	H _{SYNC}	Horizontal synchronous signal (negative)	Ι	
4	VSYNC	Vertical synchronous signal (negative)	Ι	
5	GND	GND	-	
6	R0	RED data signal (LSB)	Ι	
7	R1	RED data signal	Ι	
8	R2	RED data signal	Ι	
9	R3	RED data signal	Ι	
10	R4	RED data signal	Ι	
11	R5	RED data signal (MSB)	Ι	
12	GND	GND	-	
13	G0	GREEN data signal (LSB)	Ι	
14	G1	GREEN data signal	Ι	
15	G2	GREEN data signal	Ι	
16	G3	GREEN data signal	Ι	
17	G4	GREEN data signal	Ι	
18	G5	GREEN data signal (MSB)	Ι	
19	GND	GND	-	
20	B0	BLUE data signal (LSB)	Ι	
21	B1	BLUE data signal	Ι	
22	B2	BLUE data signal	Ι	
23	B3	BLUE data signal	Ι	
24	B4	BLUE data signal	Ι	
25	B5	BLUE data signal (MSB)	Ι	
26	GND	GND	-	
27	ENAB	Signal to settle the horizontal display position (positive)	Ι	1)
28	V _{DD}	3.3V power supply	-	,
29	VDD	3.3V power supply	-	
30	R/L	Horizontal display mode select signal L : Normal , H : Left / Right reverse mode	Ι	2)
31	U/D	Vertical display mode select signal H : Normal , L : Up / Down reverse mode	Ι	2)
32	NC	No connect	-	
33	CA1	Cathode 1	-	
34	CA2	Cathode 2	-	
35	CA3	Cathode 3	-	
36	NC	No connect	-	
37	AN1	Anode 1	-	
38	AN2	Anode 2	-	
39	AN3	Anode 3	-	
40	NC	No connect	-	

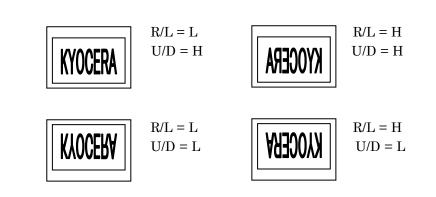
LCD connector Recommended matching FFC or FPC : 0.5mm pitch

: IMSA-9681S-40A-GF (IRISO)



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 The horizontal display start timing is settled in accordance with a rising timing of ENAB signal. In case ENAB is fixed "Low", the horizontal start timing is determined. Don't keep ENAB "High" during operation.



7-2. Touch panel

2)

No.	Symbol	Description
1	уL	y-Lower terminal
2	xL	x-Left terminal
3	yU	y-Upper terminal
4	xR	x-Right terminal

Touch panel side connector : 1.25mm pitch



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8. Input timing characteristics

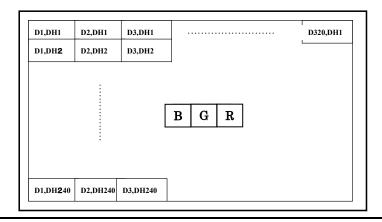
	Item	Symbol	Min.	Тур.	Max.	Unit	Note
Clock	Frequency	1/Tc	_	6.3	7.0	MHz	2)
	Duty ratio	Tch/Tc	40	50	60	%	
Data	Set up time	Tds	12			ns	
Data	Hold time	Tdh	12			ns	
	Cuelo	TH	50.0	63.6		μ s	
	Cycle	ΙП	360	400	450	clock	
Horizontal sync. signal	Pulse width	THp	5	30	_	clock	
Signal	Set up time	THs	12			ns	
	Hold time	THh	12	_	_	ns	
	Cycle	TV	251	262	280	line	
Vertical sync.	Pulse width	TVp	1	3	5	line	
signal	Set up time	TVs	2	_	_	clock	
	Hold time	TVh	12	_	_	ns	
	Pulse width	TEp		320		clock	
Enable signal (ENAB)	Set up time	TEs	12	_	_	ns	
	Hold time	TEh	12	_	_	ns	
H _{SYNC} - Enable signal phase difference		THE	36	68	88	clock	
Vertical sync. signal start position		TVE	2	18	38	line	
Horizontal displa	ay period	THd	320			clock	
Vertical display	period	TVd	240			line	

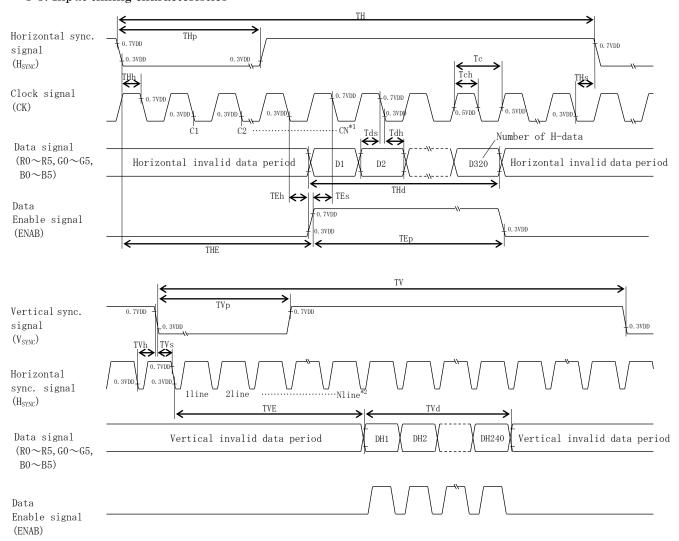
8-1. Timing characteristics 1)

1) If the display is used under the condition which is out of specifications such as higher clock frequency than specified value, there is a possibility phenomenon such as display error including white display, malfunction and no image may occur. Please use the display under the conditions written in the specification.

- 2) In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.
- 3) When ENAB is fixed at "Low", the horizontal display starts from the data of C68 (clock) as shown in 8-3.
- 4) When ENAB is fixed at "Low", the vertical sync. signal start position is 18 (line) as shown in 8-3.

8-2. Input data signals and display position on the screen





- 1) When ENAB is fixed at "Low", the horizontal display starts from the data of C68 (clock).
- 2) When ENAB is fixed at "Low", the vertical sync. signal start position is 18 (line).



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9. LED backlight characteristics

Item		Symbol	Min.	Тур.	Max.	Unit	Note
Forward current	1)	IF	-	15	-	mA	Ta=-20~70°C
			-	22.1	25.0	V	IF=15mA, Ta=-20°C
Forward voltage	1)	VF	-	21.7	24.5	V	IF=15mA, Ta=25°C
			-	21.3	24.1	V	IF=15mA, Ta=70°C
Operating life time	2) 3)	Т	-	40,000	-	h	IF=15mA, Ta=25°C

1) For each "AN-CA"

2) When brightness decrease 50% of minimum brightness.

3) Life time is estimated data. (Condition : IF=15mA, Ta= 25° C in chamber).

- 4) An input current below 5.0mA may reduce the brightness uniformity of the LED backlight. This is because the amount of light from each LED chip is different. Therefore, please evaluate carefully before finalizing the input current.
- 5) LED formation: 7 series, 3 parallel



10. Design guidance for analog touch panel

10-1. Electrical (In customer's design, please remember the following considerations.)

- 1) Do not use the current regulated circuit.
- Keep the current limit with top and bottom layer. (Please refer to "Electrical absolute maximum ratings" for details.)
- () Analog touch panel can not conce two points touching concertain
- 3) Analog touch panel can not sense two points touching separately.
- A contact resistance is appeared at the touch point between top and bottom layer. After this resistance has stable read of the touch panel position data.
- 5) Because noise of inverter or peripheral circuits may interfere signal of touch panel itself it is necessary to design carefully in advance to avoid these noise problem.

10-2. Software

- 1) Do the "User Calibration".
- "User Calibration" may be needed with long term using. Include "User Calibration" menu in your software.
- 3) When drawing a line with a stylus, there may be a slight discontinuity when the stylus passes over a spacer-dot. If necessary, please provide a compensation feature within your software.

10-3. Mounting on display and housing bezel

- 1) Do not use an adhesive tape to bond it on the front of touch panel and hang it to the housing bezel.
- 2) Never expand the touch panel top layer (PET-film) like a balloon by internal air pressure. The life of the touch panel will be extremely short
- 3) If a dew will be on the heat-sealed area or exposed traces at the end of a flexible tail, the migration of silver can occur. This will cause sometimes a short circuit.



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11. Lot number identification

The lot number shall be indicated on the back of the backlight case of each LCD.

ERA TCG057QVLCSAFC	$GA00:\Box\Box$			MADE IN
\downarrow	$\downarrow \downarrow$	\downarrow	\downarrow	\downarrow
1	23	4	5	6

No.① – No.⑥ above indicate
① Data matrix (For internal control purpose only)
② Year code (The last digit of the year)
③ Month code
④ Day code
5 Version number (Max. 7 characters)
6 Country of origin

3 Month code

Month	Jan.	Feb.	Mar.	Apr.	May	Jun.
Code	1	2	3	4	5	6

Month	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Code	7	8	9	Х	Y	Z

12. Warranty

12-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

12-2. Production warranty

Kyocera warrants its LCD's for a period of 12 months from the ship date. Kyocera shall, by mutual agreement, replace or re-work defective LCD's that are shown to be Kyocera's responsibility.



13. Precautions for use

- 13-1. Installation of the LCD
 - 1) The LCD shall be installed so that there is no pressure on the LSI chips.
 - 2) The LCD shall be installed flat, without twisting or bending.
 - 3) Must maintain a gap between inside of bezel and touch panel to avoid malfunction or electrode damage of touch panel.

13-2. Static electricity

- 1) Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required.
- 2) Workers should use body grounding. Operator should wear ground straps.

13-3. LCD operation

1) The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.

13-4. Storage

- 1) The LCD shall be stored within the temperature and humidity limits specified. Store in a dark area, and protect the LCD from direct sunlight or fluorescent light.
- 2) Always store the LCD so that it is free from external pressure onto it.

13-5. Usage

- 1) <u>DO NOT</u> store in a high humidity environment for extended periods. Polarizer degradation bubbles, and/or peeling off of the polarizer may result.
- 2) Do not push or rub the touch panel's surface with hard to sharp objects such as knives, or the touch panel may be scratched.
- 3) When the touch panel is dirty, gently wipe the surface with a soft cloth, sometimes moistened by mild detergent or alcohol. If a hazardous chemical is dropped on the touch panel by mistake, wipe it off right away to prevent human contact.
- 4) Touch panel edges are sharp. Handle the touch panel with enough care to prevent cuts.
- 5) Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizer.
- 6) Do not disassemble LCD module because it will result in damage.
- 7) This Kyocera LCD module has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas. Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.
- 8) Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend using screen saver etc. in cases where a solid-base image pattern must be used.
- 9) Liquid crystal may leak when the module is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body; rinse it off right away with water and soap.



14. Reliability test data

Test item	Test condition	Test time	Jud	gement
High temp. atmosphere	80°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Low temp. atmosphere	-30°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. humidity atmosphere	40°C 90% RH	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Temp. cycle	-30°C 0.5h R.T. 0.5h 80°C 0.5h	10cycles	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. operation	70°C	500h	Display function Display quality Current consumption	: No defect : No defect : No defect
Point Activation life	Silicon rubber, Tip : R = 4.0 Hitting force 3N Hitting speed 2 time/s	one million times	Terminal resistance Insulation resistance Linearity Actuation Force	 No defect No defect No defect No defect

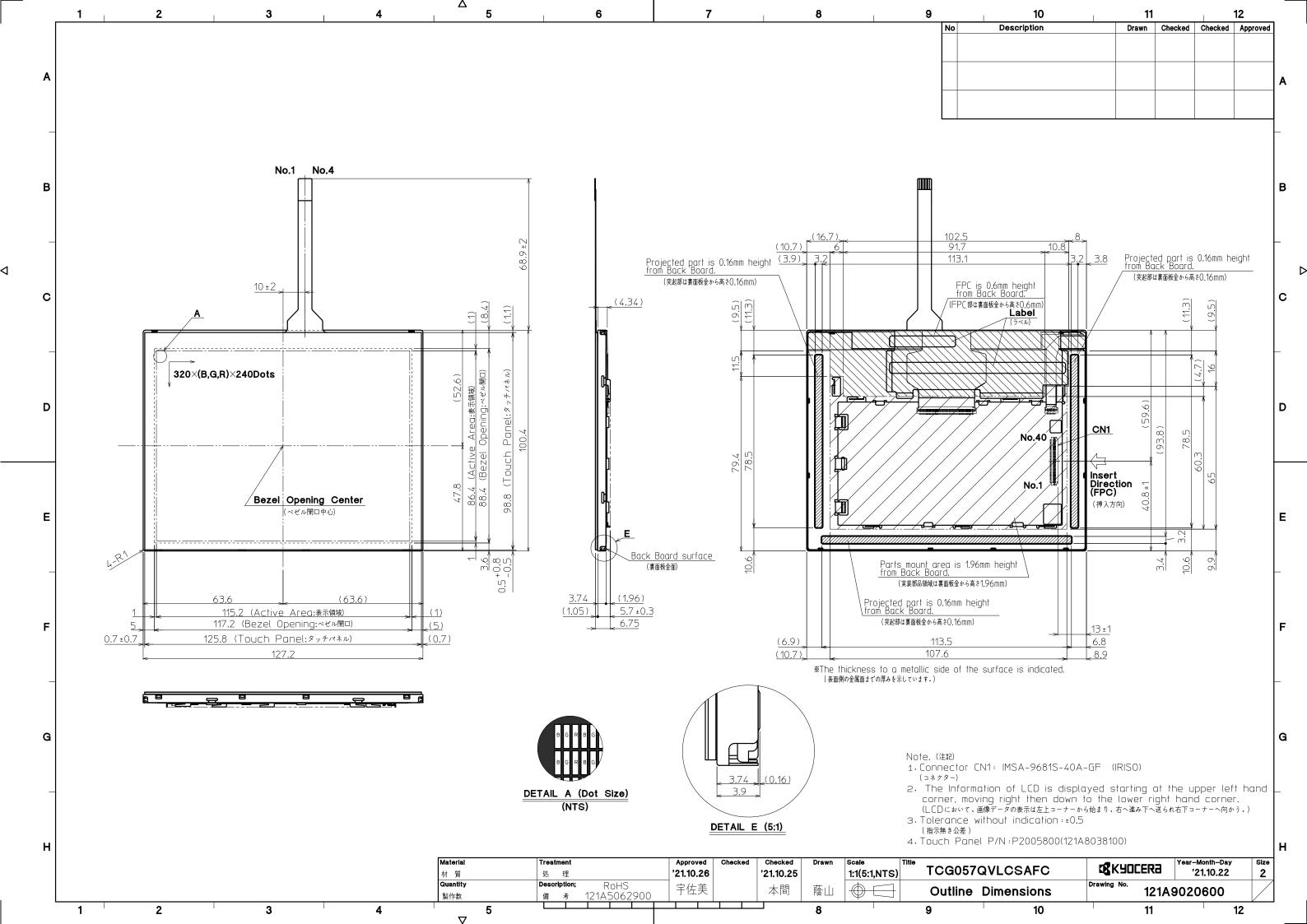
1) Each test item uses a test LCD only once. The tested LCD is not used in any other tests.

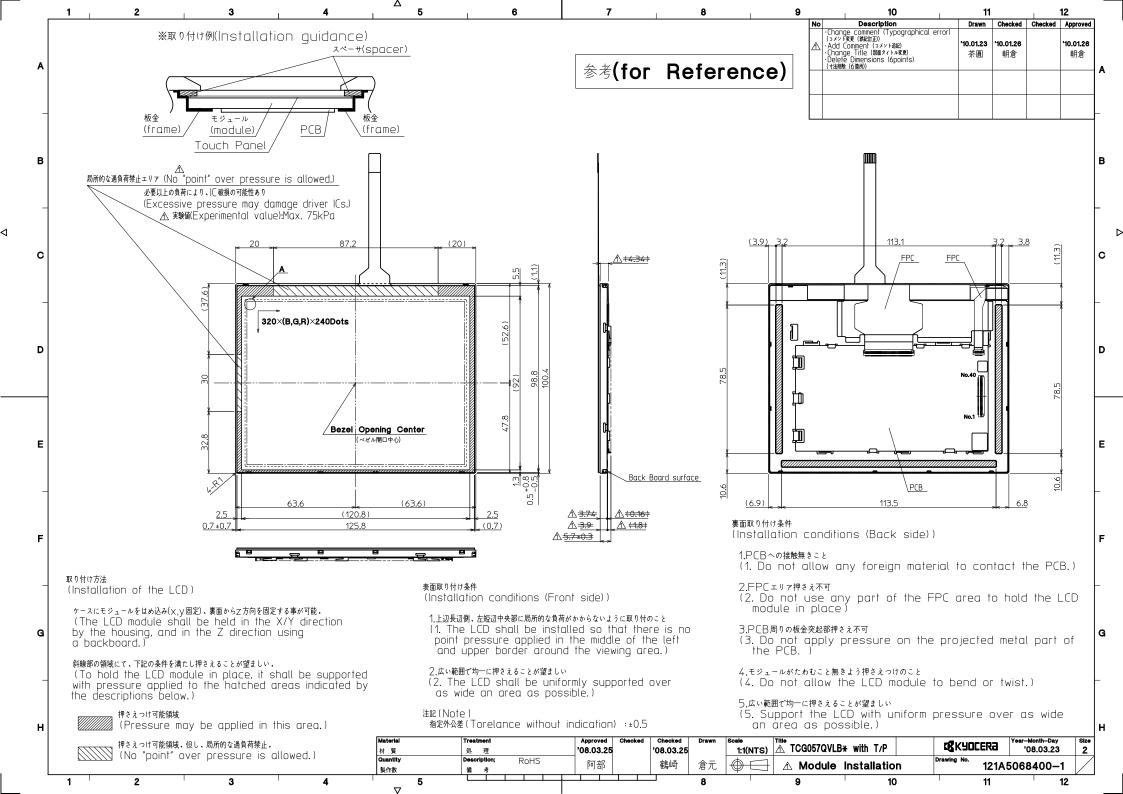
2) The LCD is tested in circumstances in which there is no condensation.

3) The reliability test is not an out-going inspection.

 The result of the reliability test is for your reference purpose only. The reliability test is conducted only to examine the LCD's capability.



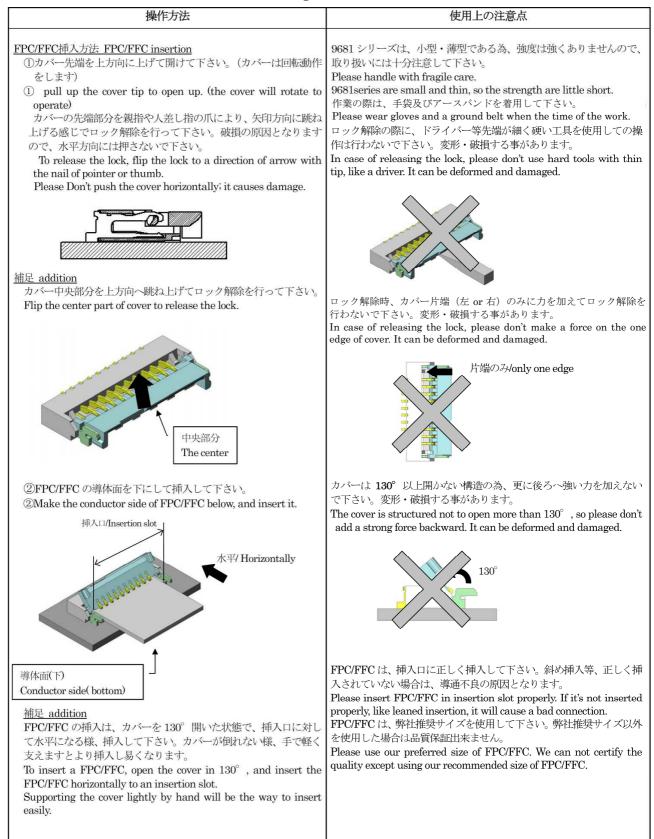




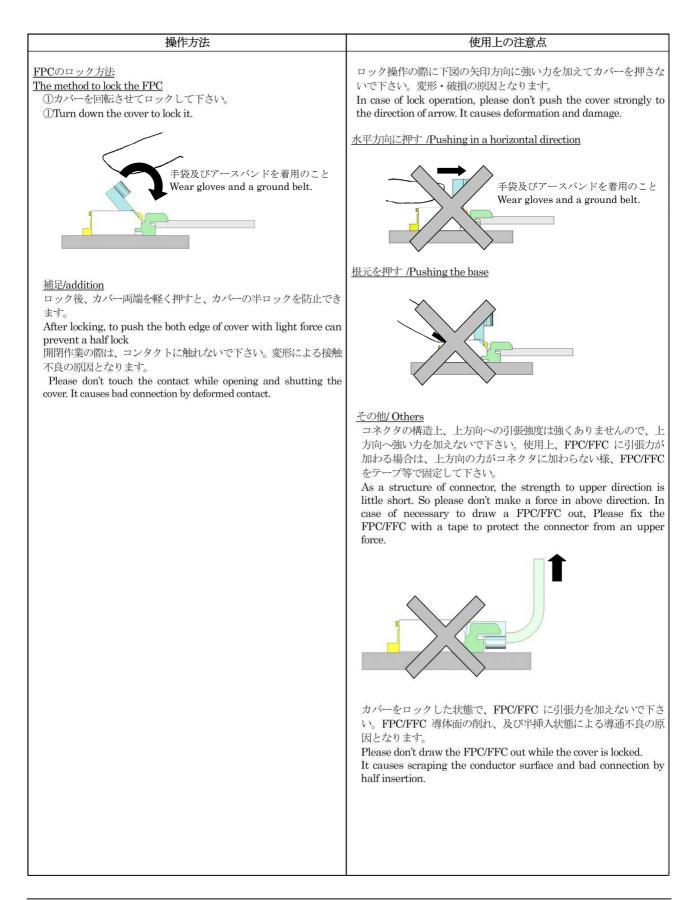
002-110930-1

参考(for Reference)

<u>IRISO 製 9681 シリーズコネクタの取り扱い上の注意</u> Precautions when using IRISO.9681 series connector



イリソ電子工業株式会社作成の資料より引用 Refer to the data made by IRISO ELECTRONICS CO., LTD.



Spec No.	TQ3C-8EAF0-E2YAN63-00
Date	February 1, 2023

KYOCERA INSPECTION STANDARD

TYPE : TCG057QVLCSAFC-GA00

KYOCERA CORPORATION

Original	Designed by :	Engineering de	Confirmed by : QA dept.		
Issue Date	Prepared	Checked	Approved	Checked	Approved
February 1, 2023	T. Onodera	I. Kawajiri	A. Iwasaki	Y. Aritsubo	M. Aoyama



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Visuals specification

1) Note

1) Note							
			Note				
General	1. Customer identified anomalies not defined within this inspection standard shall be						
	reviewed by Kyocera, and an additional standard shall be determined by mutual consent.						
	2. This inspection standard about the image quality shall be applied to any defect within the						
	active area and shall not be applicable to outside of the area.						
	3. Inspec	tion conditions					
	Lumin	ance	: 500 Lux min.				
	Inspec	tion distance	: 300 mm.				
	Tempe	erature	$:25 \pm 5^{\circ}$ C				
	Direct	ion	: Directly above				
Definition of	Dot defect	Bright dot defect	The dot is constantly "on" when power applied to the				
inspection			LCD, even when all "Black" data sent to the screen.				
item			Inspection tool: 5% Transparency neutral density				
			filter.				
			Count dot: If the dot is visible through the filter.				
			Don't count dot: If the dot is not visible through the				
			filter.				
			RGBRGBRGB				
			R G B R G B R G B				
		Black dot defect	The dot is constantly "off" when power applied to the				
			LCD, even when all "White" data sent to the screen.				
			Similar size compared to bright dot.				
		White dot	Pixel works electrically, however, circular/foreign				
		(Circular/foreign particle)	particle makes dot appear to be "on" even when all				
			"Black" data is sent to the screen.				
		Adjacent dot	Adjacent dot defect is defined as two or more bright				
		U	dot defects or black dot defects.				
			RGBRGBRGB				
	External	Bubble, Scratch, Foreign	Visible operating (all pixels "Black" or "White") and				
	inspection	particle	non operating.				
		(Polarizer, Cell, Backlight)					
		Appearance inspection	Does not satisfy the value at the spec.				
	Definition	Definition of circl					
	of size	A	····				
		~ vy					
		~ ~ ~ ~	<mark>≪ </mark>				
		\checkmark					
		a: major axis, b: mi	nor axis				
		d = (a + b) / 2	2				



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2) Standard

2) Standa Classif	ication	Inspect	tion item		Judgement	standar	d
Defect	Single	Bright dot		Acceptable number : 4			-
(in LCD	dot	8		Bright dot spacing		: 5 mm	or more
glass)		Black dot	defect	Acceptable number : 5			
8-0000		Diaon aou		Black dot spacing			or more
	Adjacent dot	2 dots	Bright dot defect	Acceptable number		: 2	
			Black dot defect	Acceptable number		: 3	
		3 or more	dots	Acceptable number		:0	
	Total dot o	lefects		Acceptable number		: 5 Max	ζ
	Others	White dot,	Dark dot				
		(Circle)		Size (mm	l)	Ace	ceptable number
				d ≦			(Neglected)
				0.2 < d \leq			5
				0.4 < d ≦	0.5		3
				0.5 < d			0
External	inspection	Polarizer (Scratch)				
(Defect on	L			Width (mm)	Length (mm)	Acceptable number
Polarizer	or			$W \leq 0.1$		< 7 0	(Neglected)
between F	Polarizer			$0.1 < W \leq 0.3$	$\frac{L}{5.0 < L}$	≤ 5.0	(Neglected) 0
and LCD	glass)			0.3 < W -			0
				0.0 < 11			0
		Polarizer (Bubble)	g: ()		
				$\frac{\text{Size (mm}}{d} \leq \frac{1}{2}$		Ace	(Namber d)
				$d \cong 0.2 < d \cong$			(Neglected) 5
				$\begin{array}{c ccccccccccccccccccccccccccccccccccc$			3
				0.5 < d = 0.5 < d	0.0		0
				0.0 (u			0
		Foreign pa		~· (\ \		
		(Circular shape)				Acceptable number	
					≤ 0.2		(Neglected)
				$\begin{array}{c ccccccccccccccccccccccccccccccccccc$			<u>5</u> 3
				$0.4 < d \ge 0.5 < d$	0.0		0
				0.0 \ u			0
		Foreign pa			T (1	()	A (11 1
		(Linear s	hape)	$\frac{\text{Width (mm)}}{\text{W} \le 0.02}$	Length	(mm)	Acceptable number
		Scratch		$W \leq 0.03$	1	≤ 2.0	(Neglected) (Neglected)
				$0.03 < W \leq 0.1$	2.0 < L		(ivegrected)
					4.0 < L		0
				0.1 < W			(According to
							circular shape)
		Color varia (Mura)	ation	Not to be significantly Consultation shall be	-		



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Inspection item		Judgement	standard					
Scratch,	Item	Width(mm)	Length(mm)	Ac	ceptable number			
Foreign particle		$W \leq 0.03$	$L \leq 20$		Neglected			
(Touch panel	Scratch	$0.03~<~{ m W} \leq 0.05$	$L \leq 10$	2pc	cs within φ20mm			
portion)	(Linear shape)	$0.05~<~{ m W} \leq 0.08$	$L \leq 6$	2pc	cs within φ20mm			
		$0.08 < \mathrm{W} \leq 0.1$	$L \leq 4$	1p	cs within φ30mm			
	Foreign	$W \leq 0.05$	Neglected		Neglected			
	(line like)	$0.05 < \mathrm{W} \leq 0.1$	$L \leq 5$	2pc	es within ϕ 30mm			
	Foreign	$d \leq$	0.2		Neglected			
	(Circular shape)	$0.2 < d \leq$	0.3	2pc	es within ϕ 30mm			
	Above are applied	to the visible area.		_				
	Unless there are	e foreign particle and da	mage affected	seriou	sly to the electrical			
	performance out o	f the active area, we approv	e of this produc	:t.				
Glass crack	(t = Glass thicknes	s)						
(Touch panel		.,			A (11			
portion)	Item	Size (mn	n)		Acceptable number			
_								
				≤ 3				
	Corner crack		Y	≤ 3	2 pcs			
			-		/panel			
			77	<t				
		\checkmark	Z	<τ				
			1/	<i>_</i>				
	Crack in	$+$ \times $/$	X	≤ 5				
	other area				2 pcs			
	than in		Y	≤ 1.5	/side			
	corner							
		\checkmark	Z	<t				
			II					
	Progressive							
	crack	\sim						
	Above are applied to the visible area.							
	Unless there are	e foreign particle and da	mage affected	seriou	sly to the electrical			
	performance out o	f the active area, we approv	e of this produc	:t.				
Newton's ring	Neglected.							
			N N	Vewton'	's ring			



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Date	February 1, 2023

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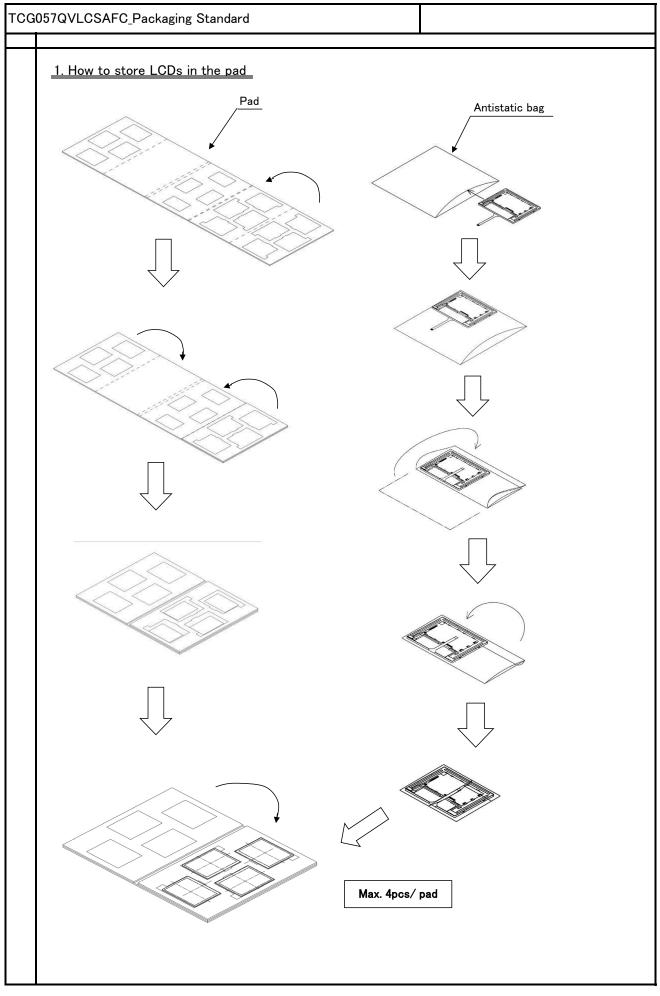
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Original Issue Date	Designed by :	Engineering de	Confirmed by : QA dept.		
	Prepared	Checked	Approved	Checked	Approved
February 1, 2023	T. Onodera	I. Kawajiri	A. Iwasaki	Y. Aritsubo	M. Aoyama

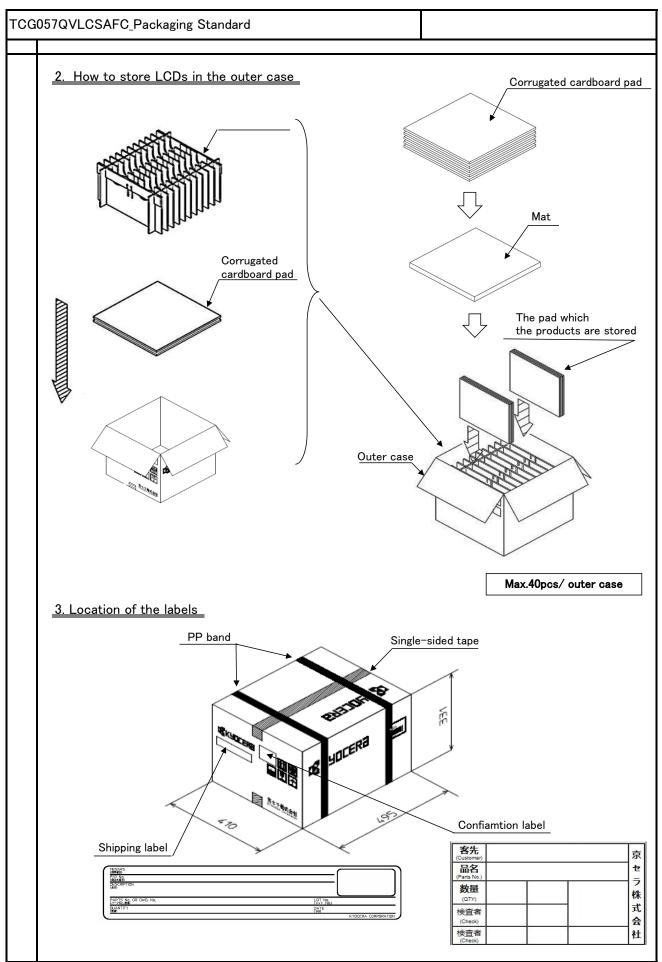


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