



SPECIFICATION



TCG062HVLDB-G20

6.2" - HVGA - RGB

Version: 04

Date: 29.11.2023

Note: This specification is subject to change without prior notice

www.data-modul.com

SPEC for Mass Production

Spec No.	TQ3C-8EAF0-E1DEY16-04
Date	November 29, 2023

TYPE: TCG062HVLDB-G20

< 6.2 inch HVGA transmissive color TFT with LED backlight, constant current circuit for LED backlight and touch panel>

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KYOCERA CORPORATION

This specification is subject to change without notice.

Consult Kyocera before ordering.

Original	Designed by: Engi	Confirmed by: QA dept.		
Issue Date	Prepared	Checked	Approved	Approved
January 18, 2010	Y. Yamazaki	T. Fukui	A. Iwasaki	T. Sawada



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Warning

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control, home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.
- 2. Customer agrees to indemnify, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs, and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

Caution

- 1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.
- 2. Please note that we may not be able to respond to new environmental regulations after receiving the final mass production order for this product.



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Revision record

			Rev	ision reco	ord			
Date November 29, 2023		Design	Igned by : Engineering dept. Confirm QA dep					
		Pre	pared	Checked	Approved	Approved		
		Y. Yamazaki		T. Fukui	A. Iwasaki	T. Sawada		
Rev.No.	Date	Page		De	escriptions			
01	Nov 26, 2019	_	Touch par	nel: Film change	-			
		_		Cover page Changed the company name.				
		3		h panel l resistance $xL\sim x$ $1,100\Omega \rightarrow 800\Omega\sim 2$				
		8		ng characteristics note about clock fi	requency.			
		13		umber identificatio data matrix and c		otion.		
02	Oct 3, 2022	3		note about lineari				
		5	Change	ntness measuring p d from 30minutes t	o 5 minutes.			
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03	Dec 19, 2022	14		he outline drawing tallation of the LCI				
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04	Nov. 29, 2023	3	5-2. Touc		s protection min.			
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1. Application

This document defines the specification of TCG062HVLDB-G20. (RoHS Compliant)

2. Construction and outline

LCD : Transmissive color dot matrix type TFT

Backlight system : LED

Polarizer : Glare treatment

Additional circuit : Timing controller, Power supply (3.3V input)

(with constant current circuit for LED Backlight)

Touch panel : Analog type, Non-Glare treatment

3. Mechanical specifications

3-1. LCD

Item	Specification	Unit
Outline dimensions 1)	173(W)×70(H)×7.75(D)	
Active area	147.84(W)×55.44(H) (15.8cm/6.2 inch(Diagonal))	mm
Effective viewing area	149.8(W)×57.4(H)	mm
Dot format	640×(B,G,R)(W)×240(H)	dot
Dot pitch	0.077(W)×0.231(H)	mm
Base color 2)	Normally White	
Mass	135	

- 1) Projection not included. Please refer to outline for details.
- 2) Due to the characteristics of the LCD material, the color varies with environmental temperature.

3-2. Touch panel

Item	Specification	Unit
Input	Radius-0.8 stylus or Finger	-
Actuation Force	0.05~0.8	N
Transmittance	Typ. 79	%
Surface hardness	Pencil hardness 2H or more according	-
Anti newton's ring treatment	None	-



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4. Absolute maximum ratings

4-1. Electrical absolute maximum ratings

Item	Symbol	Min.	Max.	Unit
Supply voltage for logic	V_{DD}	0	4.0	V
Input signal voltage 1)	$V_{\rm IN}$	-0.3	6.0	V
Supply voltage for backlight	$V_{\rm IN}B$	0	6.0	V
Backlight ON-OFF	BLEN	0	$V_{\mathrm{IN}}\mathrm{B}$	V
Brightness adjust voltage	VBRT	0	$V_{\mathrm{IN}}\mathrm{B}$	V
Supply voltage for touch panel	V_{TP}	0	6.0	V
Input current of touch panel	I_{TP}	0	0.5	mA

1) Input signal: CK, R0~R5, G0~G5, B0~B5, H_{SYNC}, V_{SYNC}, ENAB, R/L, U/D

4-2. Environmental absolute maximum ratings

Item		Symbol	Min.	Max.	Unit
Operating temperature	1)	Тор	-20	70	$^{\circ}\mathrm{C}$
Storage temperature	2)	T_{STO}	-30	80	$^{\circ}\mathrm{C}$
Operating humidity	3)	Нор	10	4)	%RH
Storage humidity	3)	Hsto	10	4)	%RH
Vibration		-	5)	5)	-
Shock		-	6)	6)	-

- 1) Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25°C, another temperature range should be confirmed.
- 2) Temp. = -30°C < 48h, Temp. = 80°C < 168hStore LCD at normal temperature/humidity. Keep them free from vibration and shock. An LCD that is kept at a low or a high temperature for a long time can be defective due to other conditions, even if the low or high temperature satisfies the standard. (Please refer to "Precautions for Use" for details.)
- 3) Non-condensing
- 4) Temp. ≤ 40°C, 85%RH Max.

Temp. > 40°C, Absolute humidity shall be less than 85%RH at 40°C.

5)

Frequency	$10{\sim}55~\mathrm{Hz}$	Acceleration value
Vibration width	0.15mm	$(0.3\sim 9 \text{ m/s}^2)$
Interval	10-55-10	Hz 1 minutes

2 hours in each direction X, Y, Z (6 hours total) EIAJ ED-2531

6) Acceleration: 490 m/s 2 , Pulse width: 11 ms

3 times in each direction: $\pm X$, $\pm Y$, $\pm Z$

EIAJ ED-2531



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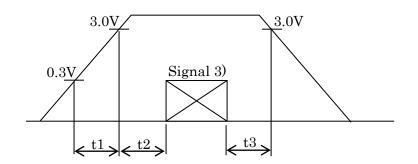
5. Electrical characteristics

5-1. LCD

Temp. = $-20 \sim 70$ °C

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Supply voltage 1)	$V_{ m DD}$	-	3.0	3.3	3.6	V
Current consumption	${ m I}_{ m DD}$	2)	-	130	170	mA
Permissive input ripple voltage	V_{RP}	-	-	-	100	mVp-p
I	$ m V_{IL}$	"Low" level	0	•	$0.3V_{\mathrm{DD}}$	V
Input signal voltage 3)	V_{IH}	"High" level	$0.7 V_{\mathrm{DD}}$	-	$ m V_{DD}$	V

1) V_{DD}-turn-on conditions



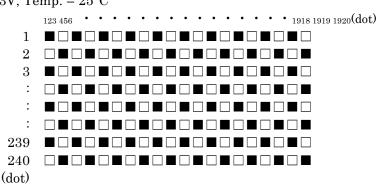
 $0 \le t1 \le 20 ms$

 $0 \le t2 \le 50 \text{ms}$

 $0 \le t3 \le 1s$

2) Display pattern:

$$V_{DD} = 3.3V$$
, Temp. = 25°C



3) Input signal: CK, R0~R5, G0~G5, B0~B5, H_{SYNC}, V_{SYNC}, ENAB, R/L, U/D

5-2. Touch panel

Item	Specification		
Supply voltage for touch panel	5.0V		
m · 1 · .	$xL\sim xR:800\Omega\sim 2,000\Omega$		
Terminal resistance	yU~yL: 100Ω~300Ω		
Linearity	less than ±2.0% (when calibrated with 4 points)		
Insulation resistance	$100 \mathrm{M}\Omega$ or more at DC25V		



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6. Optical characteristics

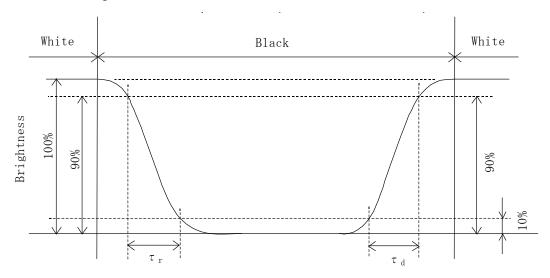
Measuring spot = ϕ 6.0mm, Temp. = 25°C

Item		Symbol	Condition	Min.	Тур.	Max.	Unit	
D .:	Rise	Τr	$\theta = \phi = 0$ °	-	15	-	ms	
Response time	Down	τd	$\theta = \phi = 0$ °	-	20	-	ms	
T	,			-	80	-	1	
Viewing angle View direction	range	θ lower	CR≧5	-	80	-	deg.	
: 6 o'cloc		ϕ LEFT	CR≦5	-	80	-	1	
(Gray inversion)		φ right		-	80	-	deg.	
Contrast ratio		CR	$\theta = \phi = 0$ °	300	500	-	-	
Brightness		L	IF=15mA/Line	165	240	-	cd/m²	
	Red	X	$\theta = \phi = 0$ °	0.55	0.60	0.65		
		У		0.31	0.36	0.41		
	C	X	$\theta = \phi = 0^{\circ}$	0.31	0.36	0.41		
Chromaticity	Green	У	$\theta - \phi - 0$	0.52	0.57	0.62		
coordinates	DI	X	$\theta = \phi = 0^{\circ}$	0.10	0.15	0.20	-	
	Blue	У	$\theta - \phi - 0$	0.08	0.13	0.18		
	XX71- : 4 -	X	0 - 4 -00	0.28	0.33	0.38		
	White	У	$\theta = \phi = 0^{\circ}$	0.30	0.35	0.40		

6-1. Definition of contrast ratio

 $CR(Contrast ratio) = \frac{Brightness with all pixels "White"}{Brightness with all pixels "Black"}$

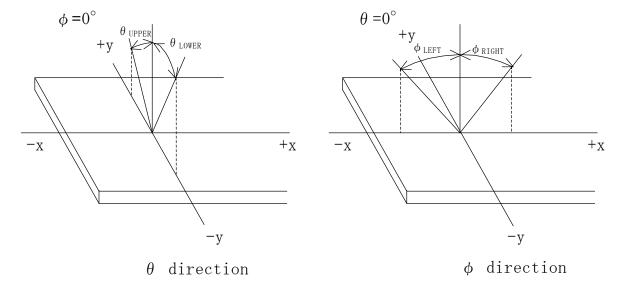
6-2. Definition of response time



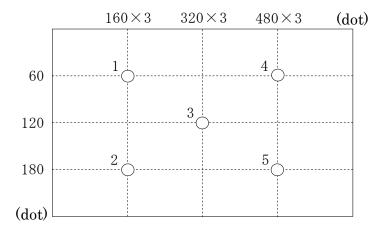


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6-3. Definition of viewing angle



6-4. Brightness measuring points



- 1) Rating is defined on the average in the viewing area. (measured point $1\sim5$)
- 2) Measured 5 minutes after the LED is powered on. (Ambient temp. = 25°C)



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7. Interface signals

7-1. LCD

No.	Symbol	Description	I/O	Note
1	GND	GND	-	
2	CK	Clock signal for sampling each data signal	I	
3	H_{SYNC}	Horizontal synchronous signal (negative)	I	
4	$V_{ m SYNC}$	Vertical synchronous signal (negative)	I	
5	GND	GND	-	
6	R0	RED data signal (LSB)	I	
7	R1	RED data signal	I	
8	R2	RED data signal	I	
9	R3	RED data signal	I	
10	R4	RED data signal	I	
11	R5	RED data signal (MSB)	I	
12	GND	GND	-	
13	G0	GREEN data signal (LSB)	I	
14	G1	GREEN data signal	I	
15	G2	GREEN data signal	I	
16	G3	GREEN data signal	I	
17	G4	GREEN data signal	I	
18	G5	GREEN data signal (MSB)	I	
19	GND	GND	-	
20	В0	BLUE data signal (LSB)	I	
21	B1	BLUE data signal	I	
22	B2	BLUE data signal	I	
23	В3	BLUE data signal	I	
24	B4	BLUE data signal	I	
25	B5	BLUE data signal (MSB)	I	
26	GND	GND	-	
27	ENAB	Signal to settle the horizontal display position (positive)	I	1)
28	$V_{ m DD}$	3.3V power supply	-	
29	$V_{ m DD}$	3.3V power supply	-	
30	R/L	Horizontal display mode select signal	I	2)
	10/12	H: Normal, L: Left / Right reverse mode	-	-/
31	U/D	Vertical display mode select signal	I	2)
20	NO	H: Normal, L: Up / Down reverse mode	_	
32	NC V. D	No connect	<u> </u>	
33	V _{IN} B	Power supply for LED backlight	_	
34	V _{IN} B	Power supply for LED backlight Power supply for LED backlight		
35	V _{IN} B	Backlight ON-OFF (H: ON, L: OFF)	-	
36	BLEN	Brightness adjust voltage	<u> </u>	
37	VBRT	GND for LED backlight	 _	
38	GNDB GNDB	GND for LED backlight GND for LED backlight	_	
39		GND for LED backlight GND for LED backlight	<u> </u>	
40	GNDB	GIAD TOL TED BECKLISH		<u> </u>

LCD connector : IMSA-9681S-40A-GF (IRISO)

Recommended matching FFC or FPC : 0.5mm pitch



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1) The horizontal display start timing is settled in accordance with a rising timing of ENAB signal. In case ENAB is fixed "Low", the horizontal start timing is determined.

Don't keep ENAB "High" during operation.

2)



$$R/L = H$$

 $U/D = H$



$$R/L = L$$

 $U/D = H$



$$R/L = H$$

 $U/D = L$



$$R/L = L$$
$$U/D = L$$

7-2. Touch panel

No.	Symbol	Description
1	xR	x-Right terminal
2	уL	y-Lower terminal
3	хL	x-Left terminal
4	уU	y-Upper terminal

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8. Input timing characteristics

8-1. Timing characteristics 1)

	Item	Symbol	Min	Тур	Max	Unit	Note
Clash	Frequency	1/Tc	22.66	25.18	27.69	MHz	2)
Clock	Duty ratio	Tch/Tc	40	50	60	%	
Data	Set up time	Tds	5	_	_	ns	
Data	Hold time	Tdh	10	_	_	ns	
Horizontal sync.	Cycle	/DIT	30.0	31.8	_	μ s	
		TH	770	800	850	clock	
Signai	Pulse width	ТНр	2	96	200	clock	
Vertical sync.	Cycle	TV	515	525	560	line	
signal	Pulse width	TVp	2	_	34	line	
Horizontal displa	ny period	THd	640		clock		
H _{SYNC} – Clock ph	ase difference	THc	10	_	Tc-10	ns	
Hsync - Vsync signal phase difference		TVh	2Tc	_	TH-THp-1	ns	
Vertical sync. signal start position		TVs	34			line	
Vertical display p	period	TVd		240		line	

- 1) If the display is used under the condition which is out of specifications such as higher clock frequency than specified value, there is a possibility phenomenon such as display error including white display, malfunction and no image may occur.
 - Please use the display under the conditions written in the specification.
- 2) In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.

8-2. Horizontal display position

Item		Symbol	Min	Тур	Max	Unit	Note
Enghlosianal	Set up time	Tes	5	_	Tc-10	ns	
Enable signal	Pulse width	Тер	2	640	TH-10	clock	
H _{SYNC} – Enable signal phase difference		The	44		TH-664	clock	

- 1) When ENAB is fixed at "Low", the display starts from the data of C104(clock) as shown in 8-5.
- 2) The horizontal display position is determined by ENAB signal.

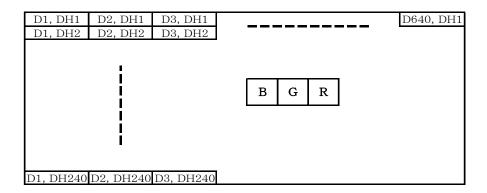
8-3. Vertical display position

- 1) The vertical display position (TVs) is 34th line.
- 2) ENAB signal is independent of vertical display position.

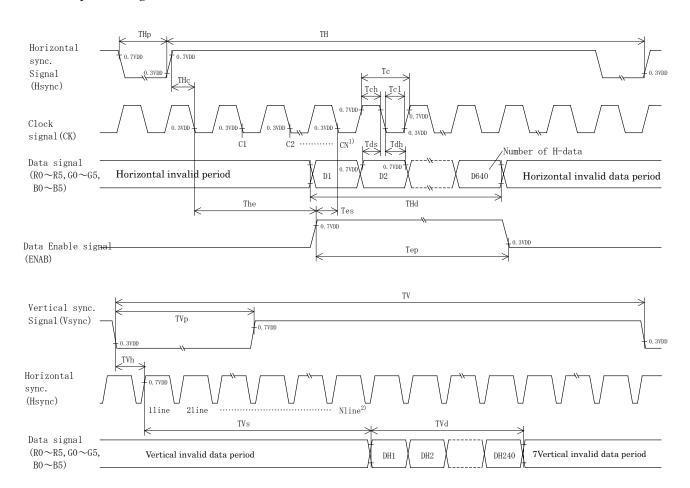


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8-4. Input Data Signals and Display position on the screen



8-5. Input timing characteristics



- 1) When ENAB is fixed at "Low", the display starts from the data of C104(Clock).
- 2) The vertical display position(TVs) is fixed at 34th line.



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9. Backlight characteristics

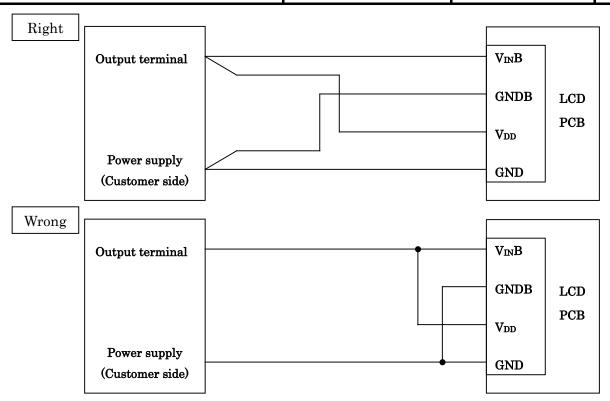
Temp.=25℃

Item	Symbol	Min.	Тур.	Max.	Unit	Note
Supply voltage	$V_{\rm IN} B$	3.0	-	5.5	V	Ta=-20∼70°C
ON-OFF (H)	BLEN	$0.8 V_{\mathrm{IN}} \mathrm{B}$	-	$V_{\mathrm{IN}}\mathrm{B}$	V	-
ON-OFF (L)	DLEN	0.0	-	$0.2 m V_{IN} B$	V	-
LED forward current	110	14	15	16	4	VBRT=0∼1.4V
1) 2)	IF	2.8	3.0	3.2	mA	VBRT=2.8V
Consider comment	I D	-	500	650	A	V _{IN} B =3.3V, IF=15mA
Supply current	$I_{IN}B$	-	320	420	mA	V _{IN} B =5.0V, IF=15mA
Operating life 3) 4)	Т	-	40,000	-	h	IF=15mA, Ta=25℃

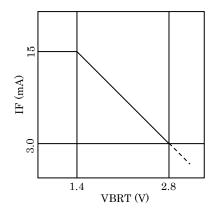
- 1) For each LED.
- 2) A forward current below 5.0mA may reduce the brightness uniformity of the LED backlight. This is because the amount of light from each LED chip is different. Therefore, please evaluate carefully before finalizing the input current.
- 3) When brightness decrease 50% of minimum brightness.
 The average life of a LED will decrease when the LCD is operating at higher temperatures.
- 4) Life time is estimated data. (Condition : IF=15mA, Ta=25 $^{\circ}$ C in chamber).
- 5) When you start-up, please charge in sequence of $V_{IN}B$ ->BLEN, or VBRT. When you shut-down, please stop in sequence of BLEN and/or VBRT-> $V_{IN}B$.
- 6) Please do not connect the other than our backlight to this output connector on the PCB.
- 7) In case V_{DD} and $V_{IN}B$ are supplied by a single power source, V_{DD} & $V_{IN}B$, and GND are connected directly and separately from the output on the power source. If the common wire are used for V_{DD} & $V_{IN}B$, and for GND, and are split near the PCB, and connect to each LCD driving circuit and backlight driving circuit, a flicker might be occurred due to a ripple between the both circuit.



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8) VBRT-IF characteristics



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10. Design guidance for analog touch panel

- 10-1. Electrical (In customer's design, please remember the following considerations.)
 - 1) Do not use the current regulated circuit.
 - 2) Keep the current limit with top and bottom layer.(Please refer to "Electrical absolute maximum ratings" for details.)
 - 3) Analog touch panel cannot sense two points touching separately.
 - 4) A contact resistance is appeared at the touch point between top and bottom layer. After this resistance has stable read of the touch panel position data.
 - 5) Because noise of inverter or peripheral circuits may interfere signal of touch panel itself it is necessary to design carefully in advance to avoid these noise problem.

10-2. Software

- 1) Do the "User Calibration".
- 2) "User Calibration" may be needed with long term using. Include "User Calibration" menu in your software.
- 3) When drawing a line with a stylus, there may be a slight discontinuity when the stylus passes over a spacer-dot. If necessary, please provide a compensation feature within your software.

10-3. Mounting on display and housing bezel

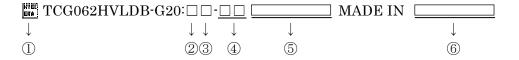
- 1) Do not use an adhesive tape to bond it on the front of touch panel and hang it to the housing bezel.
- 2) Never expand the touch panel top layer (PET-film) like a balloon by internal air pressure. The life of the touch panel will be extremely short.
- 3) If a dew will be on the heat-sealed area or exposed traces at the end of a flexible tail, the migration of silver can occur. This will cause sometimes a short circuit.
- 4) Must maintain a gap between inside of bezel and touch panel to avoid malfunction or electrode damage of touch panel.



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11. Lot number identification

The lot number shall be indicated on the back of the backlight case of each LCD.



No① - No⑥ above indicate

- ① Data matrix (For internal control purpose only)
- ② Year code (The last digit of the year)
- 3 Month code
- 4 Day code
- 5 Version number (Max. 7 characters)
- 6 Country of origin

③ Month code

Month	Jan.	Feb.	Mar.	Apr.	May	Jun.
Code	1	2	3	4	5	6

Month	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Code	7	8	9	X	Y	Z

12. Warranty

12-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

12-2. Production warranty

Kyocera warrants its LCD's for a period of 12 months from the ship date. Kyocera shall, by mutual agreement, replace or re-work defective LCD's that are shown to be Kyocera's responsibility.



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13. Precautions for use

13-1. Installation of the LCD

- 1) The LCD shall be installed so that there is no pressure on the LSI chips.
- 2) The LCD shall be installed flat, without twisting or bending.
- 3) Please design the housing window so that its edges are between the active area and the effective area of the LCD screen.

13-2. Static electricity

- 1) Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required.
- 2) Workers should use body grounding. Operator should wear ground straps.

13-3. LCD operation

1) The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.

13-4. Storage

- The LCD shall be stored within the temperature and humidity limits specified.
 Store in a dark area, and protect the LCD from direct sunlight or fluorescent light.
- 2) Always store the LCD so that it is free from external pressure onto it.

13-5. Usage

- 1) <u>DO NOT</u> store in a high humidity environment for extended periods. Polarizer degradation bubbles, and/or peeling off of the polarizer may result.
- 2) Do not push or rub the touch panel's surface with hard to sharp objects such as knives, or the touch panel may be scratched.
- 3) When the touch panel is dirty, gently wipe the surface with a soft cloth, sometimes moistened by mild detergent or alcohol. If a hazardous chemical is dropped on the touch panel by mistake, wipe it off right away to prevent human contact.
- 4) Touch panel edges are sharp. Handle the touch panel with enough care to prevent cuts.
- 5) Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizer.
- 6) Do not disassemble LCD because it will result in damage.
- 7) This Kyocera LCD has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas. Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.
- 8) Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend using screen saver etc. in cases where a solid-base image pattern must be used.
- 9) Liquid crystal may leak when the LCD is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body; rinse it off right away with water and soap.



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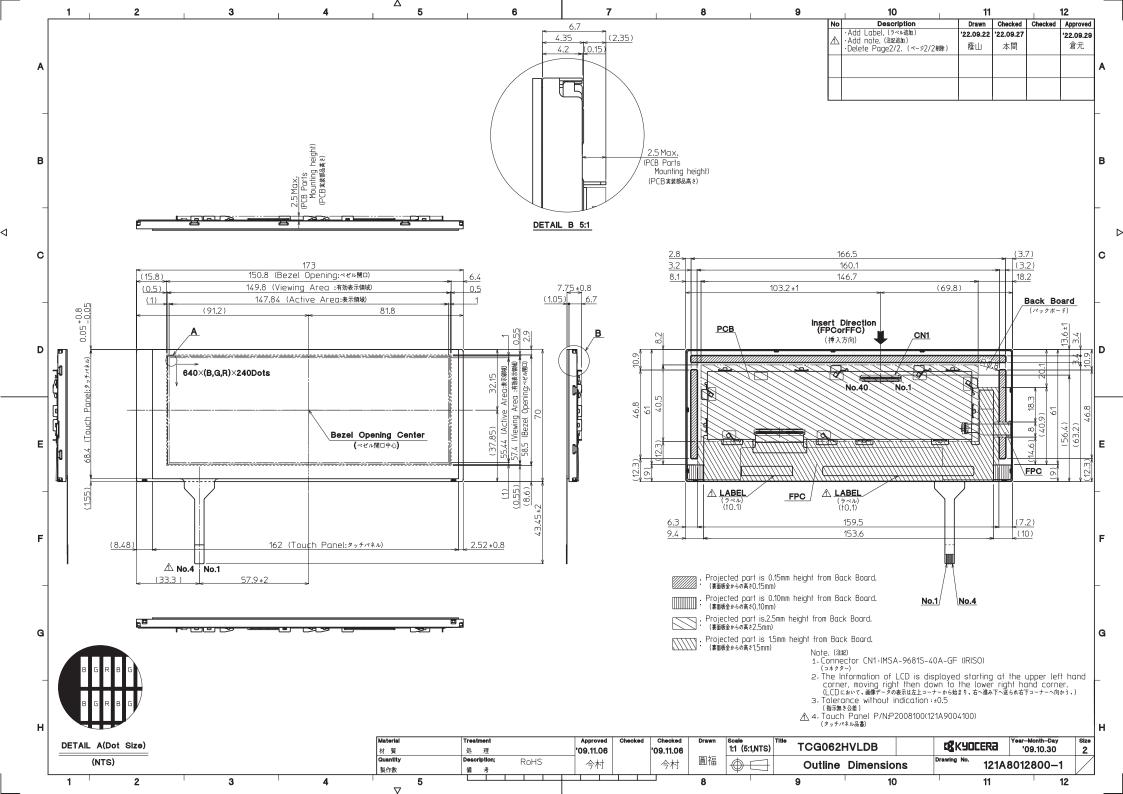
14. Reliability test data

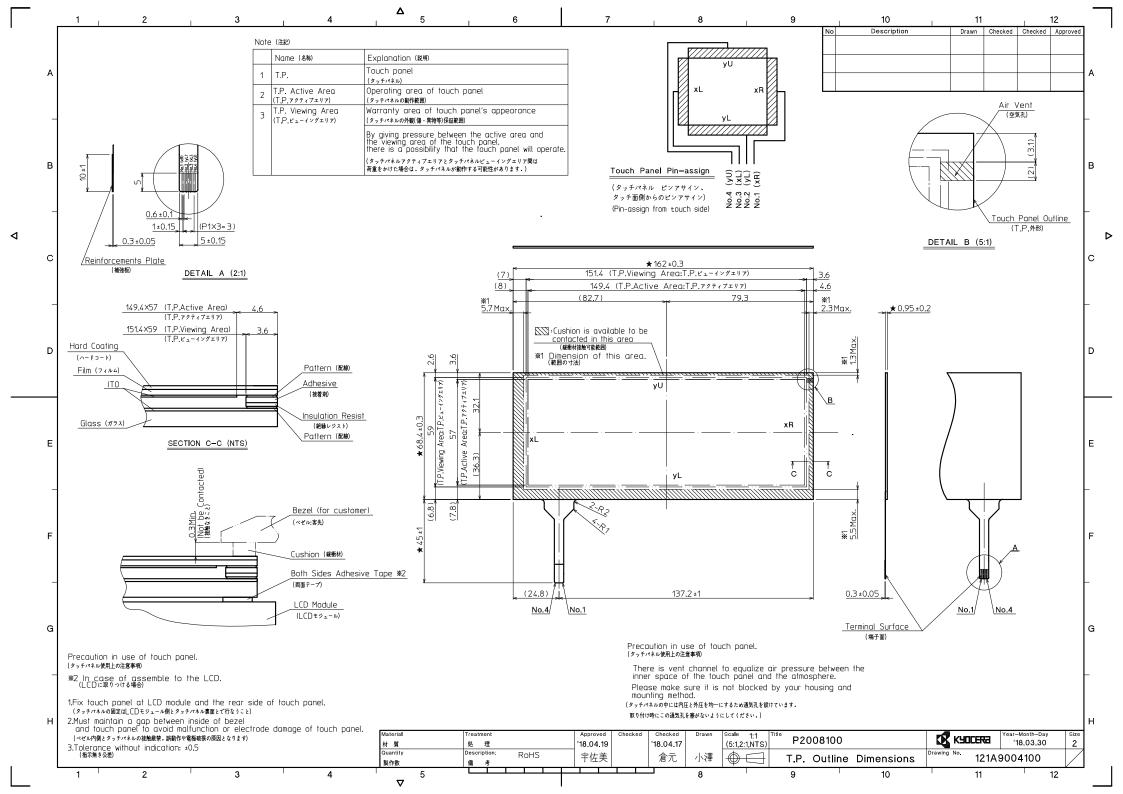
Test item	Test condition	Test time	Jud	gement
High temp. atmosphere	80°C	240h	Display function Display quality Current consumption	No defectNo defectNo defect
Low temp. atmosphere	-30°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. humidity atmosphere	40°C 90% RH	240h	Display function Display quality Current consumption	: No defect: No defect: No defect
Temp. cycle	-30°C 0.5h R.T. 0.5h 80°C 0.5h	10cycles	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. operation	70°C	500h	Display function Display quality Current consumption	: No defect : No defect : No defect
Point Activation life	Silicon rubber, Tip: R = 4.0 Hardness: 60° Hitting force 3N Hitting speed 2 time/s	one million times	Terminal resistance Insulation resistance Linearity Actuation Force	: No defect: No defect: No defect: No defect

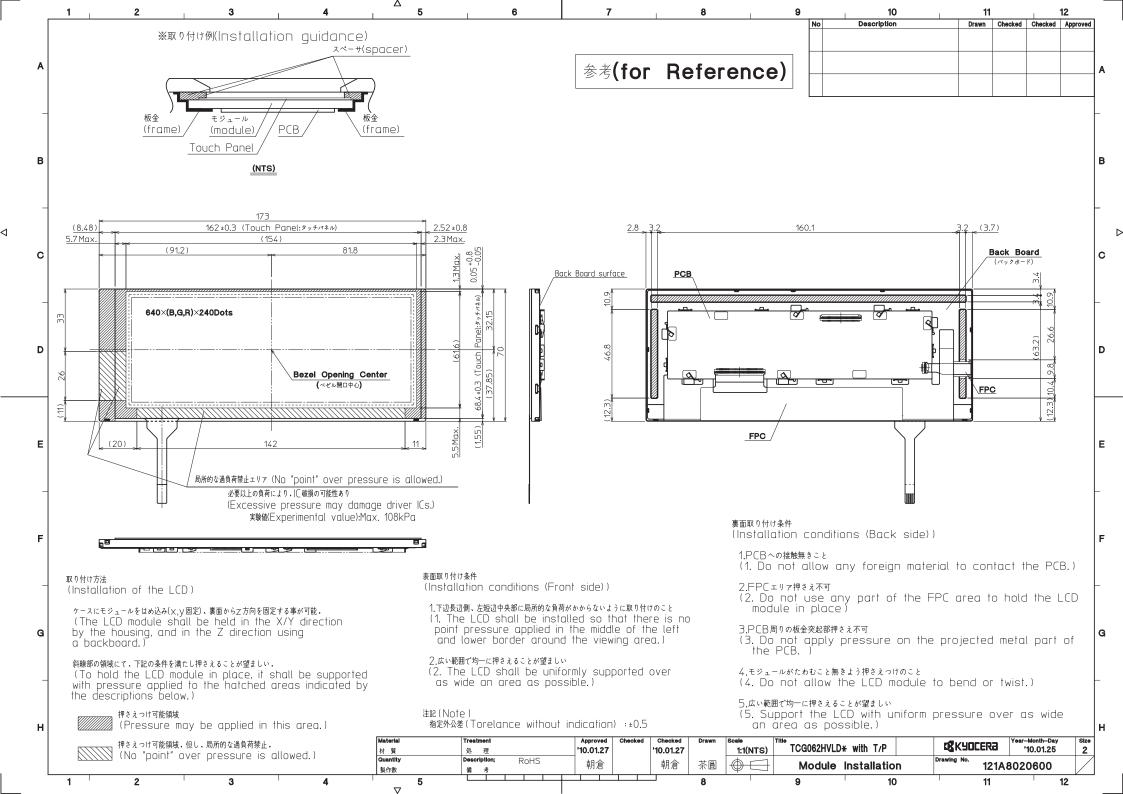
- 1) Each test item uses a test LCD only once. The tested LCD is not used in any other tests.
- 2) The LCD is tested in circumstances in which there is no condensation.
- 3) The reliability test is not an out-going inspection.
- 4) The result of the reliability test is for your reference purpose only.

 The reliability test is conducted only to examine the LCD's capability.









参考(for Reference)

IRISO 製 9681 シリーズコネクタの取り扱い上の注意 Precautions when using IRISO.9681 series connector

操作方法

使用上の注意点

FPC/FFC挿入方法 FPC/FFC insertion

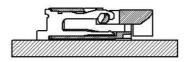
①カバー先端を上方向に上げて開けて下さい。(カバーは回転動作をします)

 \bigcirc pull up the cover tip to open up. (the cover will rotate to operate)

カバーの先端部分を親指や人差し指の爪により、矢印方向に跳ね上げる感じでロック解除を行って下さい。破損の原因となりますので、水平方向には押さないで下さい。

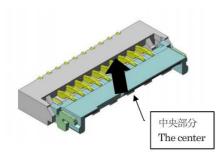
To release the lock, flip the lock to a direction of arrow with the nail of pointer or thumb.

Please Don't push the cover horizontally; it causes damage.



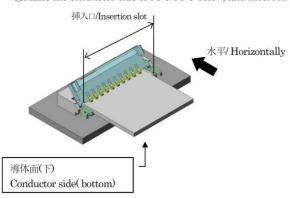
補足 addition

カバー中央部分を上方向へ跳ね上げてロック解除を行って下さい。 Flip the center part of cover to release the lock.



②FPC/FFCの導体面を下にして挿入して下さい。

②Make the conductor side of FPC/FFC below, and insert it.



補足 addition

FPC/FFCの挿入は、カバーを130°開いた状態で、挿入口に対して水平になる様、挿入して下さい。カバーが倒れない様、手で軽く支えますとより挿入し易くなります。

To insert a FPC/FFC, open the cover in 130° , and insert the FPC/FFC horizontally to an insertion slot.

Supporting the cover lightly by hand will be the way to insert easily.

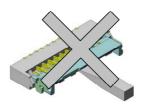
9681 シリーズは、小型・薄型である為、強度は強くありませんので、取り扱いには十分注意して下さい。

Please handle with fragile care.

9681 series are small and thin, so the strength are little short. 作業の際は、手袋及びアースバンドを着用して下さい。

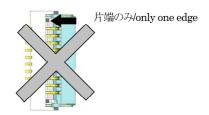
Please wear gloves and a ground belt when the time of the work. ロック解除の際に、ドライバー等先端が細く硬い工具を使用しての操作は行わないで下さい。変形・破損する事があります。

In case of releasing the lock, please don't use hard tools with thin tip, like a driver. It can be deformed and damaged.



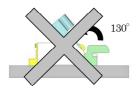
ロック解除時、カバー片端(左 or 右)のみに力を加えてロック解除を 行わないで下さい。変形・破損する事があります。

In case of releasing the lock, please don't make a force on the one edge of cover. It can be deformed and damaged.



カバーは **130°** 以上開かない構造の為、更に後ろへ強い力を加えないで下さい。変形・破損する事があります。

The cover is structured not to open more than 130° , so please don't add a strong force backward. It can be deformed and damaged.



FPC/FFCは、挿入口に正しく挿入して下さい。斜め挿入等、正しく挿入されていない場合は、導通不良の原因となります。

Please insert FPC/FFC in insertion slot properly. If it's not inserted properly, like leaned insertion, it will cause a bad connection.

FPC/FFCは、弊社推奨サイズを使用して下さい。弊社推奨サイズ以外を使用した場合は品質保証出来ません。

Please use our preferred size of FPC/FFC. We can not certify the quality except using our recommended size of FPC/FFC.

操作方法

FPCのロック方法

The method to lock the FPC

①カバーを回転させてロックして下さい。

①Turn down the cover to lock it.



補足/addition

ロック後、カバー両端を軽く押すと、カバーの半ロックを防止できます。

After locking, to push the both edge of cover with light force can prevent a half lock

開閉作業の際は、コンタクトに触れないで下さい。変形による接触 不良の原因となります。

Please don't touch the contact while opening and shutting the cover. It causes bad connection by deformed contact.

使用上の注意点

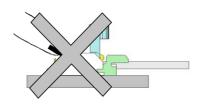
ロック操作の際に下図の矢印方向に強い力を加えてカバーを押さないで下さい。変形・破損の原因となります。

In case of lock operation, please don't push the cover strongly to the direction of arrow. It causes deformation and damage.

水平方向に押す /Pushing in a horizontal direction



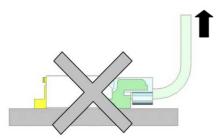
根元を押す /Pushing the base



その他/Others

コネクタの構造上、上方向への引張強度は強くありませんので、上 方向へ強い力を加えないで下さい。使用上、FPC/FFC に引張力が 加わる場合は、上方向の力がコネクタに加わらない様、FPC/FFC をテープ等で固定して下さい。

As a structure of connector, the strength to upper direction is little short. So please don't make a force in above direction. In case of necessary to draw a FPC/FFC out, Please fix the FPC/FFC with a tape to protect the connector from an upper force.



カバーをロックした状態で、FPC/FFC に引張力を加えないで下さい。FPC/FFC 導体面の削れ、及び半挿入状態による導通不良の原因となります。

Please don't draw the FPC/FFC out while the cover is locked. It causes scraping the conductor surface and bad connection by half insertion.

Spec No.	TQ3C-8EAF0-E2DEY16-02
Date	October 3, 2022

KYOCERA INSPECTION STANDARD

TYPE: TCG062HVLDB-G20

KYOCERA CORPORATION

Original	Designed by:	Engineering dep	Confirmed by : QA dept.		
Issue Date	Prepared	Checked	Approved	Checked	Approved
January 18, 2010	T. Onodera	K. Komurasaki	I. Kawajiri	Y. Aritsubo	M. Aoyama



Spec No.	Part No.	Page
TQ3C-8EAF0-E2DEY16-02	TCG062HVLDB-G20	-

Revision record

Revision record								
	Data	Designed	d by:	Engineering de	ept.	Confirmed by : QA dept.		
	Date	Prepai	red	Checked	Approved	Checked	Approved	
Octo	ber 3, 2022	T. Onoo					M. Aoyama	
Rev.No.	Date	Page			Descripti	ons		
01	Nov 26, 2019	_	Changed company name.					
		1	Definition of inspection item Deleted LED wires.					
		2	Add	rnal inspection led Mura stand	ard.			
02	Oct 3, 2022	1	Dot defect Modified Black dot defect. Added White dot (Circular/foreign particle). Definition of size Modified the illustration and added description.					
		2	Defe Cla	ct (in LCD glas rified the class:	s) ification and m	nodified the wo		
		3	Scratch, Foreign particle (Touch screen portion) Modified the words. Glass crack (Touch screen portion) Modified the words.					



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TQ3C-8EAF0-E2DEY	16-02 TCG062HVLDB-G20	1

Visuals specification

1) Note

1) Note	T						
			Note				
General	1. Custom	er identified anomalies no	t defined within this inspection standard shall be				
	reviewed by Kyocera, and an additional standard shall be determined by mutual consent.						
	2. This ins	spection standard about th	ection standard about the image quality shall be applied to any defect within the				
	effective viewing area and shall not be applicable to outside of the area.						
	3. Inspecti	on conditions					
	Lumina	ance	: 500 Lux min.				
	Inspect	ion distance	: 300 mm.				
	Temper		: 25 ± 5℃				
	Direction		: Directly above				
Definition of	Dot defect	Bright dot defect	The dot is constantly "on" when power applied to the				
inspection item	Bot dolest	Bright dot dorest	LCD, even when all "Black" data sent to the screen.				
mopeonon nom			Inspection tool: 5% Transparency neutral density filter.				
			Count dot: If the dot is visible through the filter.				
			Don't count dot: If the dot is not visible through the				
			filter.				
			RGBRGBRGB				
			R G B R G B R G B				
			R G B R G B R G B				
		Black dot defect	The dot is constantly "off" when power applied to the				
			LCD, even when all "White" data sent to the screen.				
			Similar size compared to bright dot.				
		White dot	Pixel works electrically, however, circular/foreign				
		(Circular/foreign	particle makes dot appear to be "on" even when all				
		particle)	"Black" data is sent to the screen.				
		Adjacent dot	Adjacent dot defect is defined as two or more bright dot				
			defects or black dot defects.				
			RIGIBIRIGIBIRIGIB				
			R G B R G B dot defect				
			RGBRGBR B				
	External	Bubble, Scratch,	Visible operating (all pixels "Black" or "White") and non				
	inspection	Foreign particle	operating.				
	1	(Polarizer, Cell,	. L				
		Backlight)					
		Appearance inspection	Does not satisfy the value at the spec.				
	Definition	Definition of cir	·				
	of size	Demintion of cir	the size Deminion of inflear size				
	or size		1				
			<u></u> . <u>.</u>				
		*	VV				
		a' maiori- 1:	ainon avia				
		a: major axis, b: n d = (a + b)					
		$\alpha = (a + b)$	1 4				



Spec No.	Part No.	Page
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2) Standard

2) Standar		1							
Classification		Inspe	ction item	Judgement standard					
Defect	Single	Bright d	ot defect	Acceptable number : 4					
(in LCD	dot			Bright dot spacing : 5 m		: 5 mm	n or more		
glass)		Black do	t defect	Acceptable number : 5					
				Black dot spacing		: 5 mm	n or more		
	Adjacent	2 dots	Bright dot defect	Acceptable number		: 2			
			Black dot defect	Acceptable number		: 3			
		3 or mor	e dots	Acceptable number		: 0			
	Total dot	defects		Acceptable number		: 5 Ma	X		
	Others	White do	ot, Dark dot						
		(Circle)		Size (mn	n)	Ac	ceptable number		
					0.2		(Neglected)		
				0.2 < d ≦	0.4		5		
				0.4 < d ≤	0.5		3		
				0.5 < d			0		
D . 1		D.1 .	(0 . 1)						
	inspection	Polarizei	r (Scratch)			,			
(Defect on				Width (mm)	Length (mm)	Acceptable number		
Polarizer o				$W \leq 0.1$		< - 0	(Neglected)		
between P				$1.0.1 \le W \le 0.3$		≦ 5.0	(Neglected)		
and LCD g	glass)			0.0 < W	5.0 < L		0		
				0.3 < W	_		0		
		Polarize	r (Bubble)						
				Size (mn	n)	Ac	ceptable number		
				d ≦	0.2		(Neglected)		
				0.2 < d ≦	0.3		5		
				0.3 < d ≦	0.5		3		
				0.5 < d			0		
		Foreign	particle						
		(Circular	shape)	Size (mn	1)	Ac	ceptable number		
	(323 33333 3333)				•		0.2		(Neglected)
				0.2 < d ≦			5		
				0.4 < d ≦			3		
				0.5 < d			0		
		Formi	nantials						
		Foreign		XX7: 1:1 /	7 .1	()	A . 11 1		
		(Linear s	snape)	Width (mm)	Length	(mm)	Acceptable number		
		Scratch		$W \leq 0.03$		< 0.0	(Neglected)		
				$0.03 < W \le 0.1$	$\begin{array}{c c} L \leq 2.0 \\ \hline 2.0 < L \leq 4.0 \end{array}$		(Neglected)		
				$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$			3		
				0.1 < W	4.0 \ L	4.0 < L			
				U.1 \ VV					
							circular shape)		
		Color va	riation	Not to be significant					
		(Mura)		Consultation shall be	onsultation shall be held as necessary.				
<u> </u>									



Spec No.	Part No.	Page
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Inspection item		Judgement standard					
Scratch,							
Foreign particle	Item Width(mm) Ler		Length(mm) Ac		ceptable number		
(Touch screen		$d \le 0.03$	$L \le 20$		Neglected		
portion)	Constal	$0.03 < d \le 0.05$ $L \le 10$		2pc	$2pcs$ within $\phi 20mm$		
	Scratch	$0.05 < d \le 0.08$	$L \le 6$	2pc	es within φ20mm		
		$0.08 < d \le 0.1$	$L \le 4$	1pc	c within φ30mm		
	Foreign	$W \le 0.05$	Neglected		Neglected		
	particle	$0.05 < W \le 0.1$	T < 5	0	i+l-i / 20		
	(line like)	$0.05 \lor W \triangleq 0.1$	$L \le 5$	2pc	s within φ 30mm		
	Foreign	d ≦	0.2		Neglected		
	particle	0.2 < d ≦ 0	0.2	Oma	s within φ30mm		
	(circle like)	0.2 \ d \geq 0	0.5	2pc	s within φ 50mm		
	Above are applie	d to the visible area.					
	Unless there a	re foreign particle and d	damage affected	serious	sly to the electrical		
	performance out	of the active area, we appro	ove of this produc	t.			
Glass crack					Acceptable		
(Touch screen	Item	Size (m	ım)		number		
portion)							
		/ 1	z X	≦3			
	Corner crack	~ × × × v / v /	Y	≦3	2 pcs		
	Corner crack			=0	/panel		
			Z	<t< td=""><td></td></t<>			
	Crack in	x>***	X	≦5			
	other area		7	/1 F	2 pcs		
	than in		Y	≤ 1.5	/side		
	corner	2		<t< td=""><td></td></t<>			
		•		-			
			/	1 - -			
	Dograma	_		-			
	Progressive crack		\times /		0 pcs		
	crack	$\overline{}$					
	Above are applie	ed to the visible area.					
		re foreign particle and d	lamage affected	serious	sly to the electrical		
		of the active area, we appro			ory to the electrical		
	_	or the active area, we appre	or or one produc				
Newton's ring	Neglected.						
) N	ewton's	ring		



Document No.	TQ3C-8EAF0-E3DEY16-01
Date	November 29, 2023

KYOCERA PACKAGING STANDARD

TYPE: TCG062HVLDB-G20

KYOCERA CORPORATION

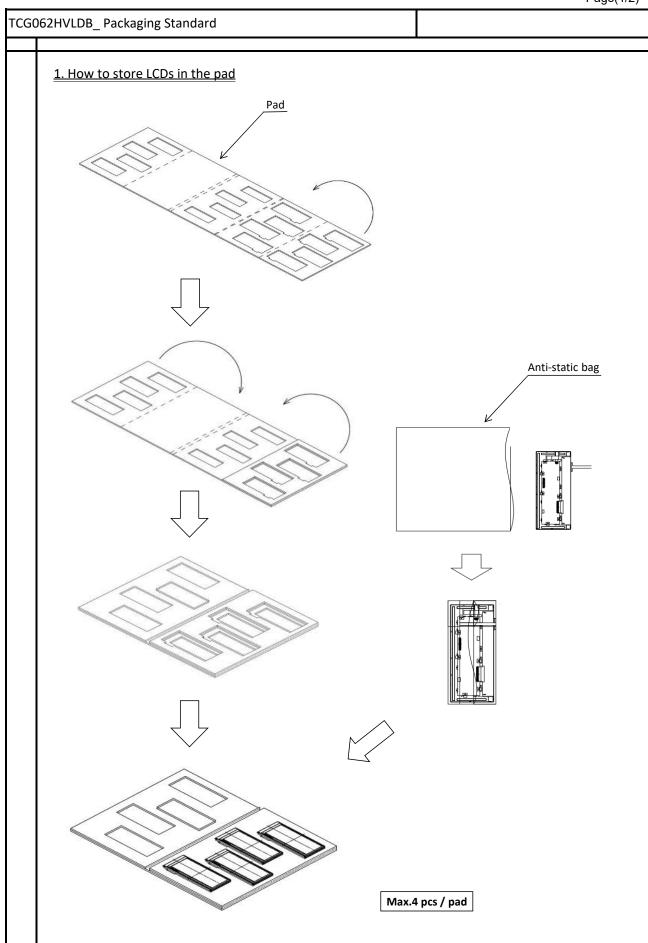
Original	Designed by: Engi	Confirmed by: QA dept.		
Issue Date	Issue Date Prepared Checked Approved			
October 3, 2022	Y. Yamazaki	T. Fukui	A. Iwasaki	T. Sawada



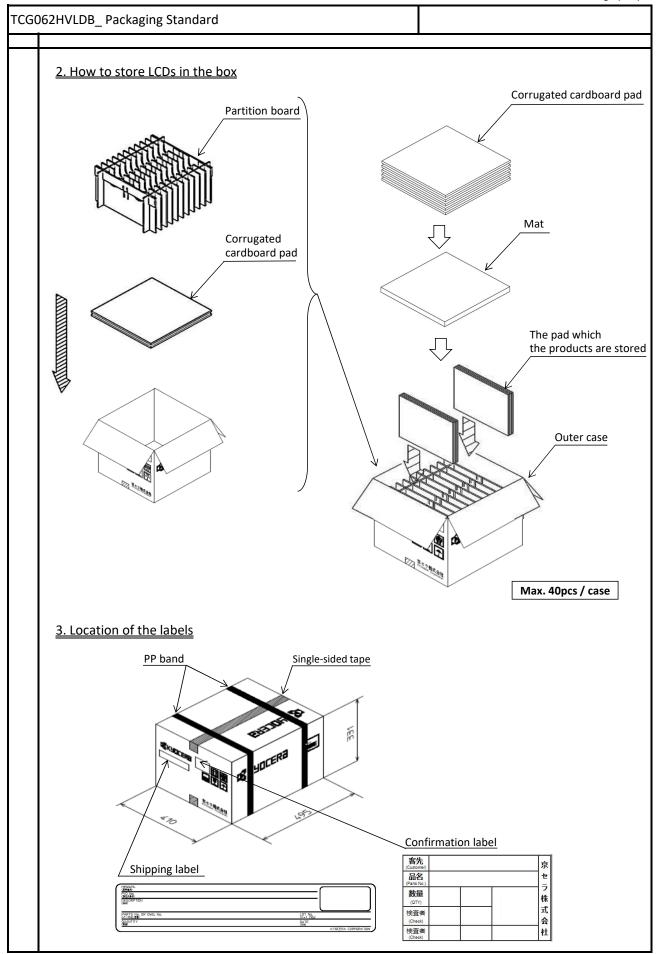
Document No.	Part No.	Page
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Revision record

	Revision record Designed by: Engineering dent Confirmed by:							
Date		Designed by: Engineering dept.			QA dept.			
		Prepared		Checked	Approved	Approved		
November 29, 2023		Y. Yamazaki		T. Fukui	A. Iwasaki	T. Sawada		
RevNo.	Date	Page		De	escriptions			
01	Nov. 29, 2023	2		store LCDs in the				
			Correct t	the maximum qua	ntity per case.			
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