



# SPECIFICATION



TCG084SVLPAANN-AN30 8.4" - SVGA - LVDS

Version: 0.0 Date: 31.01.2014

Note: This specification is subject to change without prior notice

www.data-modul.com

Spec No.	TQ3C-8EAF0-E1YAF46-00
Date	January 31, 2014

#### **TYPE : TCG084SVLPAANN-AN30**

< 8.4 inch SVGA transmissive color TFT with LED backlight and constant current circuit for LED backlight>

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#### KYOCERA DISPLAY CORPORATION

This specification is subject to change without notice. Consult Kyocera before ordering.

Original	Designed by:	Engineering de	Confirmed by: QA dept.						
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## Warning

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control, home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.
- 2. Customer agrees to indemnify, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs, and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

# Caution

1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.

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Rev.No.	Date	Page		Des	cript	ions		

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#### 1. Application

This document defines the specification of TCG084SVLPAANN-AN30. (RoHS Compliant)

#### 2. Construction and outline

LCD	: Transmissive color dot matrix type TFT
Backlight system	: LED
Polarizer	: Anti-Glare treatment
Interface	: LVDS
Additional circuit	: Timing controller, Power supply (3.3V input)
	Constant current circuit for LED Backlight(12V input)

#### 3. Mechanical specifications

Item	Specification	Unit
Outline dimensions 1)	199.5(W)×(147.4)(H)×9(D)	mm
Active area	170.4(W)×127.8(H) (21.3cm/8.4 inch(Diagonal))	mm
Dot format	800×(R,G,B)(W)×600(H)	dot
Dot pitch	0.071(W)×0.213(H)	mm
Base color 2)	Normally White	-
Mass	330	g

1) Projection not included. Please refer to outline for details.

2) Due to the characteristics of the LCD material, the color varies with environmental temperature.



#### 4. Absolute maximum ratings

Item		Symbol	Min.	Max.	Unit
Supply voltag	Supply voltage(+3.3V)		-0.3	4.0	V
Supply voltage(+12V)		VIN	-0.3	14.0	V
	RxINi+, RxINi- 1)	$V_{I1}$	-0.3	2.8	V
Input signal	CK IN+, CK IN-	$V_{I2}$	-0.3	2.8	V
voltage	SELLVDS	$V_{I3}$	-0.3	$V_{DD}$ +0.5	V
	BLBRT, BLEN	$V_{I4}$	-0.3	V <sub>IN</sub>	V

#### 4-1. Electrical absolute maximum ratings

1) i=0,1,2,3

2) V<sub>DD</sub> must be supplied correctly within the range described in 5-1.

4-2. Environmental absolute maximum ratings

Item		Symbol	Min.	Max.	Unit
Operating temperature(Ambient)	1)	Top(Ambient)	-20	70	°C
Operating temperature(Panel)	2)	T <sub>OP</sub> (Panel)	-20	70	°C
Storage temperature	3)	$T_{STO}$	-30	80	°C
Operating humidity	4)	Нор	10	5)	%RH
Storage humidity	4)	Hsto	10	5)	%RH
Vibration		-	6)	6)	-
Shock		-	7)	7)	-

- 1) Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25°C, another temperature range should be confirmed.
- 2) Panel surface temperature (all the surface)
- 3) Temp. = -30°C < 48h , Temp. = 80°C < 168h</li>
  Store LCD at normal temperature/humidity. Keep them free from vibration and shock. An LCD that is kept at a low or a high temperature for a long time can be defective due to other conditions, even if the low or high temperature satisfies the standard. (Please refer to "Precautions for Use" for details.)
- 4) Non-condensing
- 5) Temp.≦40°C, 85%RH Max.
  - Temp.>40°C, Absolute humidity shall be less than 85%RH at 40°C.

6)

Frequency	$10{\sim}55~{ m Hz}$	Acceleration value
Vibration width	0.15mm	$(0.3 \sim 9 \text{ m/s}^2)$
Interval	10-55-10	Hz 1 minutes

2 hours in each direction X, Y, Z (6 hours total) EIAJ ED-2531

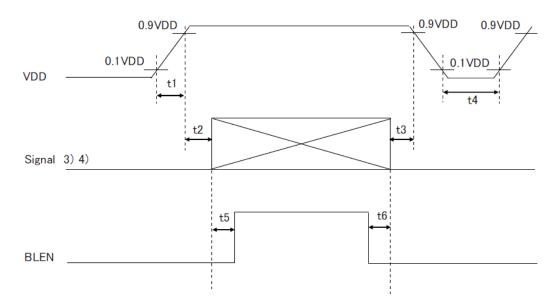
 7) Acceleration: 490 m/s<sup>2</sup>, Pulse width: 11 ms 3 times in each direction: ±X, ±Y, ±Z EIAJ ED-2531

#### 5. Electrical characteristics

#### 5-1. LCD

					Temp. =	-20~70°C
Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Supply voltage 1	V <sub>DD</sub>	-	3.0	3.3	3.6	V
Current consumption	IDD	2)	-	250	310	mA
Permissive input ripple voltage	$V_{\mathrm{RP}}$	V <sub>DD</sub> =3.3V	-	-	100	mVp-p
	VIL	"Low" level	0	-	0.8	V
Input signal voltage 3	VIH	"High" level	2.0	-	V <sub>DD</sub>	V
T , 1 1 ,	Iol	V <sub>I3</sub> =0V	-10	-	10	$\mu  \mathrm{A}$
Input leak current	Іон	V <sub>I3</sub> =3.3V	-	-	400	$\mu A$
LVDS Input voltage 4	VL	-	0	-	1.9	V
Differential input voltage 4	VID	-	250	350	450	mV
Differential input	5) V <sub>TL</sub>	"Low" level	V <sub>CM</sub> -100	-	-	mV
threshold voltage 4	VTH VTH	"High" level	-	-	V <sub>CM</sub> +100	mV
Terminator	$R_1$	-	-	100	-	Ω
	t1	-	0.1	-	10	ms
	t2	-	0	-	-	ms
<b>X</b> 7 to a second it is a second secon	t3	-	0	-	-	ms
V <sub>DD</sub> -turn-on conditions 1	t4	-	1.0	-	-	s
	t5	-	200	-	-	ms
	t6	-	200	-	-	ms

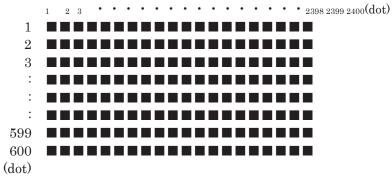
#### 1) $V_{DD}$ -turn-on conditions



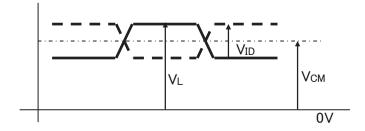
\* If the condition of t5, t6 doesn't fill it, the display noise might be seen.



- 2) Display pattern:
  - $V_{DD} = 3.3V$ , Temp. = 25°C



- 3) Input signal : SELLVDS
- 4) Input signal : RxIN3+, RxIN3-, RxIN2+, RxIN2-, RxIN1+, RxIN1-, RxIN0+, RxIN0-CK IN+, CK IN-

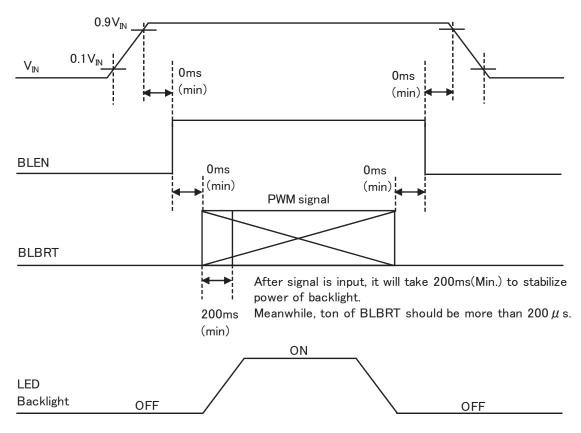


5)  $V_{CM}$ : LVDS Common mode voltage ( $V_{CM}$ =1.25V)

#### 5-2. Constant current circuit for LED Backlight

	-			Ter	mp. = -20~	-70°C
Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Supply voltage 1)	V <sub>IN</sub>	-	10.8	12.0	13.2	V
Current consumption	I <sub>IN</sub>	2)	-	330	500	mA
Permissive input ripple voltage	$V_{\mathrm{RP}\_\mathrm{BL}}$	$V_{IN}=12.0V$	-	-	100	mVp-p
DI DDT Inspect size al scalto re	VIL_BLBRT	"Low" level	0	-	0.8	V
BLBRT Input signal voltage	VIH_BLBRT	"High" level	2.3	-	$V_{\rm IN}$	V
BLBRT Input pull-down resistance	$R_{\rm IN\_BLBRT}$	-	100	300	500	kΩ
DI EN Lucret cigned solte as	VIL_BLEN	"Low" level	0	-	0.8	V
BLEN Input signal voltage	VIH_BLEN	"High" level	2.3	-	$V_{\rm IN}$	V
BLEN Input pull-down resistance	$R_{\rm IN\_BLEN}$	-	100	300	500	kΩ
PWM Frequency 3)	fpwm	-	200	-	10k	Hz
		$f_{PWM}$ =200Hz	1	-	100	%
P WM Duty ratio 3)	Dpwm	f <sub>PWM</sub> =2kHz	10	-	100	%
		f <sub>PWM</sub> =10kHz	50	-	100	%
Operating life time 4), 5)	Т	Temp.=25°C	-	70,000	-	h

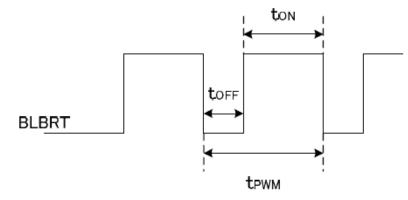
1)  $V_{IN}$ -turn-on conditions



2)  $V_{IN} = 12V$ , Temp. = 25°C,  $D_{PWM} = 100\%$ 



3) PWM Timing Diagram



ton, toff  $\geq 50 \,\mu$  s.

In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.

- 4) When brightness decrease 50% of minimum brightness.The average life of a LED will decrease when the LCD is operating at higher temperatures.
- 5) Life time is estimated data.(Condition : IF=60mA, Ta= $25^{\circ}$ C in chamber).



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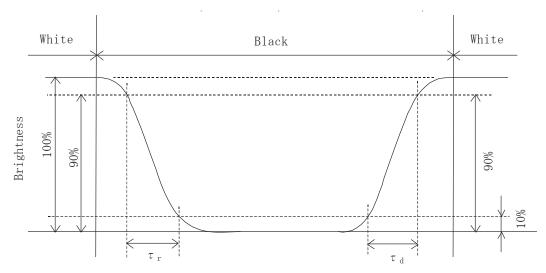
## 6. Optical characteristics

Measuring spot =  $\phi$  6.0mm, Temp. = 25°C

Item		Symbol	Condition	Min.	Тур.	Max.	Unit
	Rise	τr	$\theta = \phi = 0^{\circ}$	-	8	-	ms
Response time	Down	τd	$\theta = \phi = 0^{\circ}$	-	22	-	ms
	•	heta upper		-	80	-	1
Viewing angle View direction	range	$\theta$ lower	CD > 10	-	60	-	deg.
÷ 6 o'cloc		$\phi$ left	$CR \ge 10$	-	80	-	1
(Gray in	version)	$\phi$ right		-	80	-	deg.
Contrast ratio		CR	$\theta = \phi = 0^{\circ}$	560	800	-	-
Brightness		L	IF=60mA/Line	525	750	-	cd/m <sup>2</sup>
	D . 1	х	$\theta = \phi = 0^{\circ}$	(0.550)	(0.600)	(0.650)	
	Red	У	$\theta = \phi = 0^{\circ}$	(0.300)	(0.350)	(0.400)	
	0	х	$\theta = \phi = 0^{\circ}$	(0.280)	(0.330)	(0.380)	
Chromaticity	Green	У		(0.520)	(0.570)	(0.620)	
coordinates	ות	x	$0 - 1 - 0^{\circ}$	(0.105)	(0.155)	(0.205)	-
	Blue	У	$\theta = \phi = 0^{\circ}$	(0.065)	(0.115)	(0.165)	
	<b>XX71</b> · /	x	$\theta = \phi = 0^{\circ}$	(0.250)	(0.300)	(0.350)	
	White	У		(0.265)	(0.315)	(0.365)	

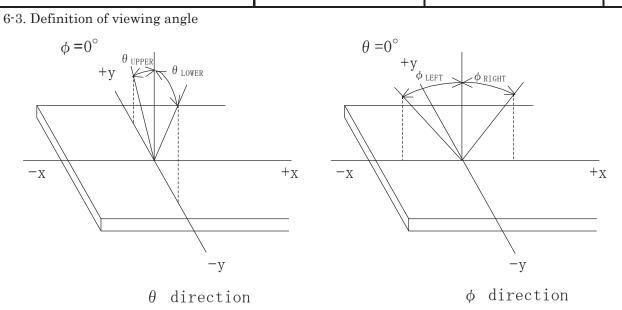
6-1. Definition of contrast ratio

#### 6-2. Definition of response time

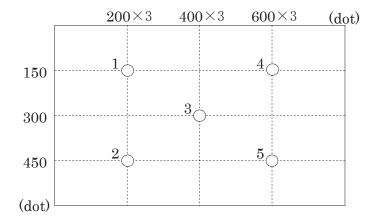




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#### 6-4. Brightness measuring points



- 1) Rating is defined as the white brightness at center of display screen(3).
- 2) 5 minutes after LED is turned on. (Ambient Temp.= $25^{\circ}$ C)



#### 7. Interface signals

#### 7-1. Interface signals

No.	Symbol	Description	Note
1	GND	GND	
2	SELLVDS	Mode select signal(LVDS Data mapping)	
3	GND	GND	
4	GND	GND	
5	RxIN3+	LVDS receiver signal CH3(+)	LVDS
6	RxIN3-	LVDS receiver signal CH3(·)	LVDS
7	GND	GND	
8	CK IN+	LVDS receiver signal CK(+)	LVDS
9	CK IN-	LVDS receiver signal CK(-)	LVDS
10	GND	GND	
11	RxIN2+	LVDS receiver signal CH2(+)	LVDS
12	RxIN2-	LVDS receiver signal CH2(·)	LVDS
13	GND	GND	
14	RxIN1+	LVDS receiver signal CH1(+)	LVDS
15	RxIN1-	LVDS receiver signal CH1(·)	LVDS
16	GND	GND	
17	RxIN0+	LVDS receiver signal CH0(+)	LVDS
18	RxIN0-	LVDS receiver signal CH0(-)	LVDS
19	GND	GND	
20	GND	GND	
21	VDD	+3.3V power supply	
22	Vdd	+3.3V power supply	
23	GND	GND	
24	BLBRT	PWM signal(Brightness adjustment)	
25	BLEN	ON/OFF terminal voltage	
26	GND	GND	
27	VIN	+12V power supply	
28	VIN	+12V power supply	
29	GND	GND	
30	GND	GND	

LCD connector	:	FI-X30SSLA-HF	(JAE)
Matching connector	:	FI-X30HL	(JAE)
	:	FI-X30HL-T	(JAE)
	:	FI-X30C2L-NPB	(JAE)
	:	FI-X30C2L-T-NPB	(JAE)

LVDS receiver	:	Embedded in ASIC
Matching LVDS transmitter	:	THC63LVDM83R(THine Electronics) or compatible



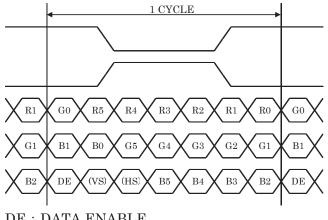
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7-2. Data mapping(6bit RGB input)

1	) Location of SELLVDS	(THC63LVDM83R(THine Electronics) or c	ompatible)
	Transmitten	OD: CELLVDC	

Transmitter		2Pin SELLVDS		
Pin No.	Data	= L(GND) or OPEN	= H(3.3V)	
51	TA0	—	R0(LSB)	
52	TA1	—	R1	
54	TA2	—	R2	
55	TA3	—	R3	
56	TA4	—	R4	
3	TA5	_	R5(MSB)	
4	TA6	—	G0(LSB)	
6	TB0	—	G1	
7	TB1	—	G2	
11	TB2	—	G3	
12	TB3	—	G4	
14	TB4	—	G5(MSB)	
15	TB5	—	B0(LSB)	
19	TB6	—	B1	
20	TC0	—	B2	
22	TC1	_	B3	
23	TC2	—	B4	
24	TC3	—	B5(MSB)	
27	TC4	—	(HS)	
28	TC5	—	(VS)	
30	TC6	—	DE	
50	TD0	—	GND	
2	TD1	_	GND	
8	TD2	—	GND	
10	TD3	_	GND	
16	TD4	—	GND	
18	TD5	—	GND	
25	TD6	_	GND	

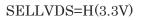
SELLVDS=H(3.3V)

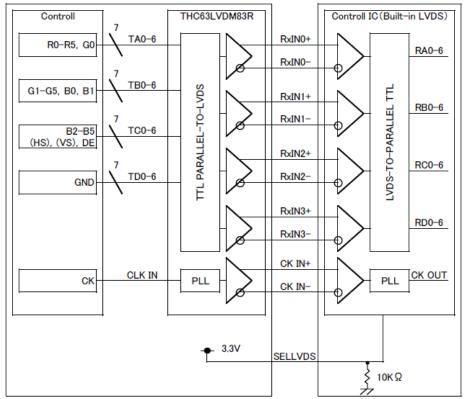


DE : DATA ENABLE HS : H<sub>SYNC</sub>

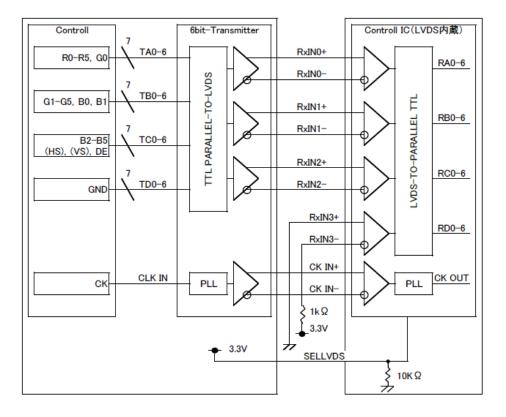
 $VS \, : \, V_{\rm SYNC}$ 

#### 2) Block Diagram





When using "6-bit Transmitter", please connect the unused channel of the control IC receiver as described in the diagram below.





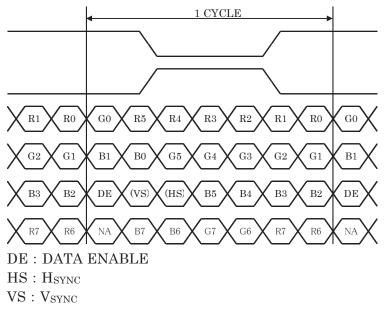
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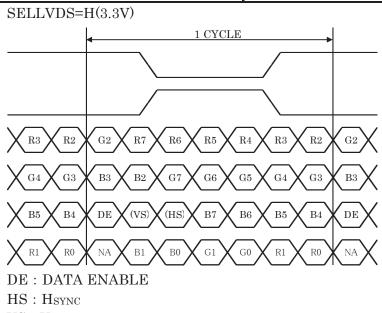
7-3. Data mapping(8bit RGB input)

1	1) Location of SELLVDS (THC63LVDM83R(THine Electronics) or compatib				
Transmitter 2Pin SELLVDS					

Transmitter		2Pin SELLVDS		
Pin No.	Data	= L(GND) or OPEN	= H(3.3V)	
51	TA0	R0(LSB)	R2	
52	TA1	R1	R3	
54	TA2	R2	R4	
55	TA3	R3	R5	
56	TA4	R4	R6	
3	TA5	R5	R7(MSB)	
4	TA6	G0(LSB)	G2	
6	TB0	G1	G3	
7	TB1	G2	G4	
11	TB2	G3	G5	
12	TB3	G4	G6	
14	TB4	G5	G7(MSB)	
15	TB5	B0(LSB)	B2	
19	TB6	B1	B3	
20	TC0	B2	B4	
22	TC1	B3	B5	
23	TC2	B4	B6	
24	TC3	B5	B7(MSB)	
27	TC4	(HS)	(HS)	
28	TC5	(VS)	(VS)	
30	TC6	DE	DE	
50	TD0	R6	R0(LSB)	
2	TD1	R7(MSB)	R1	
8	TD2	G6	G0(LSB)	
10	TD3	G7(MSB)	G1	
16	TD4	B6	B0(LSB)	
18	TD5	B7(MSB)	B1	
25	TD6	(NA)	(NA)	

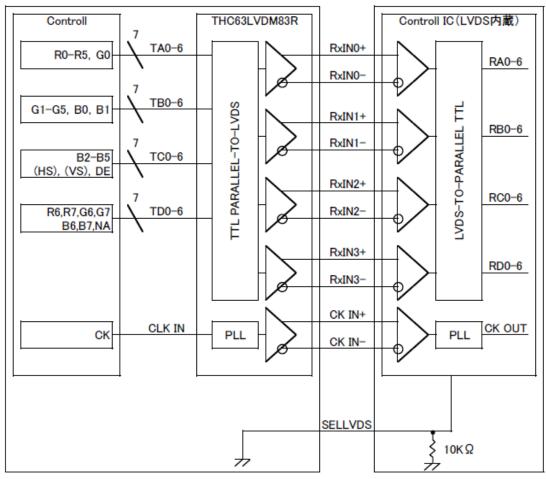
#### SELLVDS=L(GND) or OPEN





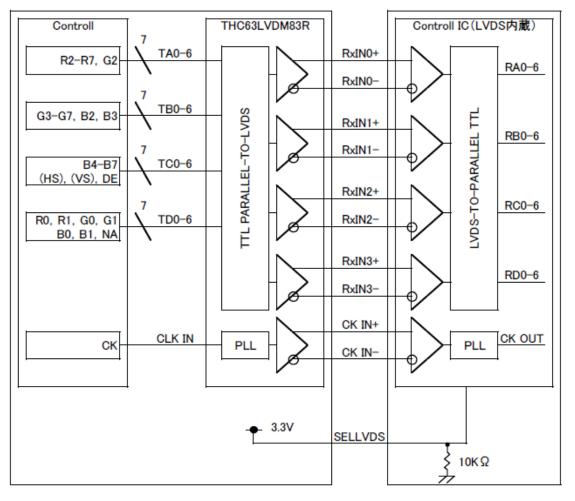
- $VS : V_{SYNC}$
- 2) Block Diagram

#### SELLVDS=L(GND) or OPEN





#### SELLVDS=H(3.3V)





#### 8. Input timing characteristics

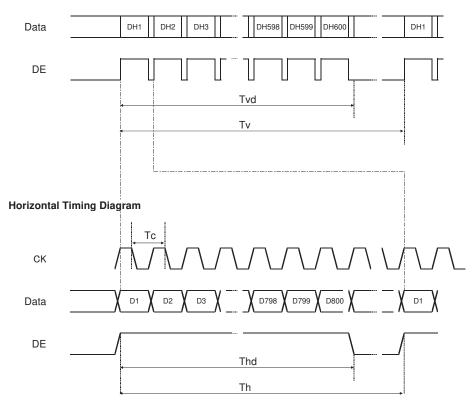
8-1. Timing characteristics

	Item	Symbol	Min.	Тур.	Max.	Unit	Note
Clock (CK)	Frequency	1/Tc	30	40	48	MHz	
	Horizontal Dariad	Th	860	1056	1395	Тс	
	Horizontal Period	Th	24.0	26.4	-	$\mu \ s$	1)
Enable signal (DE)	Horizontal display period	Thd		800		Тс	
	Vertical Period	Tv	610	628	1024	Th	
	Vertical display period	Tvd		600		Th	
Refresh rate		fv	50	60	70	Hz	2)

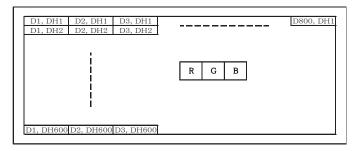
1) Please set a clock frequency, a vertical dormant period, and the horizontal dormant period so that the Horizontal Period should not reach less than Min. value.

2) If the refresh rate reach less than Min. value, the deterioration of the display quality, flicker etc., may occur.(fv=1/Tv)

Vertical Timing Diagram



8-2. Input Data Signals and Display position on the screen





#### 9. Lot number identification

The lot number shall be indicated on the back of the backlight case of each LCD.

 $\begin{array}{c|ccccc} TCG084SVLPAANN-AN30 & - \square & - \square & - \square & MADE IN & \square \square \square \square \\ & \downarrow \downarrow & \downarrow & \downarrow & & \downarrow \\ & 1 & 2 & 3 & 4 & 5 \end{array}$ 

- No1. No5. above indicate
  - 1. Year code
    - 2. Month code
    - 3. Date
    - 4. Version Number
    - 5. Country of origin (Japan or China)

Year	2014	2015	2016	2017	2018	2019
Code	4	5	6	7	8	9

Month	Jan.	Feb.	Mar.	Apr.	May	Jun.
Code	1	2	3	4	5	6

Month	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Code	7	8	9	Х	Y	Z

#### 10. Warranty

10-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

#### 10-2. Production warranty

Kyocera warrants its LCD's for a period of 12 months from the ship date. Kyocera shall, by mutual agreement, replace or re-work defective LCD's that are shown to be Kyocera's responsibility.



#### 11. Precautions for use

- 11-1. Installation of the LCD
- 1) Please ground either of the mounting (screw) holes located at each corner of an LCD, in order to stabilize brightness and display quality.
- 2) A transparent protection plate shall be added to protect the LCD and its polarizer.
- 3) The LCD shall be installed so that there is no pressure on the LSI chips.
- 4) The LCD shall be installed flat, without twisting or bending.

11-2. Static electricity

- 1) Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required.
- 2) Workers should use body grounding. Operator should wear ground straps.

#### 11-3. LCD operation

- 1) The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.
- 2) Please select the best display pattern based on your evaluation because flicker, lines or nonuniformity or unevenness can be visible depending on display patterns.

11-4. Storage

- 1) The LCD shall be stored within the temperature and humidity limits specified. Store in a dark area, and protect the LCD from direct sunlight or fluorescent light.
- 2) Always store the LCD so that it is free from external pressure onto it.

11-5. Usage

- 1) <u>DO NOT</u> store in a high humidity environment for extended periods. Polarizer degradation bubbles, and/or peeling off of the polarizer may result.
- 2) The front polarizer is easily scratched or damaged. Prevent touching it with any hard material, and from being pushed or rubbed.
- 3) The LCD screen may be cleaned by wiping the screen surface with a soft cloth or cotton pad using a little Ethanol.
- 4) Water may cause damage or discoloration of the polarizer. Clean condensation or moisture from any source immediately.
- 5) Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizer.
- 6) Do not disassemble LCD because it will result in damage.
- 7) This Kyocera LCD has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas. Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.
- 8) Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend using screen saver etc. in cases where a solid-base image pattern must be used.
- 9) Liquid crystal may leak when the LCD is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body; rinse it off right away with water and soap.



#### 12. Reliability test data

Test item	Test condition	Test time	Jud	gement
High temp. atmosphere	80°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Low temp. atmosphere	-30°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. humidity atmosphere	40°C 90% RH	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Temp. cycle	-30°C 0.5h R.T. 0.5h 80°C 0.5h	10cycles	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. operation	70°C	500h	Display function Display quality Current consumption	: No defect : No defect : No defect

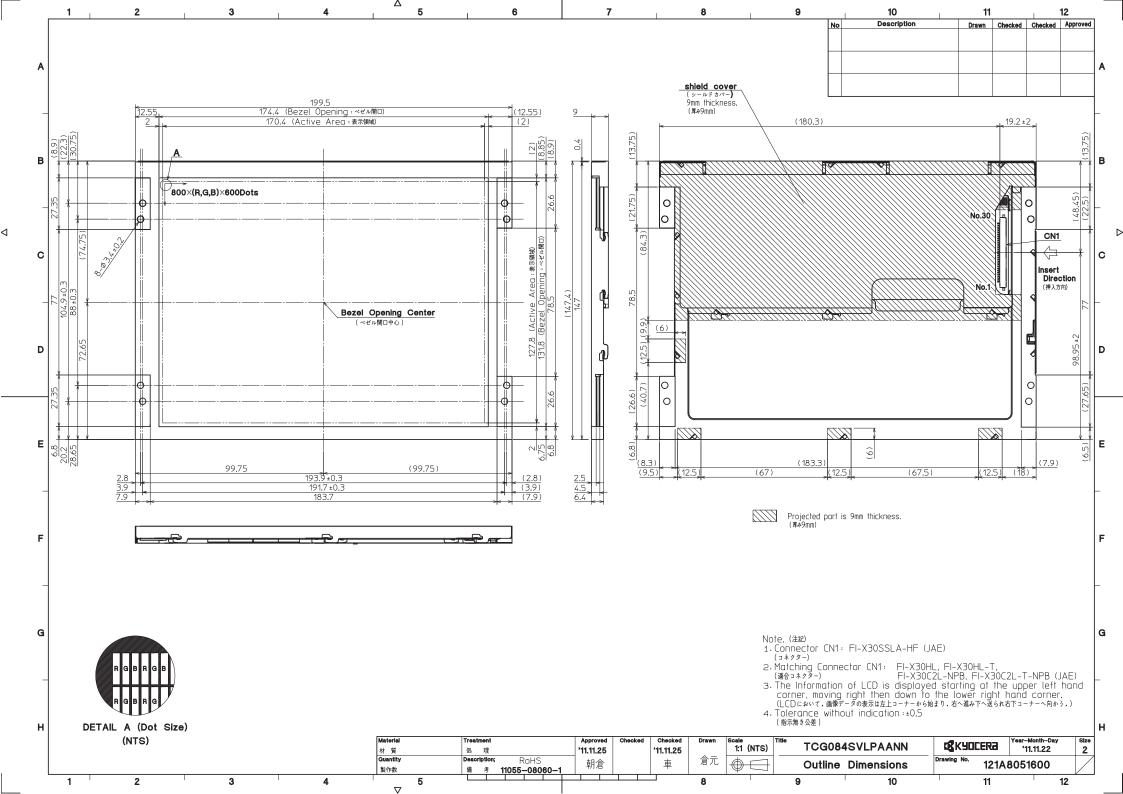
1) Each test item uses a test LCD only once. The tested LCD is not used in any other tests.

2) The LCD is tested in circumstances in which there is no condensation.

3) The reliability test is not an out-going inspection.

 The result of the reliability test is for your reference purpose only. The reliability test is conducted only to examine the LCD's capability.





Spec No.	TQ3C-8EAF0-E2YAF46-00
Date	January 31, 2014

## **KYOCERA INSPECTION STANDARD**

### **TYPE : TCG084SVLPAANN-AN30**

KYOCERA DISPLAY CORPORATION

Original	Designed by :	Engineering de	Confirmed by : QA dept.		
Issue Date	Prepared	Checked	Approved	Checked	Approved
January 31, 2014	X. Mori	Y. Yomazaki	M.F.jiTanj	0. Soto	I-Hamar S



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# Visuals specification

1) Note	
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1) Note			Note			
General	reviewe consent 2. This ins	reviewed by Kyocera, and an additional standard shall be determined by mutual consent.				
	Lumina	ion distance rature	: 500 Lux min. : 300 mm. : 25 ± 5°C : Directly above			
Definition of inspection item	Dot defect	Bright dot defect Black dot defect Adjacent dot	<ul> <li>Directly above</li> <li>The dot is constantly "on" when power applied to the LCD, even when all "Black" data sent to the screen. Inspection tool: 5% Transparency neutral density filter. Count dot: If the dot is visible through the filter.</li> <li>Don't count dot: If the dot is not visible through the filter.</li> <li>R G B R G B R G B</li> <li>R G B R G B R G B</li> <li>R G B R G B R G B</li> <li>G G B R G B R G B</li> <li>G G B R G B R G B</li> <li>G G B R G B R G B</li> <li>G G B R G B R G B</li> <li>G G B R G B R G B</li> <li>G G B R G B R G B</li> <li>G G B R G B R G B</li> <li>G G G G G G G G</li> <li>G G G G G G</li> <li>G G G G G G</li> <li>G G G G</li></ul>			
	External inspection	Bubble, Scratch, Foreign particle (Polarizer, Cell, Backlight) Appearance inspection	Visible operating (all pixels "Black" or "White") and non operating.         Does not satisfy the value at the spec.			
	Definition of size					



Spec No.
TQ3C-8EAF0-E2YAF46-00

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#### 2) Standard

2) Standa:		Inspect	ion itom		Independent	atondor	d	
ClassificationInspection itemDefectDotBright dot defect		Judgement standard			a			
		-				-	-	
(in LCD	defect					5 mm or more		
glass)		Black dot o	defect	Acceptable number		:5		
				Black dot spacing		:5 mm	or more	
		2 dot join	Bright dot defect	Acceptable number		:2		
			Black dot defect	Acceptable number		: 3		
		3 or more of	dots join	Acceptable number		:0		
		Total dot d	efects	Acceptable number		: 5 Max	X	
	Others	White dot,		*				
		(Circle)		Size (mm	)	Ac	ceptable number	
		(011010)		$d \leq d$		110	(Neglected)	
				$0.2 < d \leq$			5	
				$0.4$ < d $\leq$	1		3	
				$0.5  <  \mathrm{d}$			0	
	inspection	Polarizer (	Scratch)			<u> </u>		
(Defect on				Width (mm)	Length (n	nm)	Acceptable number	
Polarizer				$W \leq 0.1$			(Neglected)	
between F				$0.1 < W \leq 0.3$	L ≦	5.0	(Neglected)	
and LCD	glass)			$\begin{array}{c ccccccccccccccccccccccccccccccccccc$		0		
				0.3 < ₩			0	
		Polarizer (	Bubble)					
				Size (mm		Ac	ceptable number	
				d ≦	1		(Neglected)	
				$0.2 < d \leq$			5	
				$0.3 < d \leq$	0.5		3	
				0.5 < d			0	
		Foreign pa	rticle					
		(Circular	shape)	Size (mm	l)	Ac	ceptable number	
				d ≦			(Neglected)	
				$0.2 < d \leq$			5	
				$0.4 < d \leq$	0.5		3	
				0.5 < d			0	
		Foreign pa	rticle					
		(Linear s		Width (mm)	Length	(mm)	Acceptable number	
		Scratch		W $\leq$ 0.03			(Neglected)	
						$\leq 2.0$	(Neglected)	
				$0.03 < W \leq 0.1$	2.0 < L	$\leq 4.0$	3	
					4.0 < L		0	
				0.1 < W	-		(According to	
							circular shape)	
				<del> </del>				



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