



# **SPECIFICATION**



# TCG121XGLPAPNN-AN20-TA

12.1" - XGA - LVDS

Version:

Date: 24.06.2022

Note: This specification is subject to change without prior notice

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# **SPEC for Mass Production**

Spec No.	TQ3C-8EAF0-E1YAL85-00
Date	June 24, 2022

#### TYPE: TCG121XGLPAPNN-AN20-TA

< 12.1 inch XGA transmissive color TFT with LED backlight and constant current circuit for LED backlight>

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#### KYOCERA CORPORATION

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# Warning

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control, home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.
- 2. Customer agrees to indemnify, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs, and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

#### Caution

- 1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.
- 2. Please note that we may not be able to respond to new environmental regulations after receiving the final mass production order for this product.



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#### Revision record

	Revision record						
	Date			Engineering of		Confirmed by	
	Date	Prepa	ared	Checked	Approved	Checked	Approved
Rev.No.	Date	Page			Descripti	ons	

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# 1. Application

This document defines the specification of TCG121XGLPAPNN-AN20-TA. (RoHS Compliant)

#### 2. Construction and outline

LCD : Transmissive color dot matrix type TFT

Backlight system : LED

Polarizer : Anti-Glare treatment

Interface : LVDS

Additional circuit : Timing controller, Power supply (3.3V input)

With constant current circuit for LED Backlight (12V input)

# 3. Mechanical specifications

Item	Specification	Unit
Outline dimensions 1)	260.5(W)×(203.4)(H)×10.3(D)	mm
Active area	245.76(W)×184.32(H) (30.8cm/12.1 inch(Diagonal))	mm
Dot format	1,024×(B,G,R)(W)×768(H)	dot
Dot pitch	0.08(W)×0.24(H)	mm
Base color 2)	Normally Black	-
Mass	670	g

- 1) Projection not included. Please refer to outline for details.
- 2) Due to the characteristics of the LCD material, the color varies with environmental temperature.



#### 4. Absolute maximum ratings

#### 4-1. Electrical absolute maximum ratings

	Symbol	Min.	Max.	Unit	
Supply voltage(	+3.3V)	$ m V_{DD}$	-0.3	3.95	V
Supply voltage(	+12V)	$V_{\rm IN}$	-0.3	14.0	V
	RxINi+, RxINi- (i=0,1,2,3)	$V_{I1}$	-0.3	$V_{\mathrm{DD}}$ +0.3	V
Input signal	CK IN+, CK IN-	$V_{I2}$	-0.3	$V_{\mathrm{DD}}$ +0.3	V
Voltage 1)	MODE, SC	$V_{I3}$	-0.3	$V_{\mathrm{DD}}$ +0.3	V
	BLBRT, BLEN	$V_{I4}$	-0.3	$V_{\mathrm{IN}}$	V

1) V<sub>DD</sub> must be supplied correctly within the range described in 5-1.

#### 4-2. Environmental absolute maximum ratings

Item	Symbol	Min.	Max.	Unit
Operating temperature(Ambient) 1)	Top(Ambient)	-30	80	$^{\circ}\mathrm{C}$
Operating temperature(Panel) 2)	T <sub>OP</sub> (Panel)	-30	80	$^{\circ}\mathrm{C}$
Storage temperature 3)	$T_{\mathrm{STO}}$	-30	80	°C
Operating humidity 4)	Нор	10	5)	%RH
Storage humidity 4)	${ m H}_{ m STO}$	10	5)	%RH
Vibration	-	6)	6)	-
Shock	-	7)	7)	-

- 1) Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25°C, another temperature range should be confirmed.
- 2) Panel surface temperature (all the surface)
- 3) Temp. = -30°C< 48h, Temp. = 80°C< 168h

Store LCD at normal temperature/humidity. Keep them free from vibration and shock. An LCD that is kept at a low or a high temperature for a long time can be defective due to other conditions, even if the low or high temperature satisfies the standard.

(Please refer to "Precautions for Use" for details.

- 4) Non-condensing
- 5) Temp.  $\leq 40$ °C, 85%RH Max.

Temp. >40°C, Absolute humidity shall be less than 85%RH at 40°C.

6)

Frequency	10∼55 Hz	Acceleration value
Vibration width	0.15mm	$(0.3\sim 9 \text{ m/s}^2)$
Interval	10-55-1	0 Hz 1minute

2 hours in each direction X, Y, Z (6 hours total) EIAJ ED-2531

7) Acceleration: 490 m/s<sup>2</sup>, Pulse width: 11 ms

3 times in each direction:  $\pm X$ ,  $\pm Y$ ,  $\pm Z$ 

**EIAJ ED-2531** 



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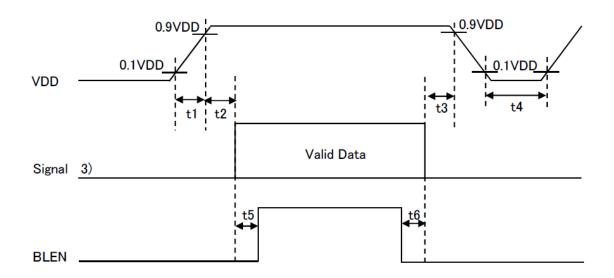
### 5. Electrical characteristics

### 5-1. LCD

Temp. =  $-30 \sim 80$ °C

						remp. –	30° ° 30° C
Item		Symbol	Condition	Min.	Typ.	Max.	Unit
Supply voltage	1)	$V_{ m DD}$	-	3.0	3.3	3.6	V
Current consumption		$I_{\mathrm{DD}}$	2)	-	300	390	mA
Permissive input ripple volt	age	$V_{\mathrm{RP}}$	V <sub>DD</sub> =3.3V	-	-	100	mVp-p
T 1 1,	0)	$V_{\mathrm{IL}}$	"Low" level	0	-	$0.3V_{\mathrm{DD}}$	V
Input signal voltage	3)	$V_{\mathrm{IH}}$	"High" level	$0.7 V_{\mathrm{DD}}$	-	$V_{ m DD}$	V
T , 1 1		$I_{\mathrm{OL}}$	V <sub>13</sub> =0V	-10	-	10	μΑ
Input leak current		Іон	$V_{I3} = 3.3V$	-	-	400	$\mu$ A
LVDS Input voltage	4)	$V_{\rm L}$	-	0	-	1.9	V
Differential input voltage	4)	$V_{\mathrm{ID}}$	-	200	-	600	mV
Differential input	4) 5)	$V_{\mathrm{TL}}$	"Low" level	V <sub>CM</sub> -100	-	-	mV
threshold voltage	4) 5)	$V_{\mathrm{TH}}$	"High" level	-	-	V <sub>CM</sub> +100	mV
Terminator		$R_1$	-	-	100	-	Ω
		t1	-	0.1	-	20	ms
		t2	-	10	-	-	ms
77	1) (1)	t3	-	0		-	ms
V <sub>DD</sub> -turn-on conditions	1) 6)	t4	-	2	-	-	s
		t5	-	200	-	-	ms
		t6	-	200	-	-	ms

### 1) V<sub>DD</sub>-turn-on conditions

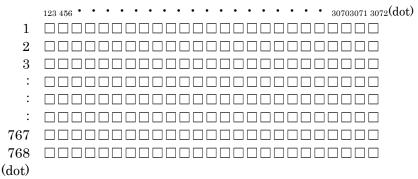




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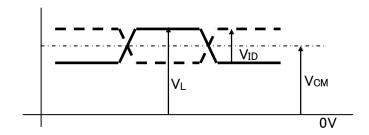
2) Display pattern:

$$V_{DD} = 3.3V$$
, Temp. = 25°C



3) Input signal: MODE, SC

4) Input signal : RxIN3+, RxIN3-, RxIN2+, RxIN2-, RxIN1+, RxIN1-, RxIN0+, RxIN0- CK IN+, CK IN-



5) V<sub>CM</sub>: LVDS Common mode voltage (V<sub>CM</sub>=1.25V)

6) Please power on LVDS transmitter at the same time as VDD, or LVDS transmitter should be powered on first.



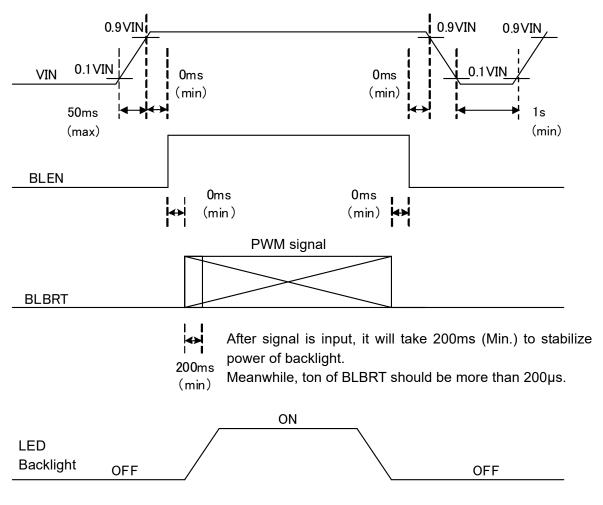
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### 5-2. Constant current circuit for LED Backlight

Temp. =  $-30 \sim 80$ °C

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Supply voltage 1)	$V_{\rm IN}$	-	10.8	12.0	13.2	V
Current consumption	$I_{\rm IN}$	2)	-	480	560	mA
Permissive input ripple voltage	$V_{\mathrm{RP\_BL}}$	V <sub>IN</sub> =12.0V	i	-	100	mVp-p
DI DDT Issued simual realts as	V <sub>IL_BLBRT</sub>	"Low" level	0	-	0.8	V
BLBRT Input signal voltage	V <sub>IH_BLBRT</sub>	"High" level	2.3	-	$V_{\mathrm{IN}}$	V
BLBRT Input pull-down resistance	R <sub>IN_BLBRT</sub>	•	100	300	500	$k\Omega$
DI EN Insust signal meltage	V <sub>IL_BLEN</sub>	"Low" level	0	-	0.8	V
BLEN Input signal voltage	V <sub>IH_BLEN</sub>	"High" level	2.3	-	$V_{\mathrm{IN}}$	V
BLEN Input pull-down resistance	R <sub>IN_BLEN</sub>	•	100	300	500	$k\Omega$
PWM Frequency 3)	fрwм	-	200	-	10k	Hz
		$f_{PWM}=200Hz$	1	-	100	%
PWM Duty ratio 3)	$\mathrm{D}_{\mathrm{PWM}}$	$ m f_{PWM}$ = $2kHz$	10	-	100	%
		f <sub>PWM</sub> =10kHz	50	-	100	%
Operating life time 4), 5)	Т	Temp.=25°C	-	50,000	-	h

### 1) V<sub>IN</sub>-turn-on conditions

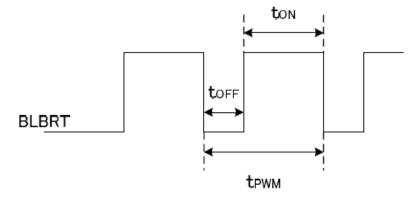


2)  $V_{IN} = 12V$ , Temp. =  $25^{\circ}$ C,  $D_{PWM} = 100\%$ 



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### 3) PWM timing diagram



Please do not set toff with 0  $\mu$  s < toff < 1  $\mu$  s.

In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.

- 4) When brightness decrease 50% of minimum brightness.

  The average life of a LED will decrease when the LCD is operating at higher temperatures.
- 5) Life time is estimated data. (Condition :  $D_{PWM} = 100\%$ , Ta=25% in chamber).



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# 6. Optical characteristics

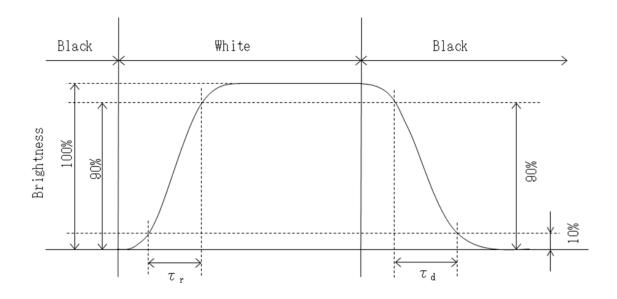
Measuring spot =  $\phi$  6.0mm, Temp. = 25°C

Item		Symbol	Condition	Min.	Тур.	Max.	Unit
Rise		τr	$\theta = \phi = 0^{\circ}$	-	18	-	ms
Response time	Down	τd	$\theta = \phi = 0^{\circ}$	-	12	-	ms
		$\theta$ upper		-	85	-	1
Viewing angle r	ange	$\theta$ lower	CR≧10	-	85	-	deg.
View direction		$\phi$ LEFT	CK≦10	-	85	-	1
		φ right		-	85	-	deg.
Contrast ratio	Contrast ratio		$\theta = \phi = 0$ °	525	750	-	-
Brightness		L	$D_{PWM} = 100\%$	280	400	-	cd/m²
	Red	X	$\theta = \phi = 0$ °	0.540	0.590	0.640	
		У		0.305	0.355	0.405	
	Consor	X	0 1 00	0.275	0.325	0.375	
Chromaticity	Green	У	$\theta = \phi = 0^{\circ}$	0.535	0.585	0.635	
coordinates	Dlass	X	$\theta = \phi = 0^{\circ}$	0.105	0.155	0.205	-
	Blue	У	$\theta = \phi = 0$	0.075	0.125	0.175	
	VV71- : 4 -	X	0 - 4 -0°	0.250	0.300	0.350	
	White	у	$\theta = \phi = 0$ °	0.280	0.330	0.380	

### 6-1. Definition of contrast ratio

 $CR(Contrast ratio) = \frac{Brightness with all pixels "White"}{Brightness with all pixels "Black"}$ 

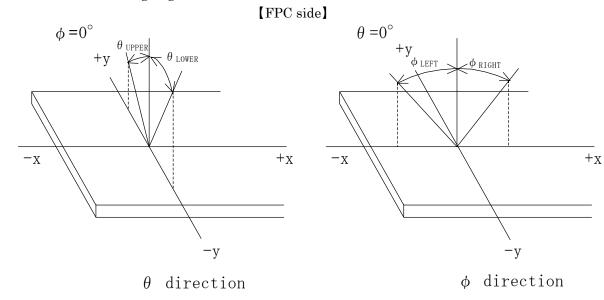
# 6-2. Definition of response time



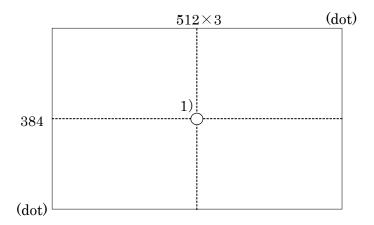


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# 6-3. Definition of viewing angle



### 6-4. Brightness measuring point



- 1) Rating is defined as the white brightness at center of display screen.
- 2) 5 minutes after LED is turned on. (Ambient Temp.=25°C)

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# 7. Interface signals

### 7-1. LCD

No.	Symbol	Description	Note
1	$V_{ m DD}$	+3.3V power supply	
2	$V_{ m DD}$	+3.3V power supply	
3	GND	GND	
4	GND	GND	
5	RxIN0-	LVDS receiver signal CH0(-)	LVDS
6	RxIN0+	LVDS receiver signal CH0(+)	LVDS
7	GND	GND	
8	RxIN1-	LVDS receiver signal CH1(-)	LVDS
9	RxIN1+	LVDS receiver signal CH1(+)	LVDS
10	GND	GND	
11	RxIN2-	LVDS receiver signal CH2(-)	LVDS
12	RxIN2+	LVDS receiver signal CH2(+)	LVDS
13	GND	GND	
14	CK IN1-	LVDS receiver signal CK(-)	LVDS
15	CK IN1+	LVDS receiver signal CK(+)	LVDS
16	GND	GND	
17	RxIN3-	LVDS receiver signal CH3(-)	LVDS
18	RxIN3+	LVDS receiver signal CH3(+)	LVDS
19	MODE	Bit data select signal(GND: 6bit mode, High: 8bit mode)	
20	SC	Scan direction control	1)

LCD connector : 20186-020E-11F (I-PEX) Matching connector : FI-S20S (JAE)

LVDS receiver : Embedded in ASIC

Matching LVDS transmitter : THC63LVDM83D(THine Electronics) or compatible

### 1) Scan direction

SC:GND SC:High







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# 7-2. LED

No.	Symbol	Description	Note
1	$ m V_{IN}$	+12V power supply	
2	$V_{\rm IN}$	+12V power supply	
3	BLBRT	PWM signal(Brightness adjustment)	
4	BLEN	ON/OFF terminal voltage	
5	GND	GND	
6	GND	GND	



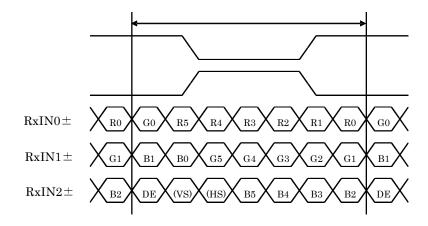
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# 7-3. Data mapping (6bit input)

# 1) Location of MODE (THC63LVDM83D (THine Electronics) or compatible)

Transi	mitter	MODE				
Pin No.	Data	= L(GND)				
51	TA0	R0(LSB)				
52	TA1	R1				
54	TA2	R2				
55	TA3	R3				
56	TA4	R4				
3	TA5	R5(MSB)				
4	TA6	G0(LSB)				
6	TB0	G1				
7	TB1	G2				
11	TB2	G3				
12	TB3	G4				
14	TB4	G5(MSB)				
15	TB5	B0(LSB)				
19	TB6	B1				
20	TC0	B2				
22	TC1	В3				
23	TC2	B4				
24	TC3	B5(MSB)				
27	TC4	(HS)				
28	TC5	(VS)				
30	TC6	DE				
50	TD0	GND				
2	TD1	GND				
8	TD2	GND				
10	TD3	GND				
16	TD4	GND				
18	TD5	GND				
25	TD6	(NA)				

# MODE=L (GND)



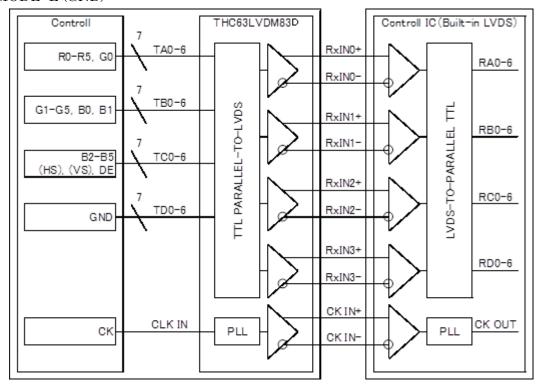
DE : DATA ENABLE

 $\begin{array}{l} HS:\,H_{SYNC}\\ VS:\,V_{SYNC} \end{array}$ 

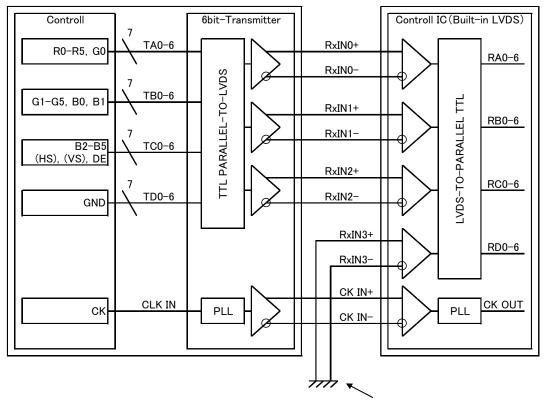


### 2) Block diagram

#### MODE=L (GND)



When using "6-bit Transmitter", please connect the unused channel of the control IC receiver as described in the diagram below.



Please connect RxIn3+/RxIn3- to GND.

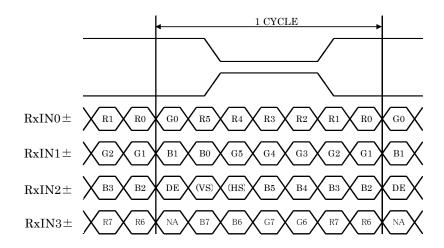


# 7-4. Data mapping (8bit input)

# 1) Location of MODE (THC63LVDM83D (THine Electronics) or compatible)

1) Location of MODI	2 (111CO3L v DMC3D (1	Time Electronics, or comp
Tran	nsmitter	MODE
Pin No.	Data	= H(3.3V)
51	TA0	R0(LSB)
52	TA1	R1
54	TA2	R2
55	TA3	R3
56	TA4	R4
3	TA5	R5
4	TA6	G0(LSB)
6	TB0	G1
7	TB1	G2
11	TB2	G3
12	TB3	G4
14	TB4	G5
15	TB5	B0(LSB)
19	TB6	B1
20	TC0	B2
22	TC1	В3
23	TC2	B4
24	TC3	B5
27	TC4	(HS)
28	TC5	(VS)
30	TC6	DE
50	TD0	R6
2	TD1	R7(MSB)
8	TD2	G6
10	TD3	G7(MSB)
16	TD4	В6
18	TD5	B7(MSB)
25	TD6	(NA)

### MODE = H (3.3V)



DE : DATA ENABLE

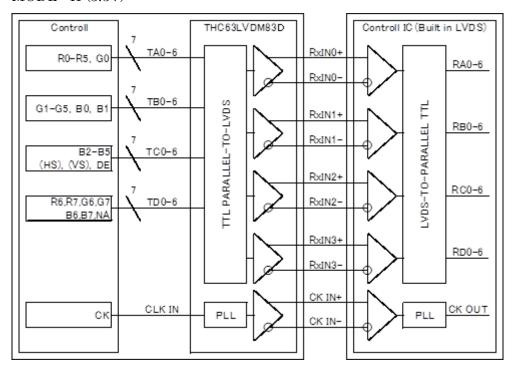
 $\begin{array}{l} HS: H_{SYNC} \\ VS: V_{SYNC} \end{array}$ 



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# 2) Block diagram

# MODE= H (3.3V)





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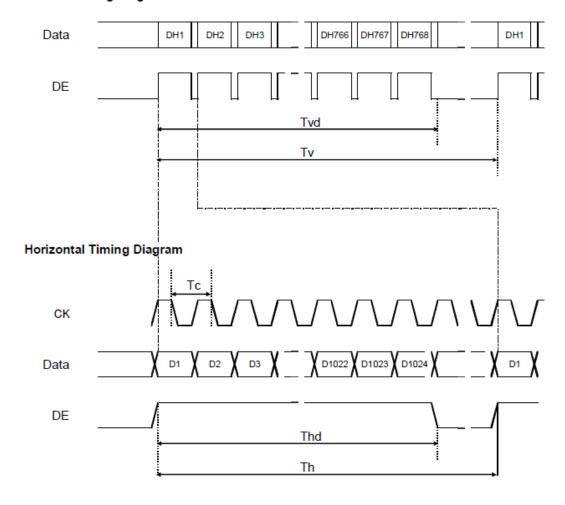
### 8. Input timing characteristics

#### 8-1. Timing characteristics 1)

	Item			Typ.	Max.	Unit	Note
Clock (CK)	Clock (CK) Frequency		52	65	71	MHz	
	Horizontal Period	Th	1,114	1,344	1,400	Dot	
	norizontai Feriod	111	15.7	20.7	23.7	$\mu$ s	2)
Enable signal (DE)	Horizontal display period	Thd		1,024		Тс	
(DL)	Vertical Period	Tv	778	806	845	Line	
	Vertical display period	Tvd		768		Th	
Refresh rate	fv	50	60	82	Hz	3)	

- 1) If the display is used under the condition which is out of specifications such as higher clock frequency than specified value, there is a possibility phenomenon such as display error including white display, malfunction and no image may occur.
  - Please use the display under the conditions written in the specification.
- 2) Please set a clock frequency, a vertical dormant period, and the horizontal dormant period so that the Horizontal Period should not reach less than Min. value.
- 3) If the refresh rate reach less than Min. value, the deterioration of the display quality, flicker etc. may occur. (fv=1/Tv)

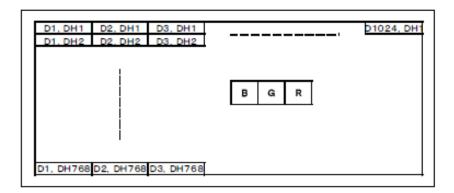
#### Vertical Timing Diagram





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8-2. Input data signals and display position on the screen



# 8-3. Input data signal and display colors

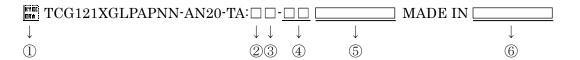
Display	y colors										gnal														
Dispia		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	В7	В6	B5	B4	В3	B2	B1	В0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
œ	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
lor	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
00	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Basic colors	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Ba	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ale		0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
SCS	dark	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ay	1						:							:									:		
Red gray scale	$\downarrow$						:							:									:		
ed	bright	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PA PA		1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Ф	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
cal	, ,	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
S S	dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
Green gray scale	Ī													:											
n s	↓ ↓	0	0	0			:	0	_	١.						0			0	0	0	0	:		0
ree	bright	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
Ü	a	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green Black	0	0	0	0	0	0	0	0	1	1	1	1	0	1	1	0	0	0	0	0	0	0	0	0
Ф	ыаск	0	0	0	0	0	0	0	0	0	0	0	0		0	0	0	0	0	0	0	0	0	0	0 1
ale	dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0 1	0
) s /	aark ↑	U	U	U	U	U	. 0	U	U	U	U	U	U	U.	U	U	U	0	U	U	U	U	. 0	1	U
Blue gray scale														:											
9) 90	↓ hniaht	0	0	0	0	0		0	0	٨	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	bright	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
Н Н	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Brue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1



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#### 9. Lot number identification

The lot number shall be indicated on the back of the backlight case of each LCD.



No.① - No.⑥ above indicate

- ① Data matrix (For internal control purpose only)
- 2 Year code (The last digit of the year)
- ③ Month code
- 4 Day code
- 5 Version number (Max. 7 characters)
- 6 Country of origin

### ③ Month code

Ī	Month	Jan.	Feb.	Mar.	Apr.	May	Jun.
	Code	1	2	3	4	5	6

Ī	Month	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
	Code	7	8	9	X	Y	Z

#### 10. Warranty

#### 10-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

#### 10-2. Production warranty

Kyocera warrants the LCD for a period of 12 months from the ship date. Kyocera shall, by mutual agreement, replace or re-work defective LCD that is shown to be Kyocera's responsibility.



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#### 11. Precautions for use

#### 11-1. Installation of the LCD

- 1) Please ground either of the mounting (screw) holes located at each corner of an LCD, in order to stabilize brightness and display quality.
- 2) A transparent protection plate shall be added to protect the LCD and its polarizer.
- 3) The LCD shall be installed so that there is no pressure on the LSI chips.
- 4) Since this product is wide viewing product, occurrence level of in-plane unevenness by the external stress is different compared to current normal viewing product. So there is a possibility that in-plane unevenness will be occurred by over twist, strain giving by attaching to LCD, and over pressure to touch panel. Please be careful of stress when designing the housing.
- 5) A transparent protection sheet is attached to the polarizer. Please remove the protection film slowly before use, paying attention to static electricity.

#### 11-2. Static electricity

- 1) Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required.
- 2) Workers should use body grounding. Operator should wear ground straps.

#### 11-3. LCD operation

- 1) The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.
- 2) Please select the best display pattern based on your evaluation because flicker, lines or nonuniformity or unevenness can be visible depending on display patterns.

#### 11-4. Storage

- 1) The LCD shall be stored within the temperature and humidity limits specified. Store in a dark area, and protect the LCD from direct sunlight or fluorescent light.
- 2) Always store the LCD so that it is free from external pressure onto it.

#### 11-5. Usage

- 1) <u>DO NOT</u> store in a high humidity environment for extended periods. Polarizer degradation bubbles, and/or peeling off of the polarizer may result.
- 2) The front polarizer is easily scratched or damaged. Prevent touching it with any hard material, and from being pushed or rubbed.
- 3) The LCD screen may be cleaned by wiping the screen surface with a soft cloth or cotton pad using a little Ethanol.
- 4) Water may cause damage or discoloration of the polarizer. Clean condensation or moisture from any source immediately.
- 5) Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizer.
- 6) Do not disassemble LCD because it will result in damage.
- 7) This Kyocera Display LCD has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas. Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.
- 8) Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend using screen saver etc. in cases where a solid-base image pattern must be used.
- 9) Liquid crystal may leak when the LCD is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body; rinse it off right away with water and soap.



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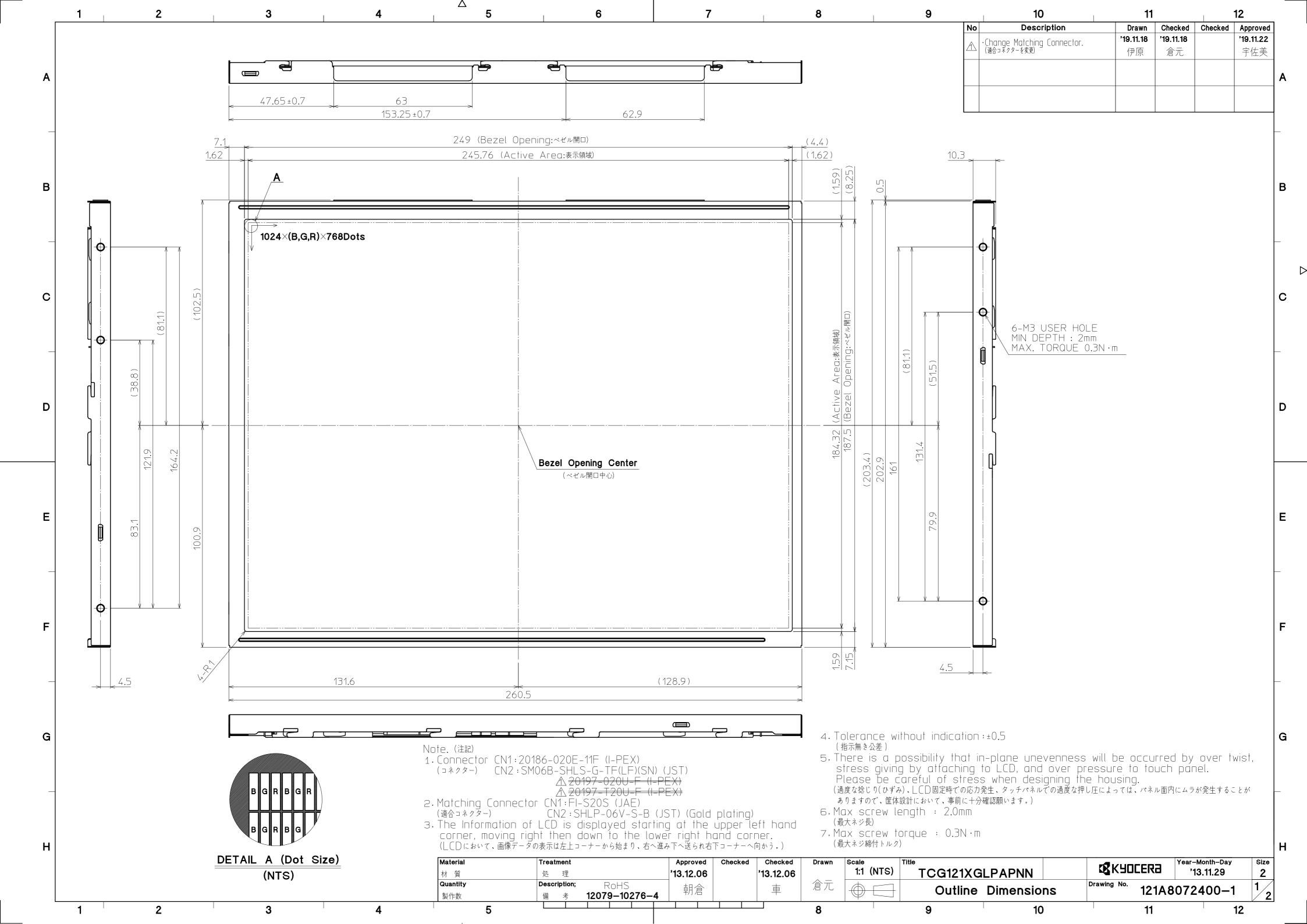
# 12. Reliability test data

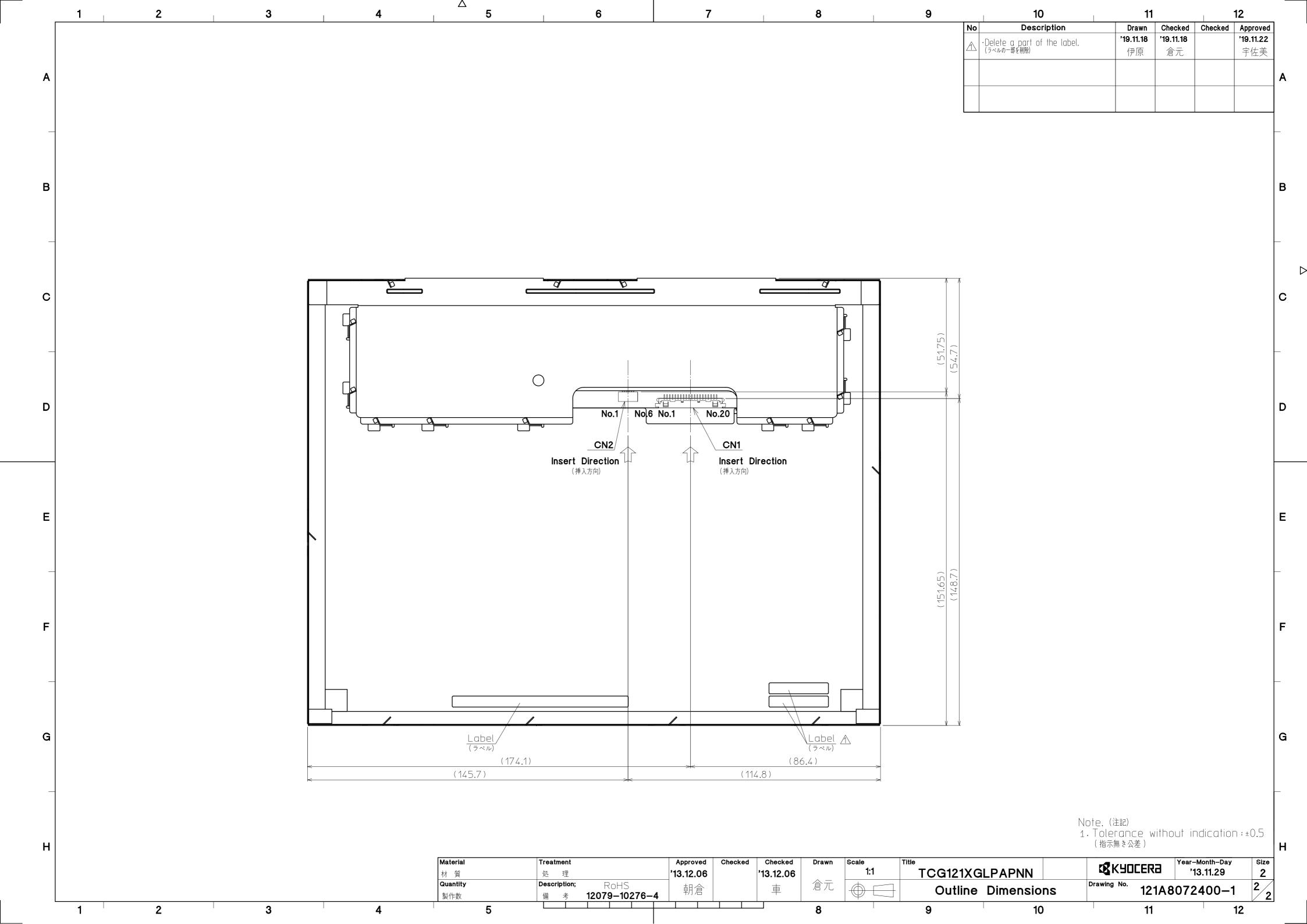
Test item	Test condition	Test time	Jud	gement
High temp. atmosphere	80°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Low temp. atmosphere	-30°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. humidity atmosphere	40°C 90% RH	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Temp. cycle	-30°C 0.5h R.T. 0.5h 80°C 0.5h	10cycles	Display function Display quality Current consumption	<ul><li>: No defect</li><li>: No defect</li><li>: No defect</li></ul>
High temp. operation	80°C	500h	Display function Display quality Current consumption	: No defect : No defect : No defect

- 1) Each test item uses a test LCD only once. The tested LCD is not used in any other tests.
- 2) The LCD is tested in circumstances in which there is no condensation.
- 3) The reliability test is not an out-going inspection.
- 4) The result of the reliability test is for your reference purpose only.

  The reliability test is conducted only to examine the LCD's capability.







Spec No.	TQ3C-8EAF0-E2YAL85-00
Date	June 24, 2022

# KYOCERA INSPECTION STANDARD

TYPE: TCG121XGLPAPNN-AN20-TA

### KYOCERA CORPORATION

Original	Designed by: I	Engineering de <sub>l</sub>	Confirmed by: QA dept.		
Issue Date	Prepared	Checked	Approved	Checked	Approved
June 24, 2022	K. komurasaki	I. Kawajiri	A. Iwasaki	$\it Y. Aritsubo$	M. Kinouchi



Spec No.	Part No		Page
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# Revision record

	Date	Designed by : Engineering dept.			Confirmed by : QA dept.		
	Date	Prepa	ared	Checked	Approved	Checked	Approved
Rev.No.	Date	Page			Descripti	ons	

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# Visuals specification 1) Note

1) Note						
			Note			
General	by Kyoo 2. This ins	era, and an additional stand spection standard about the	defined within this inspection standard shall be reviewed dard shall be determined by mutual consent.  image quality shall be applied to any defect within the			
		rea and shall not be applicable to outside of the area.				
	_	ion conditions				
	Lumina		: 500 Lux min.			
		ion distance	$300 \text{ mm}.$ $25 \pm 5\%$			
	Temper Direction		Directly above			
Definition of	Dot defect	Bright dot defect	The dot is constantly "on" when power applied to the			
inspection	Dot defect	Dright dot defect	LCD, even when all "Black" data sent to the screen.			
item			Inspection tool: 5% Transparency neutral density filter.			
			Count dot: If the dot is visible through the filter.			
			Don't count dot: If the dot is not visible through the			
			filter.  RGBRGBRGB  RGBRGBRGB  RGBRGBRGB  dot defect			
		Black dot defect	The dot is constantly "off" when power applied to the			
			LCD, even when all "White" data sent to the screen.			
			Similar size compared to bright dot.			
		White dot	Pixel works electrically, however, circular/foreign			
		(Circular/foreign	particle makes dot appear to be "on" even when all			
		particle)	"Black" data is sent to the screen.			
		Adjacent dot	Adjacent dot defect is defined as two or more bright dot defects or black dot defects.			
			R G B R G B R G B R G B R G B R G B R G B R G B R G B dot defect			
	External	Bubble, Scratch, Foreign	Visible operating (all pixels "Black" or "White") and non			
	inspection	particle	operating.			
		(Polarizer, Cell,				
		Backlight)				
		Appearance inspection	Does not satisfy the value at the spec.			
	Definition	Definition of circ	ele size Definition of linear size			
	of size	a: major axis, b: m	inor axis			
		d = (a + b) /	2			



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#### 2) Standard

2) Standa	ra fication	Incons	tion itom		Indeen	atanda-	d	
Defect	1	_	tion item	Judgement standard				
	Single	Bright dot	defect	Acceptable number		: 4 . •		
(in LCD	dot	Black dot defect		3 1 0			n or more	
glass)					Acceptable number : 5			
		_	T	Black dot spacing	n or more			
	Adjacent dot	2 dots	Bright dot defect	Acceptable number		: 2		
			Black dot defect	Acceptable number		: 3		
		3 or more	dots	Acceptable number		: 0		
	Total dot	defects		Acceptable number		∶5 Ma	X	
	Others	White dot,	, Dark dot					
		(Circle)		Size (mm		Ac	ceptable number	
				d ≦			(Neglected)	
				0.2 < d ≦			5	
				0.4 < d ≦	0.5		3	
				0.5 < d			0	
External	inspection	Polarizer (	(Scratch)	( )		, 1		
(Defect or	ı			Width (mm)	Length (1	mm)	Acceptable number	
Polarizer	or			$W \leq 0.1$			(Neglected)	
between l	Polarizer			$0.1 < W \le 0.3$	5.0 < L	≦ 5.0	(Neglected)	
and LCD	and LCD glass)			0.3 < W	0.0 < L		0	
				0.0 < 11		i	U .	
		Polarizer (	(Bubble)					
				Size (mm	)	Ac	ceptable number	
				d ≦	0.2		(Neglected)	
				0.2 < d ≦			5	
				0.3 < d ≦	0.5		3	
				0.5 < d			0	
		Foreign pa	article					
		(Circular	shape)	Size (mm	)	Ac	ceptable number	
				d ≦			(Neglected)	
				0.2 < d ≦	0.4		5	
				0.4 < d ≦	0.5		3	
				0.5 < d			0	
		Foreign pa	ntialo					
		(Linear s		Width (mm)	Length	(mm)	Acceptable number	
		Scratch	snape)	W ≤ 0.03	_		(Neglected)	
		Scratch			L	$\leq 2.0$	(Neglected)	
				$0.03 < W \le 0.1$	2.0 < L	≦ 4.0	3	
					4.0 < L		0	
				0.1 < W			(According to	
				0.1 < W			circular shape)	
l		Cali	-4:					
		Color vari	auon	Not to be significantly visible.				
(		(Mura)		Consultation shall be held as necessary.				



Spec No.	TQ3C-8EAF0-E3YAL85-00
Date	June 24, 2022

# KYOCERA PACKAGING STANDARD

TYPE: TCG121XGLPAPNN-AN20-TA

### KYOCERA CORPORATION

Original	Designed by: I	Engineering de <sub>l</sub>	Confirmed by: QA dept.		
Issue Date	Prepared	Checked	Approved	Checked	Approved
June 24, 2022	K. Komurasaki	I. Kawajiri	A. Iwasaki	Y. Aritsubo	M. Kinouchi

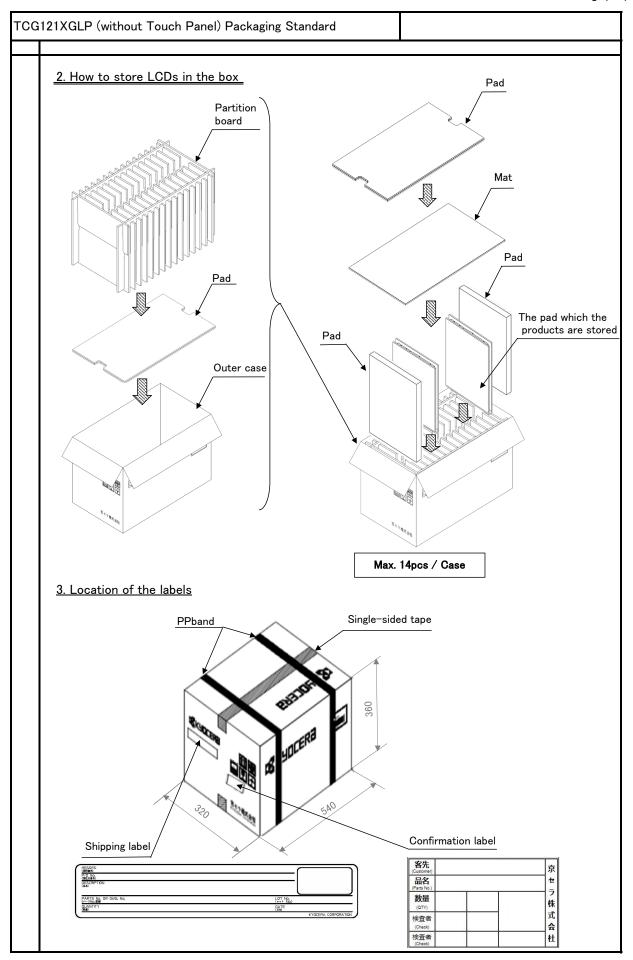


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# Revision record

Date		Designed by : Engineering dept.				Confirmed by : QA dept.	
		Prepared		Checked	Approved	Checked	Approved
Rev.No.	Date	Page		Descripti		ons	

TCG121XGLP (without Touch Panel) Packaging Standard 1. How to store LCDs in the pad Pad Anti-static bag Max. 1pc / pad







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