



SPECIFICATION



TN0227ANVNANN-GN*03

2.27" - MIP TFT

Version: Date: 14.04.2022

Note: This specification is subject to change without prior notice

www.data-modul.com

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Original	Original Designed by: Engineering dept. Confirmed by: QA dept.							
Issue Date	Prepared Checked		Approved	Checked	Approved			
April 13, 2022	M. Yamada M. Iwasaki		A. Iwasaki	Y. Aritsubo	M. Kinouchi			



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Warning

1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control, home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss.

Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.

2. Customer agrees to indemnify, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs, and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

Caution

- 1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.
- 2. Please note that we may not be able to respond to new environmental regulations after receiving the final mass production order for this product.

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1. Application

This document defines the specification of TN0227ANVNANN-GN*03. (RoHS Compliant)

2. Construction and Outline

LCD	Reflective (slightly transmissive) dot matrix memory LC display The display should be readable with polarized sunglasses in portrait mode.
Backlight System	: None
Polarizer	: Glare type
Additional circuit	: 1-bit pixel memory function in LCD

3. Mechanical Specifications

Item	Specification	Unit
Outline dimensions 1)	$41.65(W) \times 48.5(H)$	mm
Glass thickness	0.5 + 0.5	mm
Active area	36.00(W) × 45.00(H) (Diagonal 2.27 inch)	mm
Dot format	$240(W) \times 300(H)$	Dot
Dot pitch	$150(W) \times 150(H)$	μm
Color	Black-and-White (Binary)	-
LC Mode	ECB mode(Normally black)	-
Weight	TBD	g

1) FPC is not included. Please refer to the drawing for details.



4. Absolute Maximum Ratings

4-1. Electrical absolute maximum ratings

Item	Symbol	Min.	Max.	Unit
Supply voltage	VDD	-0.3	4.0	V
Input signal voltage 1)	VIN	-0.3	4.0	V

1) Input signals : SCLK, SI, SCS, RST, VCOM

4-2. Environmental absolute maximum ratings

Item		Symbol	Min.	Max.	Unit
Operating temperature	1)	Тор	-25	80	°C
Storage temperature	2)	Тѕто	-30	85	°C
Operating humidity	3)	Hop	10	4)	%RH
Storage humidity	3)	$H_{\rm STO}$	10	4)	%RH

- 1) Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25°C, another temperature range should be confirmed.
- 2) Store LCD at normal temperature/humidity. Keep them free from vibration and shock. An LCD that is kept at a low or a high temperature for a long time can be defective due to other conditions, even if the low or high temperature satisfies the standard. (Please refer to "Precautions for Use" for details.)
- 3) Non-condensing
- 4) Temp. $\leq 40^{\circ}$ C , 85%RH Max. Temp. >40°C, Absolute humidity shall be less than 85%RH at 40°C



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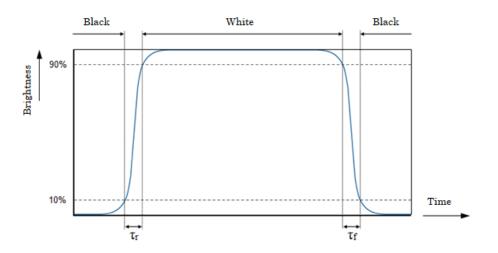
5. Optical Characteristics

						Τe	emp. = 25°C
Iten	ı	Symbol	Temp.	Unit	Min.	Typ.	Max.
Contrast r	atio 1)	CR	$25^{\circ}\!\mathrm{C}$	-	-	(40)	-
Reflects	ance	-	$25^{\circ}\!\mathrm{C}$	%	-	(18)	-
			-20°C		-	(16)	-
	Rise	τr	$25^{\circ}\!\mathrm{C}$	msec	-	(12)	-
Response time			40°C		-	(12)	-
2)3)			-20°C	msec	-	(34)	-
	Fall 7	τf	$25^{\circ}\!\mathrm{C}$		-	(13)	-
			40°C		-	(12)	-
		θupper			-	(60)	-
Viewing CR≧		θlower	$25^{\circ}\!\mathrm{C}$	dom	-	(60)	-
4)5)		θleft	20 C	deg.	-	(60)	-
		θright			-	(60)	-
Chromaticity	Wx	-	o * °O	-	-	(0.31)	-
coordinates	Wy	-	$25^{\circ}\!\mathrm{C}$	-	-	(0.34)	-

1) Definition of contrast ratio

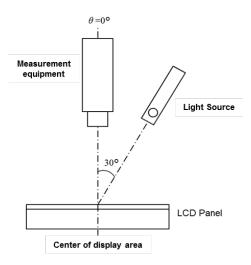
Brightness with all pixels "White" Brightness with all pixels "Black"

2) Definition of response time

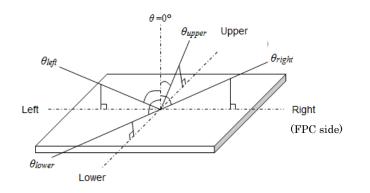


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3) About the measurement of Response time Measurement Equipment (LCD-5200)



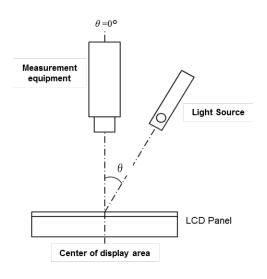
4) Definition of viewing angle



5) About the measurement of viewing angle

Measurement Equipment (LCD-5200)

The viewing angle is measured by changing the angle of the light source.





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6. Electrical Characteristics

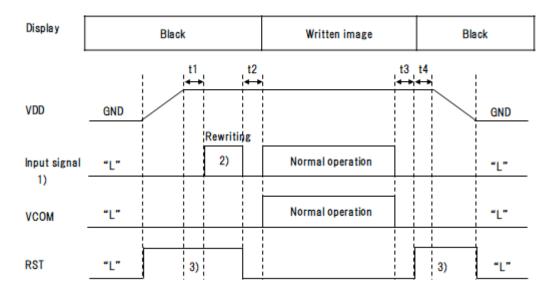
						Temp. = 25	S°C
Item		Symbol	Condition	Min.	Тур.	Max.	Unit
Supply voltage		V_{DD}	_	3.0	3.3	3.6	V
	1) 0)	VIL	"Low" level	GND-0.2	_	VDD*0.25	V
Input signal voltage	1) 2)	V_{IH}	"High" level	VDD*0.75	_	VDD+0.2	V
Input leak current	1)	I_IN	$T_{OP}=25^{\circ}C$	_	5	20	nA
Current concernation	3)	I_{DD_opr}	4)	_	TBD	TBD	μΑ
Current consumption	J)	I_{DD_stb}	5)	_	TBD	TBD	μΑ
Input capacitance	1)	$\mathrm{C}_{\mathrm{IN1}}$	_	_	5	10	pF

6-1. LCD driving characteristics

- 1) SCLK, SI, SCS, RST, VCOM
- 2) Since input leakage current is less than 20 nA, please control voltage so that V_{IL} =GND and V_{IH} =VDD in static state.
- 3) Value of the current consumption is without smoothing capacitor.
- 4) 1Hz full display rewriting (Without backlight)
- 5) Keeping static image, and polarity inversion of VCOM with a period of 1 sec. (Without backlight)
 SCLK, SCS, SI, RST: "L" state.



6-2. Power ON-OFF sequence



Item	Symbol	Min.	Тур.	Max.	Unit
D	t1	0		_	μsec
Power ON-OFF	t2	1	10	_	msec
	t3	1	10	—	msec
sequence	t4	1	10	_	msec

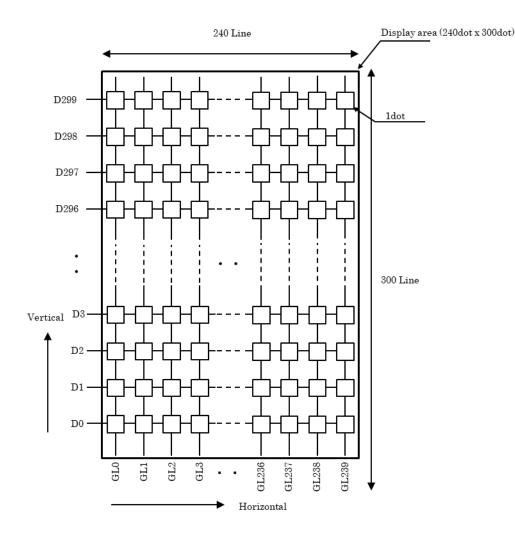
- 1) SCLK, SI, SCS
- 2) Rewriting to black for all addresses.
- 3) Display OFF when RST = "H". VCOM = "L" is necessary when RST = "H". Black display when display is OFF.

Data in pixel memory is random with black or white at power ON. Possible to prevent black-white random data display by black display when RST = "H".



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7. Address Mapping



Addressing of Gate line (GL*) to be selected is necessary for horizontal direction. Rewriting data for one line is necessary for vertical direction due to rewriting one line at once.

AG7	AG6	AG5	AG4	AG3	AG2	AG1	AG0	Selected GL
0	0	0	0	0	0	0	0	GL0
0	0	0	0	0	0	0	1	GL1
0	0	0	0	0	0	1	0	GL2
0	0	0	0	0	0	1	1	GL3
•	•	•	•	•	•	•	•	•
•	•	•	•	•	•	•	•	•
1	1	1	0	1	1	0	1	GL237
1	1	1	0	1	1	1	0	GL238
1	1	1	0	1	1	1	1	GL239

-Addressing table for vertical direction



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8. Interface Signals

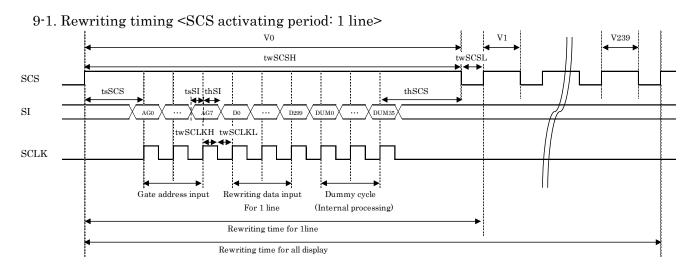
No.	Symbol	Description	I/O 1)	Voltage	Unit	Note
1	VDD	Power supply(3.3V)	Р	3.3	V	
2	VSS	GND	Р	0.0	V	
3	SCLK	Clock signal for serial input	Ι	0.0/3.3	V	3)
4	SCS	Chip select signal	Ι	0.0/3.3	V	3)
5	SI	Serial input signal	Ι	0.0/3.3	V	
6	RST	Display ON/OFF signal	Ι	0.0/3.3	V	2)3)
7	VCOM	Common power supply control	Ι	0.0/3.3	V	
8	VSS	GND	Р	0.0	V	
9	VDD	Power supply(3.3V)	Р	3.3	V	
10	NC	NC	_		_	

Matching connector: 04 6824 610 000 846+ (KYOCERA)

- 1) P: Power supply I: Input
- 2) RST = "L": Display ON RST = "H": Display OFF (Black display keeping pixel memory) VCOM = "L" is necessary when RST = "H" (When VCOM="H", Display does not turn to black, and current consumption increases by shoot-through-current in panel. (several mA)
- 3) Need to guard from signal noise If there is an abnormal signal not described in the timing chart on these signals, display may be distorted. Please carefully guard these signals since even if signal noise with small pulse width may cause malfunction.

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9. Input Timing Characteristics



3 lines (SCS, SCLK, and SI) are control signals to rewrite.

Rewriting 1 gate line by 1 gate line.

Input order of serial data

- 1. Input gate address
- 2. Input rewriting data for 1 horizontal line
- 3. Dummy cycle (internal processing for rewriting)

Relationship between rewriting data and display. Data 0: Display BLACK

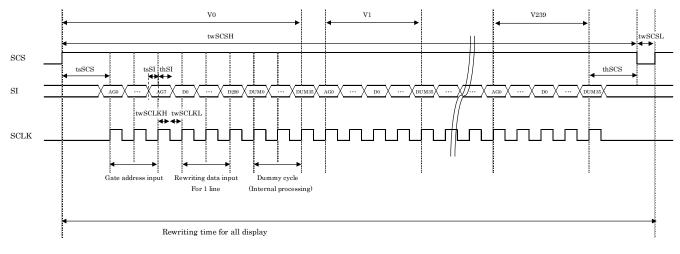
Data 1: Display WHITE

Time to rewrite in case of SCLK frequency = 1.5MHz (twSCLKH=twSCLKL=0.33us)

No.	Item	Parameters		
Serial data input	Gate address	AG0~AG7		
(1 line)	Rewriting data	D0~D299		
	Dummy cycle	DUM0~DUM35		
Total cycle for 1line		344 cycs		
Time to rewrite 1 line		5.236 msec		
Number of gate line		240 lines		
Time to rewrite full	display	1,256.7 msec		

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9-2. Rewriting timing <SCS activating period: 1 frame>



Rewriting V_0 to V max continuously during SCS activating period

3 lines (SCS, SCLK, and SI) are control signals to rewrite.

Rewriting 1 gate line by 1 gate line.

Input order of serial data

- 1. Input gate address
- 2. Input rewriting data for 1 horizontal line
- 3. Dummy cycle (internal processing for rewriting)

Possible to rewrite numerous lines continuously during SCS activating period. Gate addresses for continuous rewriting of numerous lines can be set in any order.

Relationship between rewriting data and display.

Data 0: Display BLACK Data 1: Display WHITE

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Item	Parameters
Gate address	AG0~AG7
Rewriting data	D0~D299
Dummy cycle	DUM0~DUM35
	344 cycs
9	240 lines
display	59.3 msec
	Item Gate address Rewriting data

Time to rewrite in case of SCLK frequency = 1.5MHz (twSCLKH=twSCLKL=0.33us)



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9-3. VCOM AC driving

SCS						
SI	Rewrite	Rewrite	Rewrite	Rewrite	Rewrite	Rewrite
SCLK	Rewrite	Rewrite	Rewrite	Rewrite	Rewrite	Rewrite
VCOM(external)	/				/	
VCOM	/				/	/

AC drive between 0 V and 3.3 V is required for VCOM signal.

External VCOM switching is reflected immediately on VCOM inside the panel when switching is performed at SCS = "L", however when switching is performed in SCS = "H", external VCOM switching is reflected at the next timing when SCS = "L".



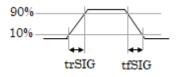
9-4. Timing characteristics

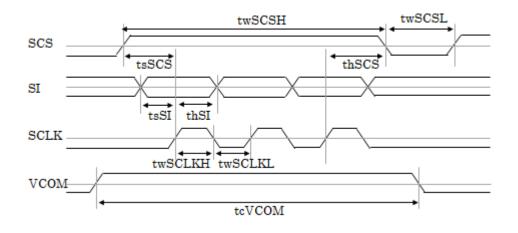
	Item	Symbol	Min.	Тур.	Max.	Unit	Note
Time to rewrite 1 line			5.236	—		msec	
Time to rewri	te full display		59.34	_		msec	
Input signal	Signal rise time	trSIG	_	—	50	nsec	2)
1)	Signal fall time	tfSIG	_	—	50	nsec	2)
	SCLK high width	twSCLKH	0.33	—	—	μsec	3)
SCLK	SCLK low width	twSCLKL	0.33	—	—	μsec	3)
at	SI set-up time	tsSI	0.33	—	_	μsec	3)
SI	SI hold time	thSI	0.33	—		μsec	3)
	SCS high width	twSCSH	5.226	—	—	msec	3)
SCS	SCS low width	twSCSL	10	—	_	µsec	3)
	SCS set-up time	tsSCS	4	—	—	msec	3)
	SCS hold time	thSCS	1	_		msec	3)
NCOM	VCOM duty	_	_	50		%	
VCOM	VCOM cycle time	tcVCOM	_	1000	_	msec	3)

1) SCLK, SI, SCS, RST, VCOM

- 2) Time for the signal to shift from 10% and 90% or 90% to 10%
- 3) Defined as 50% value of the signal.

-Timing charts





10. Warranty

10-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

10-2. Production warranty

Kyocera warrants its LCD's for a period of 12 months from the ship date. Kyocera shall, by mutual agreement, replace or re-work defective LCD's that are shown to be Kyocera's responsibility.



11. Precautions for Use

- 11-1. Installation of the LCD
- 1) A transparent protection plate shall be added to protect the LCD and its polarizer.
- 2) The LCD shall be installed flat, without twisting or bending.
- 3) A transparent protection sheet is attached to the polarizer. Please remove the protection film slowly before use, paying attention to static electricity.
- 4) FPC shall be installed after fixing the glass.
- 5) Do not apply a load to the FPC.
- 6) Do not use an assembly structure where the FPC is pulled or partially stressed.

11-2. Static electricity

- 1) Protect the LCD from static electricity.
- 2) Workers should use body grounding. Operator should wear ground straps.

11-3. LCD operation

1) The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.

11-4. Storage

- The LCD shall be stored within the temperature and humidity limits specified.
 Store in a dark area, and protect the LCD from direct sunlight or fluorescent light.
- 2) Always store the LCD so that it is free from external pressure onto it.

11-5. Usage

- 1) <u>DO NOT</u> store in a high humidity environment for extended periods. Polarizer degradation bubbles, and/or peeling off of the polarizer may result.
- 2) The front polarizer is easily scratched or damaged. Prevent touching it with any hard material, and from being pushed or rubbed.
- 3) The LCD screen may be cleaned by wiping the screen surface with a soft cloth or cotton pad using a little Ethanol
- 4) Water may cause damage or discoloration of the polarizer. Clean condensation or moisture from any source immediately.
- 5) Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizer.
- 6) Do not disassemble LCD because it will result in damage.
- 7) This Kyocera LCD has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas. Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.
- 8) Do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend using screen saver etc. in cases where a solid-base image pattern must be used.
- 9) Liquid crystal may leak when the LCD is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body; rinse it off right away with water and soap.
- 10) Do not hold the FPC alone while working.
- 11) Do not scratch or damage the exposed conductor of the FPC or FPC.
- 12) Do not touch the exposed conductor of the FPC.
- 13) Do not bend the FPC toward the surface of LCD near the glass.
- 14) Do not bend the FPC so that stress is concentrated on the FPC.



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12. Reliability Test Data

Test item Test condition		Test time	Judgement
High temp. atmosphere	85°C	240h	Function/Display: No defectCurrent consumption: No defect
Low temp. atmosphere	-30°C	240h	Function/Display: No defectCurrent consumption: No defect
High temp. humidity atmosphere	60°C 90%RH	240h	Function/Display: No defectCurrent consumption: No defect
Temp. cycle	-30°C 0.5h R.T. 0.5h 85°C 0.5h	10cycles	Function/Display: No defectCurrent consumption: No defect
High temp. operation	80°C	240h	Function/Display: No defectCurrent consumption: No defect

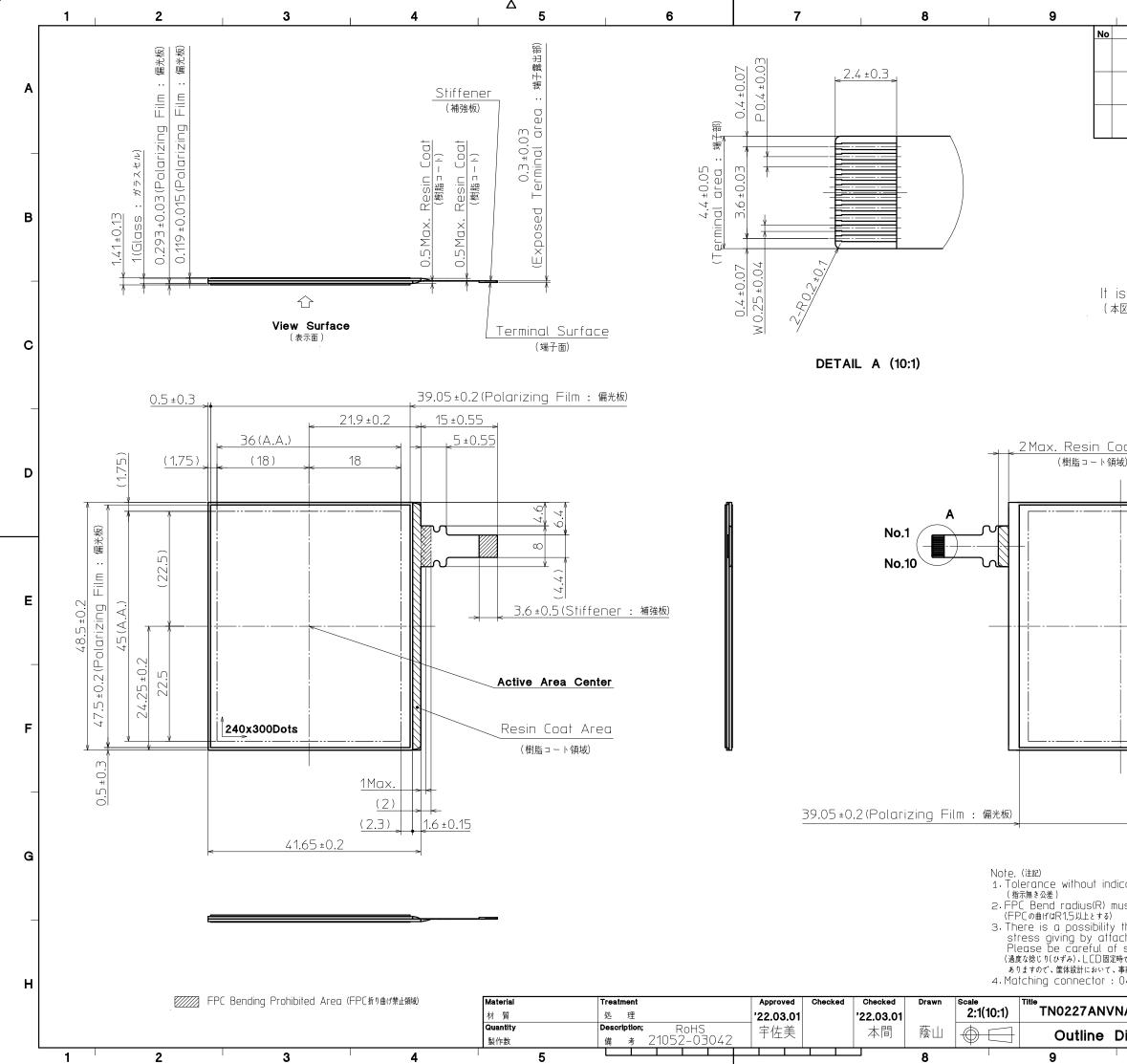
1) Each test item uses a test LCD only once. The tested LCD is not used in any other tests.

2) The LCD is tested in circumstances in which there is no condensation.

3) The reliability test is not an out-going inspection.

 The result of the reliability test is for your reference purpose only. The reliability test is conducted only to examine the LCD's capability.





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	Dimensions						
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Spec No.	TQ3C-8EAF0-E2YCA03-00
Date	April 13, 2022

KYOCERA INSPECTION STANDARD

TYPE : TN0227ANVNANN-GN*03

KYOCERA CORPORATION

Original	Designed by :	Engineering de	Confirmed by : QA dept.		
Issue Date	Prepared	Checked	Approved	Checked	Approved
April 13, 2022	M. Yamada	M. Iwasaki	A. Iwasaki	Y. Aritsubo	M. Kinouchi



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Revision record							
	D			Engineering d		Confirmed by	v: QA dept.
	Date	Prepa		Checked	Approved	Checked	Approved
Rev.No.	Date	Page			Descript	ions	•



Visuals specification

1) Note			Note					
General		ner identified anomalies not defined within this inspection standard shall be reviewed by a, and an additional standard shall be determined by mutual consent.						
		2. Dot defect and external inspection about the image quality shall be applied to any defect within the active area and shall not be applicable to outside of the active area.						
	Lumin	tion distance rature	 : 500 Lux min. : 300 mm. : 25 ± 5℃ : Directly above 					
Definition of inspection item	Dot defect	Bright dot defect Black dot defect (Included circular shape) White dot	The dot is constantly "on" when power applied to the LCD, even when all "Black" data sent to the screen. Inspection tool: 5% Transparency neutral density filter. Count dot: If the dot is visible through the filter. Don't count dot: If the dot is not visible through the filter. $\boxed{\begin{array}{c} D(1,1) D(2,1) D(3,1) D(4,1) D(5,1) \\ \hline D(1,2) D(2,2) D(3,2) D(4,2) D(5,2) \\ \hline D(1,3) D(2,3) D(3,3) D(4,3) D(5,3) \end{array}} \qquad $					
		(Circular shape)	makes dot appear to be "on" even when all "Black" data is sent to the screen.					
	External inspection	Bubble, Scratch, Foreign particle	Visible operating (all pixels "Black" or "White") and non-operating.					
		Appearance inspection	Does not satisfy the value at the spec.					
	Definition of size	Definition of cir definition of cir definitio definition of cir definition of cir definition of cir de	minor axis					



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Classification Inspection item			Judgement star	ndard		
Defect Dot (In LCD defect	Bright dot defect	Acceptable number	:0			
glass)	Black dot defect	Acceptable number : 0				
	Total dot defects	Acceptable number	:0			
Others	White dot, Black dot				1	
	(Circular shape)	Size (mm)		cceptable number		
		$\begin{array}{c c} d \leq 0, \\ \hline 0.1 < d \leq 0. \end{array}$		Neglected 2		
		$\begin{array}{c ccccccccccccccccccccccccccccccccccc$.10	0		
			÷			
External inspection	Foreign particle Scratch	Width (mm)	Length (mm)	Acceptable number	1	
(Defect on	(Linear shape)	$W \leq 0.02$	_	Neglected		
Polarizer or		$W \leq 0.03$	$L \leq 2.0$	2		
between Polarizer and LCD glass)		$W \leq 0.05$	$L \leq 0.5$	1		
and LOD glass)		$0.05 < \mathrm{W}$		0		
	Foreign particle					
	Bubble Scratch (Circular shape)	Size (mm)		ceptable number		
		$d \leq 0$).1	Neglected		
		$0.1 < d \leq 0$).2	2		
		0.2 < d 0				
		Inspection time: 1 seco Consultation shall be h				

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Inspection								
item	Judgement standard							
Glass crack	(t = Glass Thickness)							
	Item	Size (mm)	ſ	1	1	Judgement		
			a	b	с			
	Back side of the terminal	w t a	\leq t	≦w	≤ 2.0	Neglected		
	Terminal		a	b	с			
			≦t	≦w	≦2.0	Neglected		
		Sealant area t	a	b	с			
	Display side glass	\leq t	<sealant area<br="">inner boundary</sealant>	≦3.0	Neglected			
	Double glass	- Seelant area	а	b	С			
			-	-	≤ 0.2			
			≦t	<sealant area<br="">inner boundary</sealant>	≦3.0	Neglected		
			>t	<sealant area<br="">outer boundary</sealant>	≦2.0			
	Corner V V V A	Sealant area	а	b	с			
			\leq t	≦w	≦2.0	Neglected		
		a:Thickness	a	b	С			
	Projection		≦t	≦0.3	-	Neglected		



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