

# Specification

**TX18D200VM0EAA**

7" - 1920 x 1080 – LVDS

Spec Revision:  
Revision Date: 22.07.2024

Note: This specification is subject to change without prior notice



FOR MESSRS : \_\_\_\_\_

DATE : Jul. 22<sup>nd</sup>, 2024

## TECHNICAL DATA

# TX18D200VM0EAA

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ACCEPTED BY: \_\_\_\_\_

PROPOSED BY: Oblack Tsai

2. RECORD OF REVISION

DATE	SHEET No.	SUMMARY

### 3. GENERAL DATA

#### 3.1 DISPLAY FEATURES

This module is a 7" FHD of 16:9 format LTPS TFT. The pixel format is vertical stripe and sub pixels are arranged as R (red), G (green), B (blue) sequentially. This display is RoHS compliant, COG (chip on glass) technology and LED backlight are applied on this display.

Part Name	TX18D200VM0EAA
Module Dimensions	169.0(W) mm x 104.0(H) mm x 10.0 (D) mm typ.
LCD Active Area	155.52(W) mm x 87.48(H) mm
Pixel Pitch	0.081(W) mm x 0.081 (H) mm
Resolution	1920 x 3(RGB)(W) x 1080(H) dots
Color Pixel Arrangement	R, G, B Vertical Stripe
LCD Type	Transmissive Color TFT; Normally Black
Display Type	Active Matrix
Number of Colors	16.7M Colors
Backlight	3 LEDs Series x 10 Parallel (30 LEDs in Total)
Weight	180 typ. (g)
Interface	LVDS; 20 pins
Power Supply Voltage	3.3V for LCD; 12V for Backlight
Power Consumption	0.891W for LCD; 7.14W for Backlight
Viewing Direction	Super Wide Version (In-Plane Switching)

## 4. ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Min.	Max.	Unit	Remarks
Supply Voltage	V <sub>DD</sub>	-0.3	4.0	V	-
Input Voltage of Logic	V <sub>I</sub>	-0.3	V <sub>DD</sub> +0.3	V	Note 1
Operating Temperature	T <sub>op</sub>	-40	85	°C	Note 2
Storage Temperature	T <sub>st</sub>	-40	90	°C	Note 2
Backlight Input Voltage	V <sub>LED</sub>	-	15	V	-

Note 1: The rating is defined for the signal voltages of the interface such as CLK and pixel data pairs.

Note 2: The maximum rating is defined as above based on the chamber temperature, which might be different from ambient temperature after assembling the panel into the application. Moreover, some temperature-related phenomenon as below needed to be noticed:

- Background color, contrast and response time would be different in temperatures other than 25 °C.
- Operating under high temperature will shorten LED lifetime.

## 5. ELECTRICAL CHARACTERISTICS

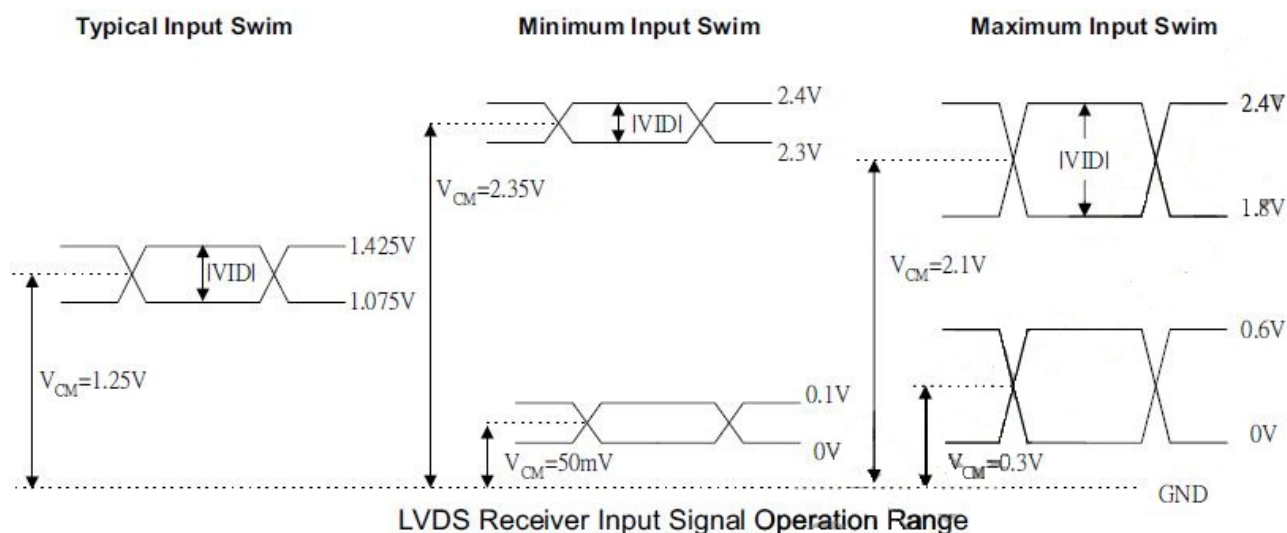
### 5.1 LCD CHARACTERISTICS

$T_a = 25\text{ }^{\circ}\text{C}$ ,  $V_{SS} = 0\text{V}$

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Remarks
Power Supply Voltage	$V_{DD}$	-	3.0	3.3	3.6	V	-
Differential Input Voltage for LVDS Receiver Threshold	$V_I$	"H" level	-	-	+100	mV	Note 1
		"L" level	-100	-	-		
Power Supply Current	$I_{DD}$	$V_{DD}=3.3\text{V}$	-	270	-	mA	Note 2
Vsync Frequency	$f_v$	-	-	60	-	Hz	Note 3
Hsync Frequency	$f_H$	-	66	67.5	68.1	KHz	
CLK Frequency	$f_{CLK}$	-	135.3	148.5	150	MHz	

Note 1:  $V_{CM}=+1.2\text{V}$

$V_{CM}$  is common mode voltage of LVDS transmitter/receiver.



Note 2: An all white check pattern is used when measuring  $I_{DD}$ .  $f_v$  is set to 60 Hz.

Note 3: For LVDS transmitter input.

## 5.2 BACKLIGHT CHARACTERISTICS

$T_a = 25\text{ }^{\circ}\text{C}$

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Remarks
LED Input Voltage	$V_{LED}$	-	11.7	12	12.3	V	Note1
LED Forward Current (Dim Control)	$I_{LED}$	0V; 0% duty	-	595	-	mA	Note 2
		3.3VDC; 100% duty	-	35	-		
LED lifetime	-	$I_{LED} = 595\text{ mA}$	-	100K	-	hrs	Note 3

Note 1: As Fig. 5.1 shown, LED current is constant, 595 mA, controlled by the LED driver when applying 12V.

Note 2: Dimming function can be obtained by applying DC voltage or PWM signal from the display interface CN1. The recommended PWM signal is 1K ~ 10K Hz with 3.3V amplitude.

Note 3: The estimated lifetime is specified as the time to reduce 50% brightness by applying 595 mA at  $25\text{ }^{\circ}\text{C}$ .

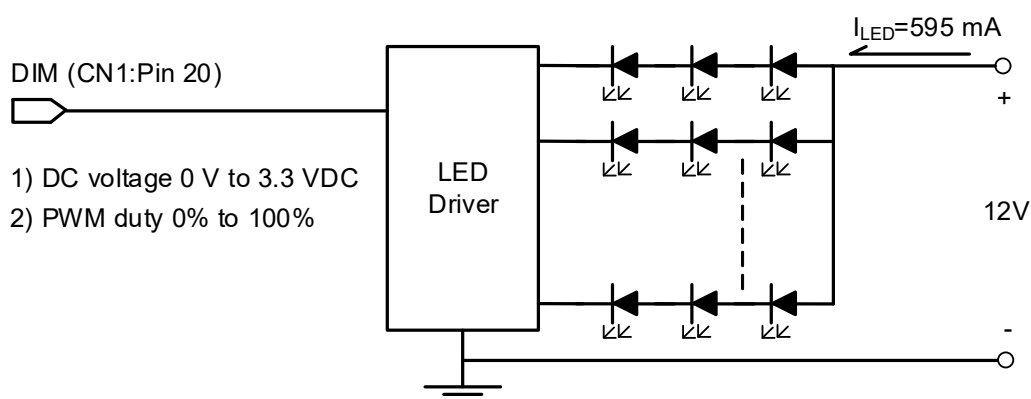


Fig. 5.1

## 6. OPTICAL CHARACTERISTICS

The optical characteristics are measured based on the conditions as below:

- Supplying the signals and voltages defined in the section of electrical characteristics.
- The backlight unit needs to be turned on for 30 minutes.
- The ambient temperature is 25 °C.
- In the dark room around 500~1000 lx, the equipment has been set for the measurements as shown in Fig 6.1.

$$T_a = 25\text{ }^{\circ}\text{C}, f_v = 60\text{ Hz}, V_{DD} = 3.3\text{ V}$$

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Remarks
Brightness of White		-	$\phi = 0^{\circ}, \theta = 0^{\circ}$ , I <sub>LED</sub> = 595 mA	960	1200	-	cd/m <sup>2</sup>	Note 1
Brightness Uniformity		-		70	-	-	%	Note 2,3
Contrast Ratio		CR		500	1300	-	-	Note 4
Response Time (Rising + Falling)		T <sub>r</sub> + T <sub>f</sub>	$\phi = 0^{\circ}, \theta = 0^{\circ}$	-	23	-	ms	Note 5
Viewing Angle		$\theta$ x	$\phi = 0^{\circ}, CR \geq 10$	-	85	-	Degree	Note 6
		$\theta$ x'	$\phi = 180^{\circ}, CR \geq 10$	-	85	-		
		$\theta$ y	$\phi = 90^{\circ}, CR \geq 10$	-	85	-		
		$\theta$ y'	$\phi = 270^{\circ}, CR \geq 10$	-	85	-		
Color Chromaticity	Red	X	$\phi = 0^{\circ}, \theta = 0^{\circ}$	0.63	0.68	0.73	-	Note 7
		Y		0.25	0.30	0.35		
	Green	X		0.24	0.29	0.34		
		Y		0.61	0.66	0.71		
	Blue	X		0.10	0.15	0.20		
		Y		0	0.05	0.10		
	White	X		0.25	0.30	0.35		
		Y		0.27	0.32	0.37		
NTSC Ratio	-	-	$\phi = 0^{\circ}, \theta = 0^{\circ}$	-	85%	-	%	-

Note 1: The brightness is measured from the panel center point, P5 in Fig. 6.2, for the typical value.

Note 2: The brightness uniformity is calculated by the equation as below:

$$\text{Brightness uniformity} = \frac{\text{Min. Brightness}}{\text{Max. Brightness}} \times 100\%$$

, which is based on the brightness values of the 9 points measured by BM-5 as shown in Fig. 6.2.

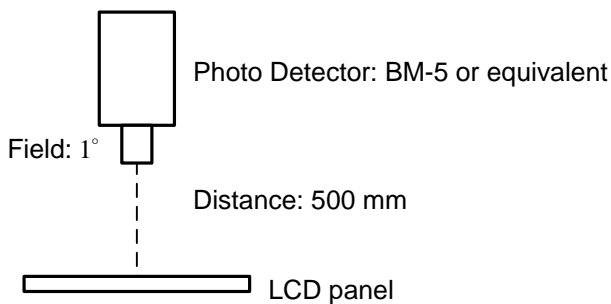


Fig. 6.1

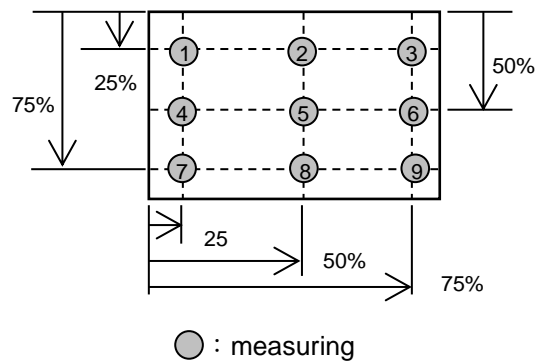


Fig. 6.2



Note 3: Continuously operating the test pattern (see below chess pattern Fig.6.3) on display for 2 hours at 25°C then switch to completely white pattern, the previous test pattern shall disappear within 2 seconds.

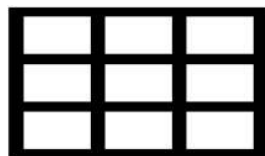


Fig.6.3

Note 4: The Contrast Ratio is measured from the center point of the panel, P5, and defined as the following equation:

$$CR = \frac{\text{Brightness of White}}{\text{Brightness of Black}}$$

Note 5: The definition of response time is shown in Fig. 6.4. The rising time is the period from 10% brightness to 90% brightness when the data is from black to white. Oppositely, Falling time is the period from 90% brightness falling to 10% brightness.

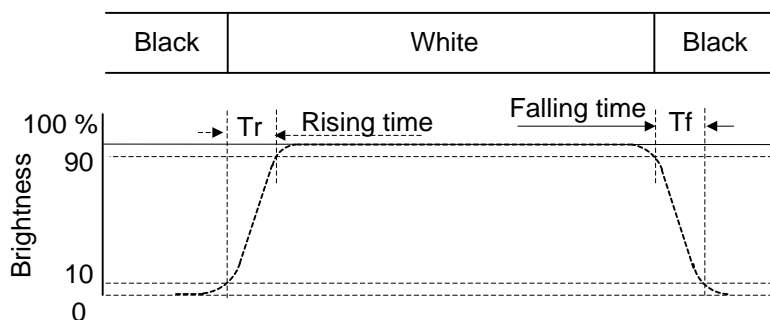


Fig.6.4

Note 6: The definition of viewing angle is shown in Fig. 6.5. Angle  $\phi$  is used to represent viewing directions, for instance,  $\phi = 270^\circ$  means 6 o'clock, and  $\phi = 0^\circ$  means 3 o'clock. Moreover, angle  $\theta$  is used to represent viewing angles from axis Z toward plane XY.

The display is super wide viewing angle version, so that the best optical performance can be obtained from every viewing direction.

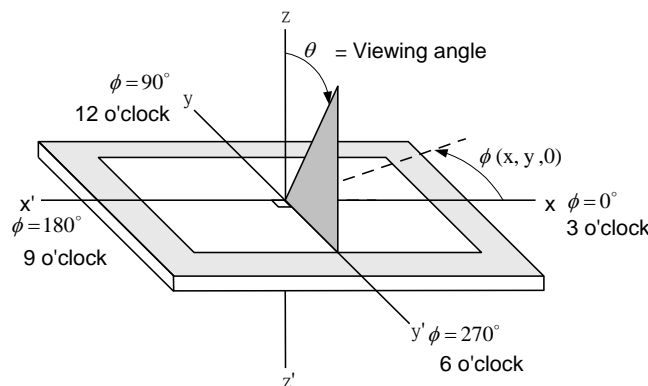
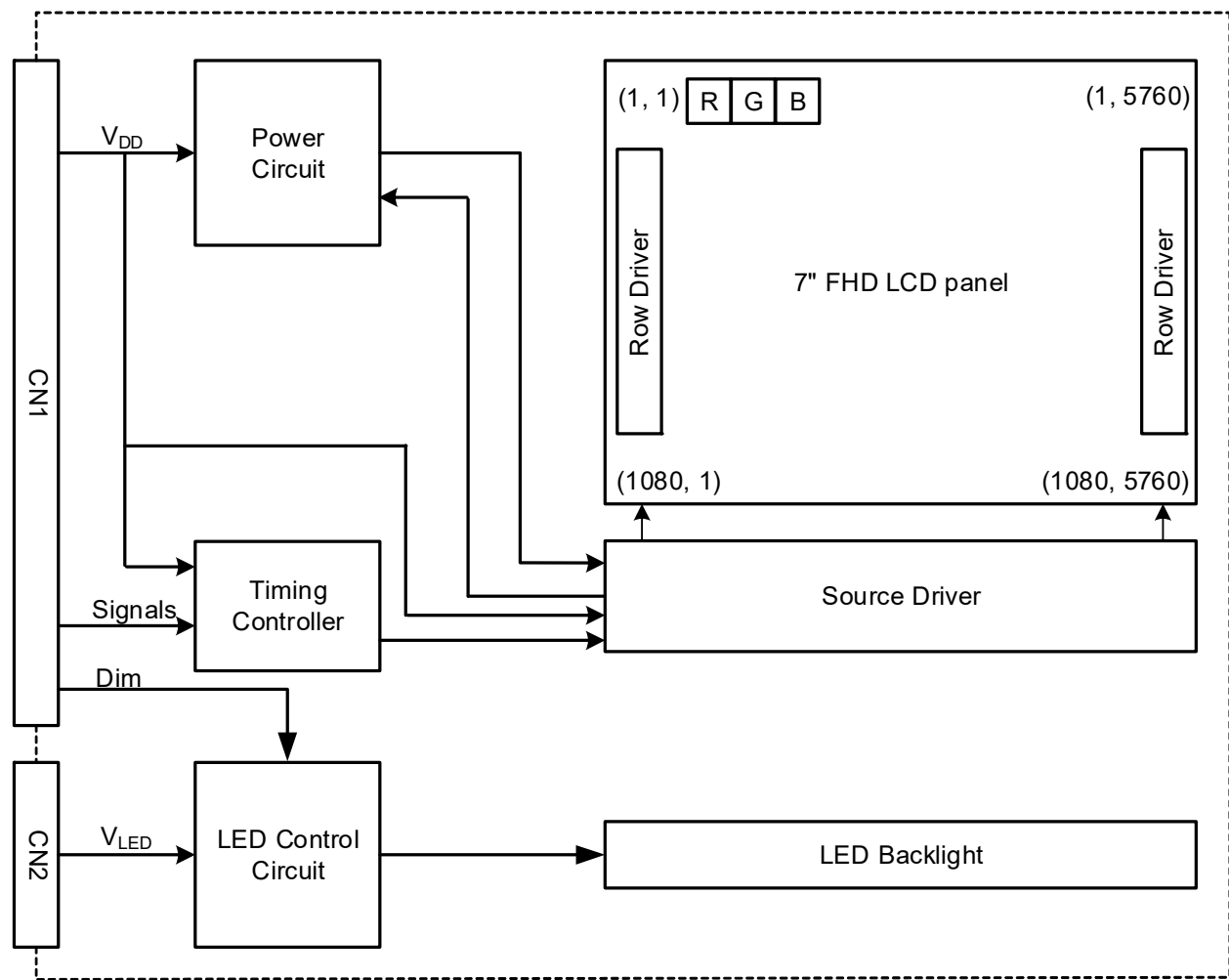


Fig 6.5

Note 7: The color chromaticity is measured from the center point of the panel, P5, as shown in Fig.6.2.

7. BLOCK DIAGRAM



Note 1: Signals are CLK and pixel data pairs.

## 8. LCD INTERFACE

### 8.1 INTERFACE PIN CONNECTIONS

The display interface connector (CN1) is FI-SEB20P-HF13E-E1500 made by JAE and pin assignment is as below:

Pin No.	Symbol	Signal	Pin No.	Symbol	Signal
1	V <sub>DD</sub>	Power Supply for Logic	11	IN2-	B2~B5, DE
2	V <sub>DD</sub>		12	IN2+	
3	V <sub>SS</sub>	GND	13	V <sub>SS</sub>	GND
4	V <sub>SS</sub>		14	CLK IN-	Pixel Clock
5	IN0-	R0~R5, G0	15	CLK IN+	
6	IN0+		16	V <sub>SS</sub>	GND
7	V <sub>SS</sub>	GND	17	IN3-	R6~R7, G6~G7, B6~B7
8	IN1-	G1~G5, B0~B1	18	IN3+	
9	IN1+		19	NC	No Connection
10	V <sub>SS</sub>	GND	20	DIM	Note 2

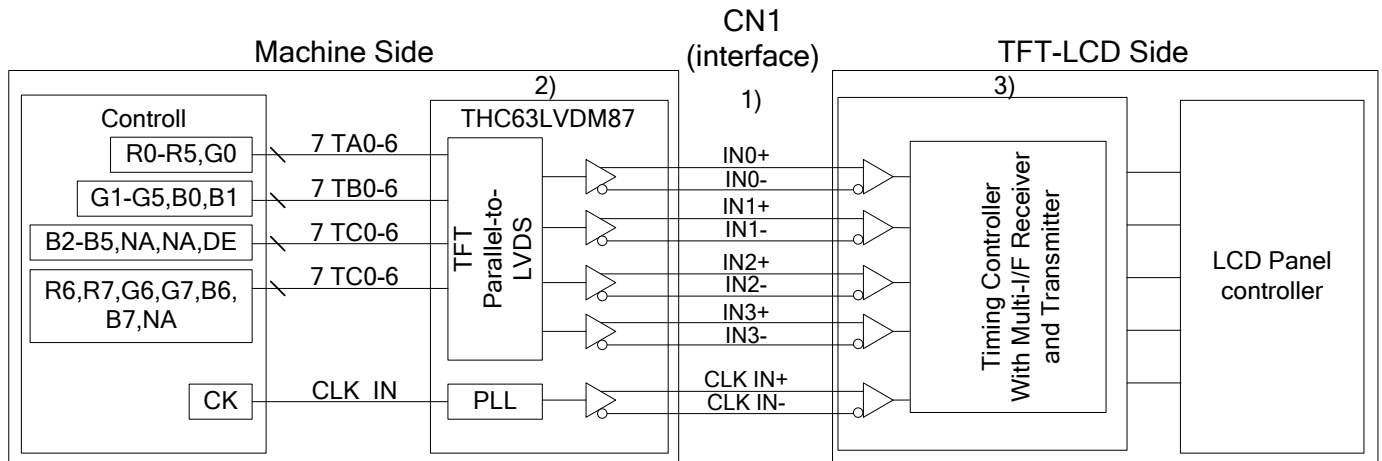
Note 1: IN n- and IN n+ (n=0, 1, 2, 3), CLK IN- and CLK IN+ should be wired by twist-pairs or side-by-side FPC patterns, respectively.

Note 2: Normal brightness: 0V or 0% PWM duty; Brightness control: 0V to 3.3V DC or 0% to 100% PWM duty.

The backlight connector (CN2) is SM02(8.0)B-BHS-1-TB(LF)(SN) made by JST, and pin assignment is as below:

Pin No.	Signal	Signal
1	V <sub>LED</sub>	12VDC
2	GND	Ground

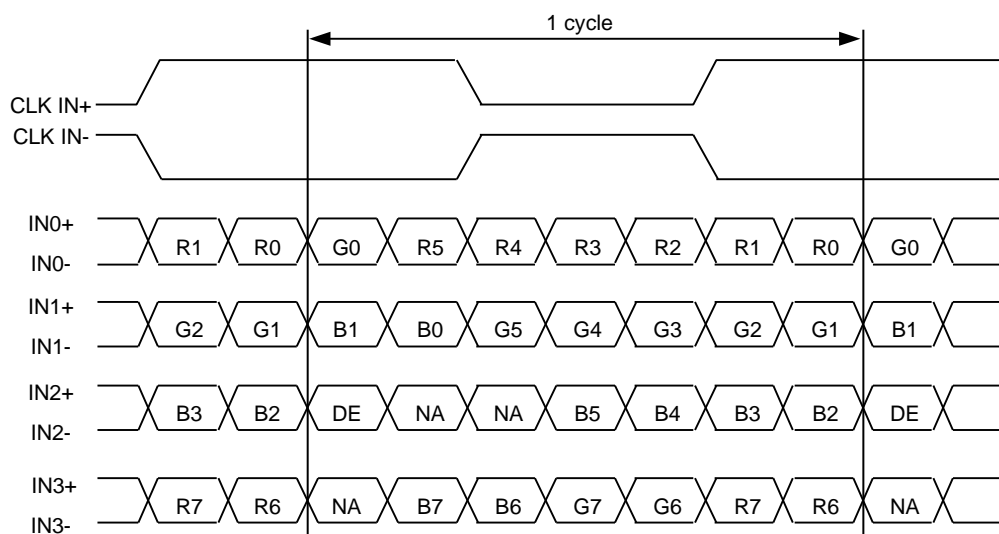
## 8.2 LVDS INTERFACE



Note 1: LVDS cable impedance should be 100 ohms per signal line when each 2-lines (+, -) is used in differential mode.

Note 2: The recommended transmitter, THC63LVDM87, is made by Thine or equivalent, which is not contained in the module.

## 8.3 LVDS DATA FORMAT (VESA)



DE: Display Enable

NA: Not Available

## 8.4 TIMING CHART

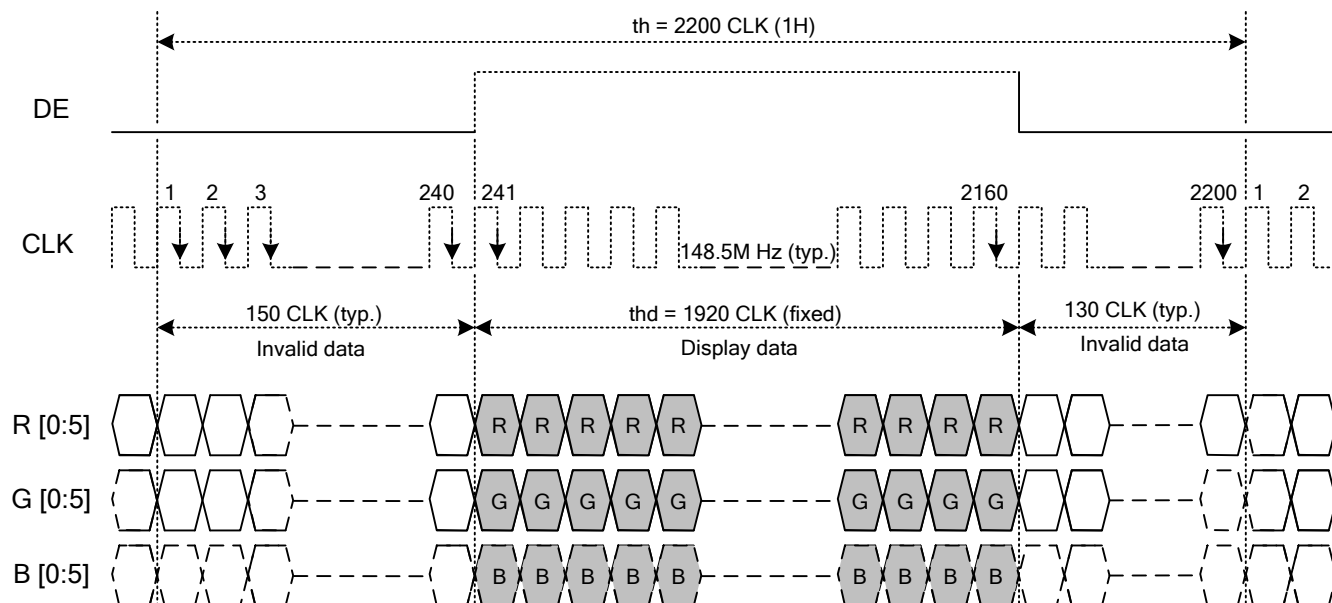


Fig. 8.1 Horizontal Timing

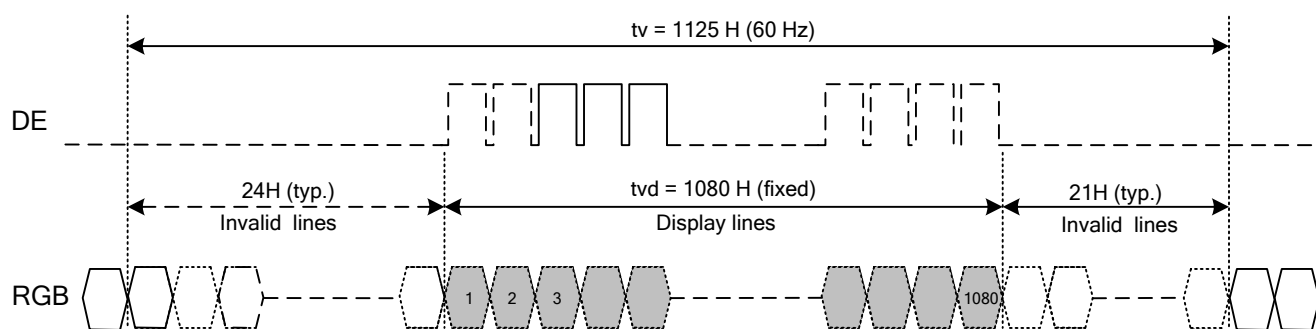


Fig. 8.2 Vertical Timing

## 8.5 TIMING TABLE

The column of timing sets including minimum, typical, and maximum as below are based on the best optical performance, frame frequency (Vsync) = 60Hz to define.

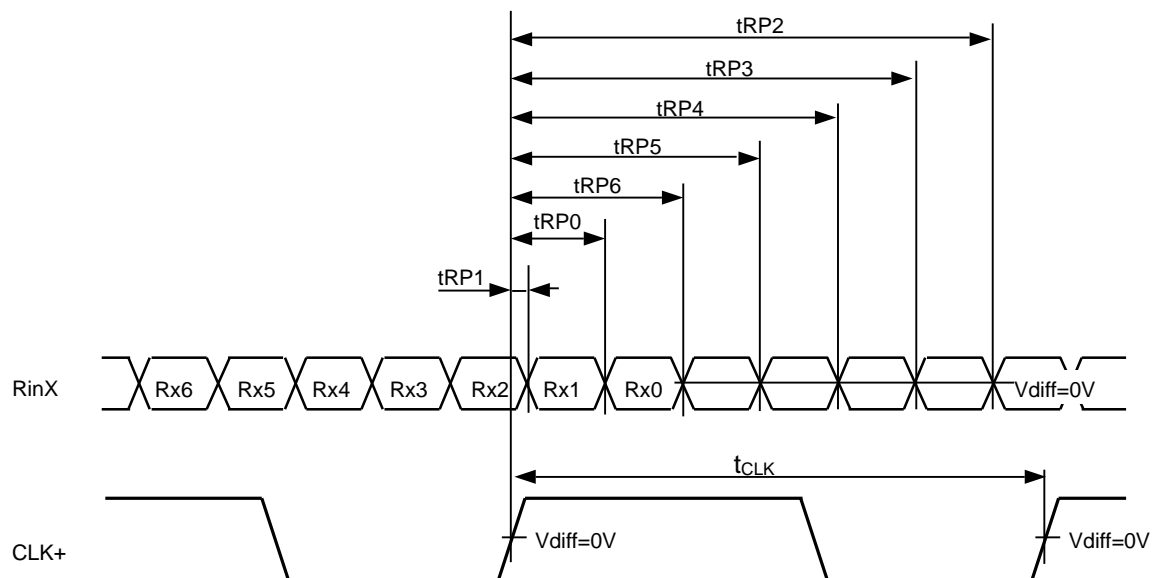
### A. DE MODE

Item		Symbol	Min.	Typ.	Max.	Unit
Horizontal	CLK Frequency	fclk	135.3	148.5	150	MHz
	Display Data	thd	1920			CLK
	Cycle Time	th	2050	2200	2248	
Vertical	Display Line	tvd	1080			H
	Cycle Time	tv	1100	1125	1150	

### B. CLOCK AND DATA INPUT TIMING

Item		Symbol	Min.	Typ.	Max.	Unit
CLK	Duty	Tcwh	47.5	50	52.5	%
	Cycle Time	Tcph	-	6.74	-	ns
Data	Setup Time	Tdsu	1	-	-	
	Hold Time	Tdhd	1	-	-	
DE	Setup Time	Tesu	1	-	-	
	Hold Time	Tehd	1	-	-	

## 8.6 LVDS RECEIVER TIMING



$$R_{inX} = (R_{inX+}) - (R_{inX-}) \quad (X=0, 1, 2, 3)$$

	Item	Symbol	Min.	Typ.	Max.	Unit
CLK	Cycle frequency	$1/t_{CLK}$	135.3	148.5	150	MHz
RinX (X=0,1,2,3)	0 data position	$t_{RP0}$	$1/7 * t_{CLK} - 0.17$	$1/7 * t_{CLK}$	$1/7 * t_{CLK} + 0.17$	ns
	1st data position	$t_{RP1}$	-0.17	0	+0.17	
	2nd data position	$t_{RP2}$	$6/7 * t_{CLK} - 0.17$	$6/7 * t_{CLK}$	$6/7 * t_{CLK} + 0.17$	
	3rd data position	$t_{RP3}$	$5/7 * t_{CLK} - 0.17$	$5/7 * t_{CLK}$	$5/7 * t_{CLK} + 0.17$	
	4th data position	$t_{RP4}$	$4/7 * t_{CLK} - 0.17$	$4/7 * t_{CLK}$	$4/7 * t_{CLK} + 0.17$	
	5th data position	$t_{RP5}$	$3/7 * t_{CLK} - 0.17$	$3/7 * t_{CLK}$	$3/7 * t_{CLK} + 0.17$	
	6th data position	$t_{RP6}$	$2/7 * t_{CLK} - 0.17$	$2/7 * t_{CLK}$	$2/7 * t_{CLK} + 0.17$	

## 8.7 POWER SEQUENCE

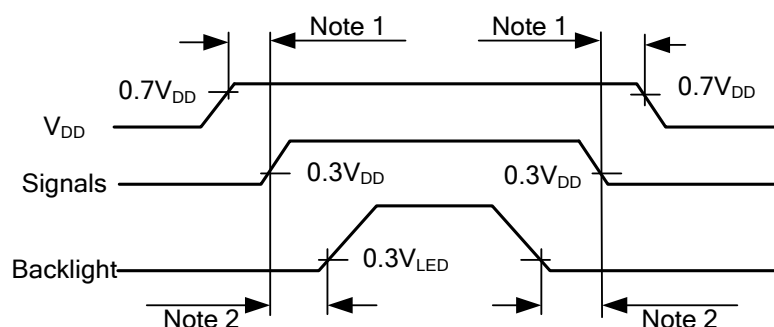


Fig. 8.3 Power Sequence Timing

Note 1: In order to avoid any damages,  $V_{DD}$  has to be applied before all other signals. The opposite is true for power off where  $V_{DD}$  has to be remained on until all other signals have been switch off. The recommended time period is 1 second.

Note 2: In order to avoid showing uncompleted patterns in transient state. It is recommended that switching the backlight on is delayed for 1 second after the signals have been applied. The opposite is true for power off where the backlight has to be switched off 1 second before the signals are removed.



## 8.8 DATA INPUT for DISPLAY COLOR

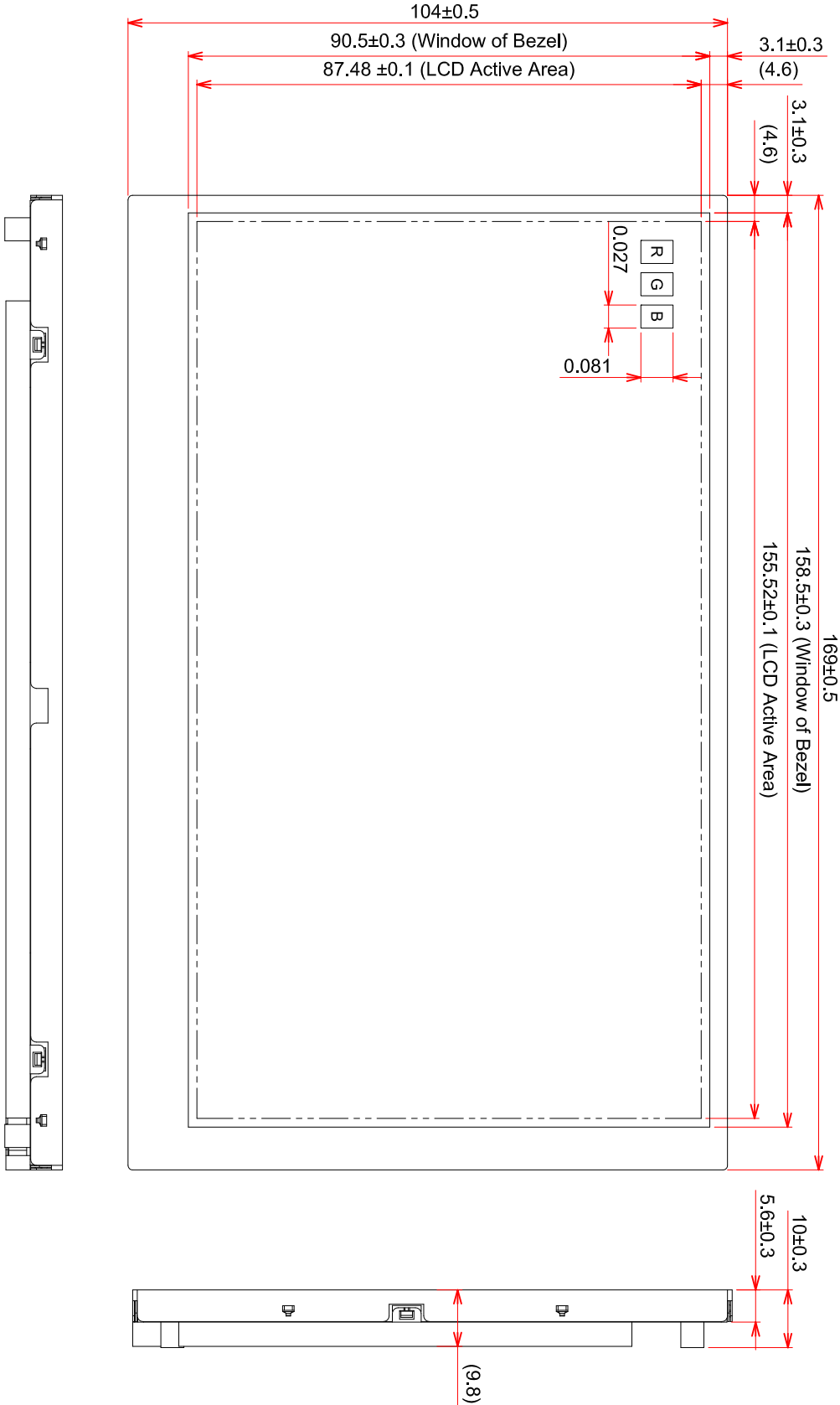
Input color		Red Data								Green Data								Blue Data							
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
		MSB							LSB	MSB							LSB	MSB							LSB
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
Red	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Red(1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Red(2)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	Red(253)	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Red(254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Green	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	
	Green(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	Green(253)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	
	Green(254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	
Blue	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	Blue(253)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	
	Blue(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	

Note 1: Definition of gray scale : Color(n) Number in parenthesis indicates gray scale level. Larger number corresponds to brighter level.

Note 2: Data Signal : 1 : High, 0 : Low

9. OUTLINE DIMENSIONS

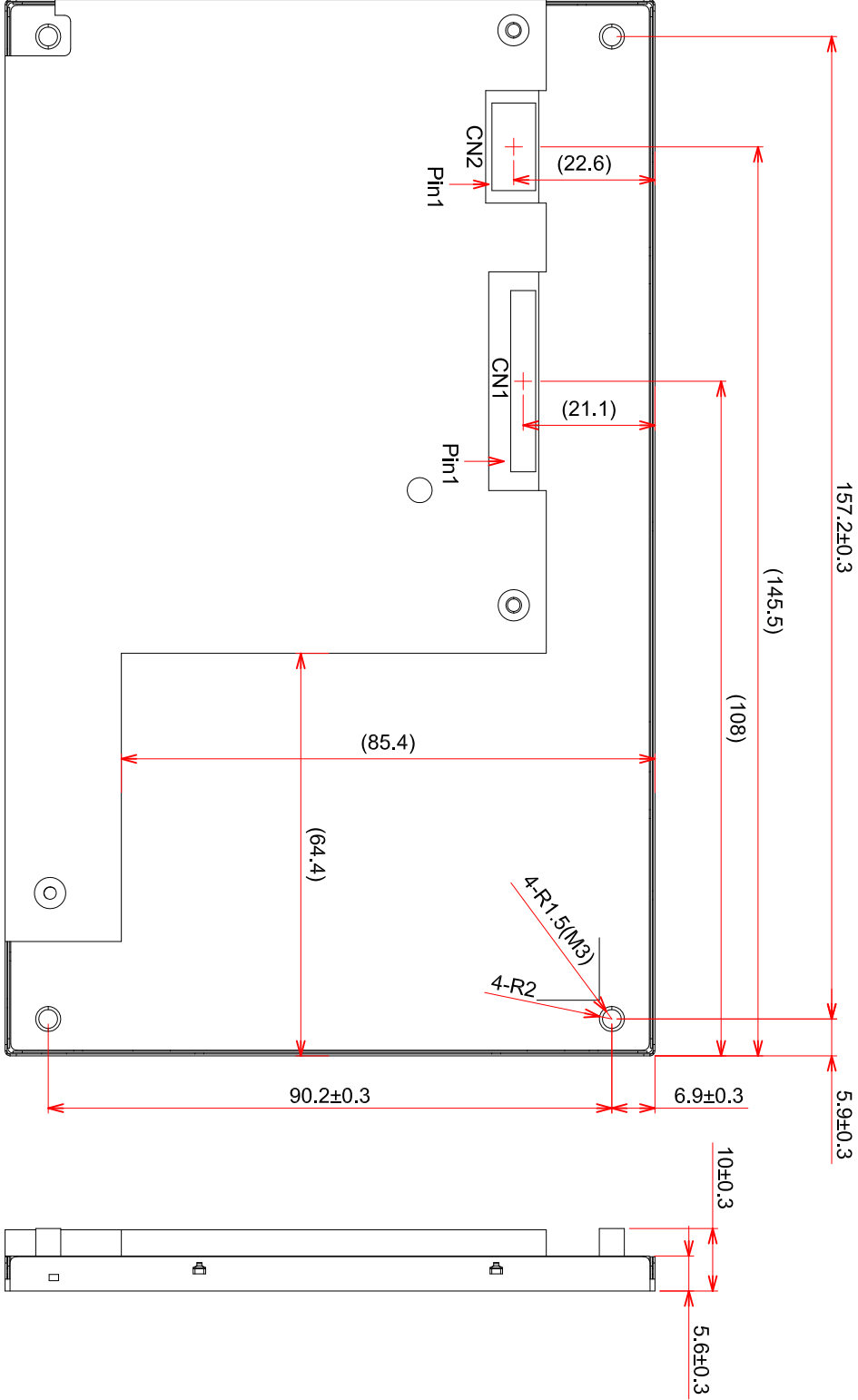
9.1 FRONT VIEW



\*\* Undefined tolerance is  $\pm 0.5\text{mm}$

Scale : NTS  
Unit : mm

9.2 REAR VIEW



\*\* Undefined tolerance is  $\pm 0.5\text{mm}$

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