



# **SPECIFICATION**



## TX18D204VM0BAA

7" TFT - Full HD - LVDS

Version: TX18D204VM0BAA-1

Date: 06.02.2015

Note: This specification is subject to change without prior notice



Kaohsiung Opto-Electronics Inc.

FOR MESSRS:	DATE: Feb. 6 <sup>th</sup> ,2	2015

## **CUSTOMER'S ACCEPTANCE SPECIFICATIONS**

## TX18D204VM0BAA

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2. REC	ORD OF REVISION	ON
DATE	SHEET No.	SUMMARY

## 3. GENERAL DATA

#### 3.1 DISPLAY FEATURES

This module is a 7" FHD of 16:9 format amorphous silicon TFT. The pixel format is vertical stripe and sub pixels are arranged as R (red), G (green), B (blue) sequentially. This display is RoHS compliant, COG (chip on glass) technology and LED backlight are applied on this display.

Part Name	TX18D204VM0BAA
Module Dimensions	169.0(W) mm x 103.0(H) mm x 7.2 (D) mm
LCD Active Area	155.52(W) mm x 87.48(H) mm
Pixel Pitch	0.081(W) mm x 0.081 (H) mm
Resolution	1920 x 3(RGB)(W) x 1080(H) Dots
Color Pixel Arrangement	R, G, B Vertical Stripe
LCD Type	Transmissive Color TFT; Normally Black
Display Type	Active Matrix
Number of Colors	16.7M Colors
Backlight	Light Emitting Diode (LED)
Weight	110 g
Interface	LVDS; 20 pins
Power Supply Voltage	3.3V for LCD; 12V for Backlight
Power Consumption	0.53W for LCD; 2.9W for Backlight
Viewing Direction	Super Wide Version (In-Plane Switching)

## 4. ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Min.	Max.	Unit	Remarks
Supply Voltage	$V_{DD}$	-0.3	4.0	<b>V</b>	-
Input Voltage of Logic	$V_{I}$	-0.3	V <sub>DD</sub> +0.3	<b>V</b>	Note 1
Operating Temperature	Тор	-20	70	°C	Note 2
Storage Temperature	Tst	-30	80	°C	Note 2
Backlight Input Voltage	$V_{LED}$	-	15	V	-

- Note 1: The rating is defined for the signal voltages of the interface such as CLK and pixel data pairs.
- Note 2: The maximum rating is defined as above based on the chamber temperature, which might be different from ambient temperature after assembling the panel into the application. Moreover, some temperature-related phenomenon as below needed to be noticed:
  - Background color, contrast and response time would be different in temperatures other than  $25\,^{\circ}\mathrm{C}$ .
  - Operating under high temperature will shorten LED lifetime.

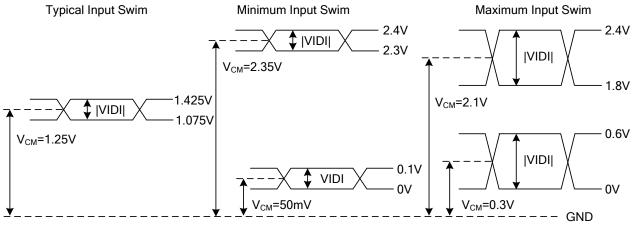
#### 5. ELECTRICAL CHARACTERISTICS

#### 5.1 LCD CHARACTERISTICS

 $T_a = 25$  °C, Vss = 0V

Item	Symbol	Condition	Min.	Тур.	Max.	Unit	Remarks
Power Supply Voltage	$V_{DD}$	-	3.0	3.3	3.6	V	-
Differential Input	Vı	"H" level	-	-	+100	>/	Note 4
Voltage for LVDS Receiver Threshold		"L" level	-100	-	-	mV	Note 1
Power Supply Current	I <sub>DD</sub>	$V_{DD}=3.3V$	1	160	210	mA	Note 2
Frame Frequency	$f_{Frame}$	-	-	60	67	Hz	Note 2
CLK Frequency	$f_{\mathit{CLK}}$	-	135.3	148.5	160	MHz	Note 3

Note 1: VCM 1.2V is common mode voltage of LVDS transmitter and receiver.



LVDS Receiver Input Signal Operation Range

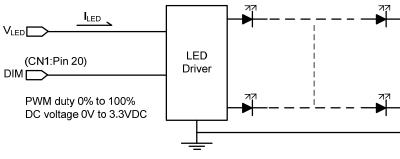
- Note 2: An all white check pattern is used when measuring I<sub>DD</sub>. *f*<sub>Frame</sub> is set to 60 Hz.
- Note 3: For LVDS transmitter input.
- Note 4: 0.89A fuse is applied in the module for I<sub>DD</sub>. For display activation and protection purpose, power supply is recommended larger than 2.3A to start the display and break fuse once any short circuit occurred.

#### 5.2 BACKLIGHT CHARACTERISTICS

 $T_a = 25 \, {}^{\circ}C$ 

Item	Symbol	Condition	Min.	Тур.	Max.	Unit	Remarks
LED Input Voltage	$V_{LED}$	-	11.7	12	12.3	V	Note1
LED Forward Current		0V; 0% duty	-	242	-	m A Nata	Nata
(Dim Control)	I <sub>LED</sub>	3.3VDC; 100% duty	-	10	-	mA	Note 2
LED lifetime	-	I <sub>LED</sub> = 242 mA	-	40K	-	hrs	Note 3

- Note 1: As Fig. 5.1 shown, LED current is constant, 242 mA, controlled by the LED driver when applying 12V.
- Note 2: Dimming function can be obtained by applying DC voltage or PWM signal from the display interface CN1. The recommended PWM signal is 1K ~ 10K Hz with 3.3V amplitude.
- Note 3: The estimated lifetime is specified as the time to reduce 50% brightness by applying 242 mA at  $25^{\circ}$ C.



#### 6. OPTICAL CHARACTERISTICS

The optical characteristics are measured based on the conditions as below:

- Supplying the signals and voltages defined in the section of electrical characteristics.
- The backlight unit needs to be turned on for 30 minutes.
- The ambient temperature is 25  $^{\circ}\mathrm{C}$  .
- In the dark room less than 100 lx, the equipment has been set for the measurements as shown in Fig 6.1.

T = 25	°C f_	$_{e} = 60  \text{Hz}, V$	nn – 3 3V
$I_a - 20$	$\cup$ , $\int_{Fram}$	, – OUTIZ, V	עט – <b>ט.ט v</b>

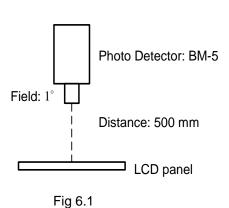
Item		Symbol	Condition	Min.	Тур.	Max.	Unit	Remarks
Brightness o	f White	-	4 00 0 00	480	600	-	cd/m <sup>2</sup>	Note 1
Brightness Uniformity		-	$\phi = 0^{\circ}, \theta = 0^{\circ},$	70	-	-	%	Note 2,3
Contrast F	Ratio	CR	I <sub>LED</sub> = 242 mA	500	800	-	-	Note 4
Response	Time	$T_r + T_f$	$\phi = 0^{\circ}, \theta = 0^{\circ}$	-	23	-	ms	Note 5
		$\theta$ x	$\phi = 0^{\circ}, CR \ge 10$	-	85	-		
\/iouring A	nalo	$\theta$ x'	$\phi = 180^{\circ}, CR \ge 10$	-	85	-	Degree	Note 6
Viewing A	arigie	$\theta$ y	$\phi = 90^{\circ}$ , CR $\geq 10$	-	85	-		
		$\theta$ y'	$\phi = 270^{\circ}$ , CR $\geq 10$	-	85	-		
	Dod	X		0.59	0.64	0.69	-	
	Red	Υ		0.27	0.32	0.37		
	0	X		0.29	0.34	0.39		
Color	Green	Υ		0.55	0.60	0.65		
Chromaticity	Blue	Х	$\phi = 0^{\circ}, \theta = 0^{\circ}$	0.09	0.14	0.19		Note 7
	blue	Υ		0.00	0.05	0.10		
	White	X		0.25	0.30	0.35		
	vviille	Y		0.26	0.31	0.36		

Note 1: The brightness is measured from the center point of the panel, P5 in Fig. 6.2, for the typical value.

Note 2: The brightness uniformity is calculated by the equation as below:

Brightness uniformity = 
$$\frac{\text{Min. Brightness}}{\text{Max. Brightness}}$$
 X100%

which is based on the brightness values of the 9 points in active area measured by BM-5 as shown in Fig. 6.2.



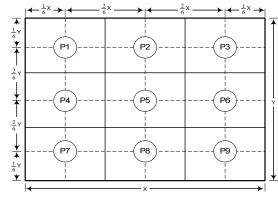


Fig 6.2

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Note 3: Continuously operating the test pattern (see below chess pattern Fig.6.3) on display for 2 hours at 25°C then switch to completely white pattern, the previous test pattern shall disappear within 2 seconds.

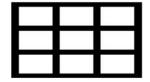


Fig.6.3

Note 4: The Contrast Ratio is measured from the center point of the panel, P5, and defined as the following equation:

$$CR = \frac{Brightness of White}{Brightness of Black}$$

Note 5: The definition of response time is shown in Fig. 6.4. The rising time is the period from 10% brightness to 90% brightness when the data is from black to white. Oppositely, Falling time is the period from 90% brightness falling to 10% brightness.

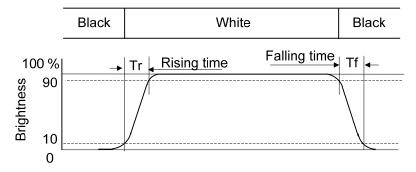


Fig.6.4

Note 6: The definition of viewing angle is shown in Fig. 6.5. Angle  $\phi$  is used to represent viewing directions, for instance,  $\phi = 270^{\circ}$  means 6 o'clock, and  $\phi = 0^{\circ}$  means 3 o'clock. Moreover, angle  $\theta$  is used to represent viewing angles from axis Z toward plane XY.

The display is super wide viewing angle version, so that the best optical performance can be obtained from every viewing direction.

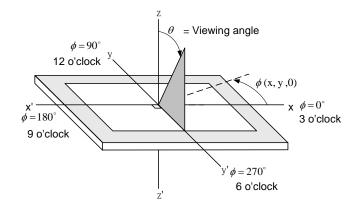
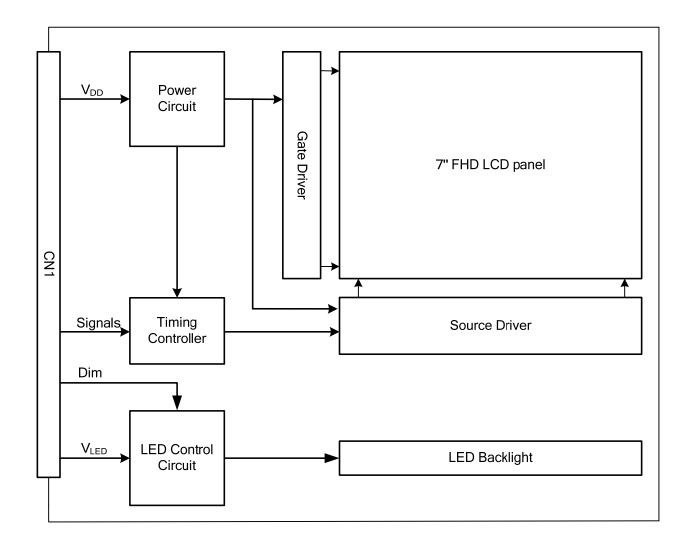


Fig 6.5

Note 7: The color chromaticity is measured from the center point of the panel, P5, as shown in Fig. 6.2.

## 7. BLOCK DIAGRAM

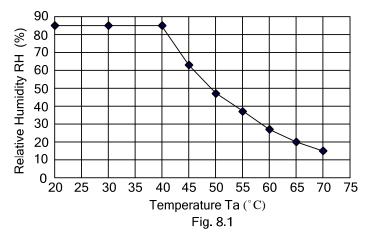


Note 1: Signals are CLK and pixel data pairs.

## 8. RELIABILITY TESTS

Test Item	Condition						
High Temperature	1) Operating 2) 70 °C	240 hrs					
Low Temperature	1) Operating 2) -20 °C	240 hrs					
High Temperature	1) Storage 2) 80 ° C	240 hrs					
Low Temperature	1) Storage 2) -30 °C	240 hrs					
Heat Cycle	1) Operating 2) -20 °C ~70 °C 3) 3hrs~1hr~3hrs	240 hrs					
Thermal Shock	1) Non-Operating 2) -35 °C ↔ 85 °C 3) 0.5 hr ↔ 0.5 hr	240 hrs					
High Temperature & Humidity	1) Operating 2) 40 °C & 85%RH 3) Without condensation	240 hrs (Note 3)					
Vibration	1) Non-Operating 2) 20~200 Hz 3) 2G 4) X, Y, and Z directions	1 hr for each direction					
Mechanical Shock	<ul> <li>1) Non-Operating</li> <li>2) 10 ms</li> <li>3) 50G</li> <li>4) ±X, ± Y and ±Z directions</li> </ul>	Once for each direction					
ESD	<ol> <li>Operating</li> <li>Tip: 200 pF, 250 Ω</li> <li>Air discharge for glass: ± 8KV</li> <li>Contact discharge for metal frame: ± 8KV</li> </ol>	1) Glass: 9 points 2) Metal frame: 8 points (Note4)					

- Note 1: Display functionalities are inspected under the conditions defined in the specification after the reliability tests.
- Note 2: The display is not guaranteed for use in corrosive gas environments.
- Note 3: Under the condition of high temperature & humidity, if the temperature is higher than  $40^{\circ}$ C, the humidity needs to be reduced as Fig. 8.1 shown.



Note 4: All pins of LCD interface (CN1) have been tested by  $\pm 100$ V contact discharge of ESD under non-operating condition.

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## 9. LCD INTERFACE

#### 9.1 INTERFACE PIN CONNECTIONS

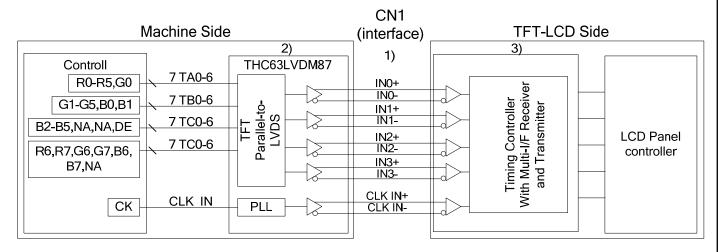
The display interface connector (CN1) is FI-SEB20P-HF13E-E1500 made by JAE and pin assignment is as below:

Pin No.	Symbol	Signal	Pin No.	Symbol	Signal
1	$V_{DD}$	Dower Cupply for Logic	11	IN2-	D2 D5 D5
2	$V_{DD}$	Power Supply for Logic	12	IN2+	B2~B5, DE
3	V <sub>SS</sub>	GND	13	$V_{SS}$	GND
4	V <sub>SS</sub>	GND	14	CLK IN-	Pixel Clock
5	INO-	R0~R5, G0	15	CLK IN+	Pixel Clock
6	IN0+	K0~K3, G0	16	$V_{SS}$	GND
7	V <sub>SS</sub>	GND	17	IN3-	DC D7 C6 C7 D6 D7
8	IN1-	G1~G5, B0~B1	18	IN3+	R6~R7, G6~G7, B6~B7
9	IN1+	G1~G3, B0~B1	19	$V_{LED}$	12 VDC
10	V <sub>SS</sub>	GND	20	DIM	Note 2

Note 1: IN n- and IN n+ (n=0, 1, 2, 3), CLK IN- and CLK IN+ should be wired by twist-pairs or side-by-side FPC patterns, respectively.

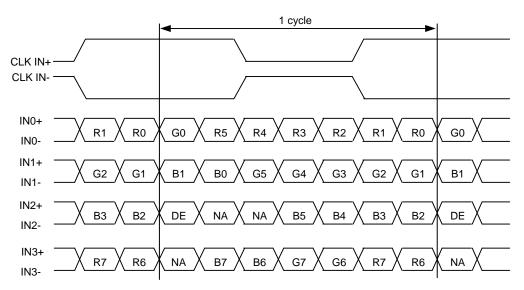
Note 2: Normal brightness: 0V or 0% PWM duty; Brightness control: 0V to 3.3V DC or 0% to 100% PWM duty.

#### 9.2 LVDS INTERFACE



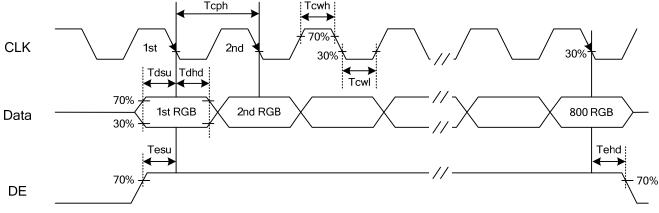
- Note 1: LVDS cable impedance should be 100 ohms per signal line when each 2-lines (+, -) is used in differential mode.
- Note 2: The recommended transmitter, THC63LVDM87, is made by Thine or equivalent, which is not contained in the module.

#### 9.3 LVDS DATA FORMAT



DE: Display Enable NA: Not Available

## 9.4 TIMING CHART th = 2200 CLK (1H)DE 240 241 2160 2200 1 CLK 148.5M Hz (typ. thd = 1920 CLK (fixed) 150 CLK (typ.) Invalid data Display data Invalid data R [0:5] G [0:5] B [0:5] Fig. 9.1 Horizontal Timing tv = 1125 H (60 Hz)DE tvd = 1080 H (fixed)24H (typ.) 21H (typ.) Invalid lines Display lines Invalid lines **RGB** Fig. 9.2 Vertical Timing Tcph Tcwh 70% CLK 2nd 30%



#### 9.5 TIMING TABLE

The column of timing sets including minimum, typical, and maximum as below are based on the best optical performance, frame frequency ( $f_{Frame}$ ) = 60Hz to define.

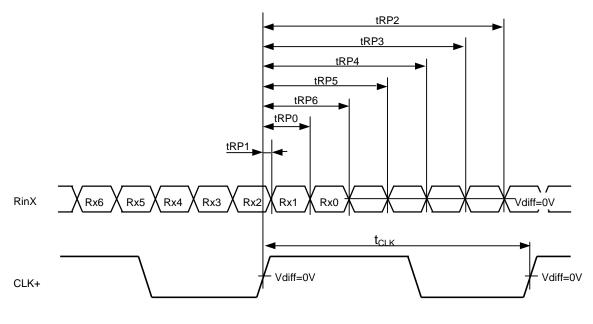
#### A. DE MODE

Item		Symbol	Min.	Тур.	Max.	Unit		
	CLK Frequency		135.3	148.5	160	MHz		
Horizontal Display Data		thd		1920				
	Cycle Time	th	2050	2200	2320	CLK		
Display Line		tvd		1080				
Vertical	Cycle Time	tv	1100	1125	1150	Н		

#### B. CLOCK AND DATA INPUT TIMING

	Item	Symbol	Min.	Тур.	Max.	Unit
CLIK	Duty	Tcwh	47.5	50	52.5	%
CLK	Cycle Time	Tcph	-	6.74	-	
Dete	Setup Time	Tdsu	1	-	-	
Data	Hold Time	Tdhd	1	-	-	ns
DE	Setup Time	Tesu	1	-	-	
DE	Hold Time	Tehd	1	-	-	

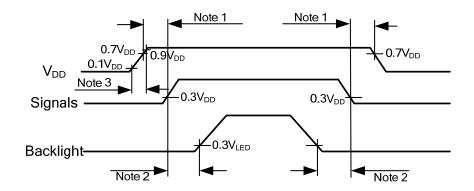
#### 9.6 LVDS RECEIVER TIMING



RinX = (RinX +) - (RinX -)	(X=0, 1, 2, 3)
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	Item		Min.	Тур.	Max.	Unit
CLK	Cycle frequency	1/tcLK	135.3	148.5	160	MHz
	0 data position	tRP0	1/7* t <sub>CLK</sub> -0.49	1/7* t <sub>CLK</sub>	1/7* t <sub>CLK</sub> +0.49	
	1st data position	tRP1	-0.49	0	+0.49	
Diay	2nd data position	tRP2	6/7* t <sub>CLK</sub> -0.49	6/7* t <sub>CLK</sub>	6/7* t <sub>CLK</sub> +0.49	
RinX	3rd data position	tRP3	5/7* t <sub>CLK</sub> -0.49	5/7* t <sub>CLK</sub>	5/7* t <sub>CLK</sub> +0.49	ns
(X=0,1,2,3)	4th data position	tRP4	4/7* t <sub>CLK</sub> -0.49	4/7* t <sub>CLK</sub>	4/7* t <sub>CLK</sub> +0.49	
	5th data position	tRP5	3/7* t <sub>CLK</sub> -0.49	3/7* t <sub>CLK</sub>	3/7* t <sub>CLK</sub> +0.49	
	6th data position	tRP6	2/7* t <sub>CLK</sub> -0.49	2/7* t <sub>CLK</sub>	2/7* t <sub>CLK</sub> +0.49	

#### 9.7 POWER SEQUENCE



- Note 1: In order to avoid any damages,  $V_{DD}$  has to be applied before all other signals. The opposite is true for power off where  $V_{DD}$  has to be remained on until all other signals have been switch off. The recommended time period is 1 second.
- Note 2: In order to avoid showing uncompleted patterns in transient state. It is recommended that switching the backlight on is delayed for 1 second after the signals have been applied. The opposite is true for power off where the backlight has to be switched off 1 second before the signals are removed.
- Note 3: In order to avoid high Inrush current, V<sub>DD</sub> rising time need to set more than 0.5ms.

#### 9.8 DATA INPUT for DISPLAY COLOR

				1	Red	Data	a					G	Green	Dat	а						Blue	Data	ı		
Inp	ut color	R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	В7	В6	B5	В4	ВЗ	B2	B1	В0
		MSB							LSB	MSB							LSB	MSB							LSB
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(2)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Red	:		:	:	:	:	:	:	:		•••		:	:	:	:			:	:	:	:	:	:	:
	Red(253)	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	Green(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
Green	:		••	:	:	:	:	:	:		•••		•••		:	:		• •	:	:	:	:	:		:
	Green(253)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	Green(254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
Blue	÷	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	;	:	:	:	:	:
	Blue(253)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	Blue(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

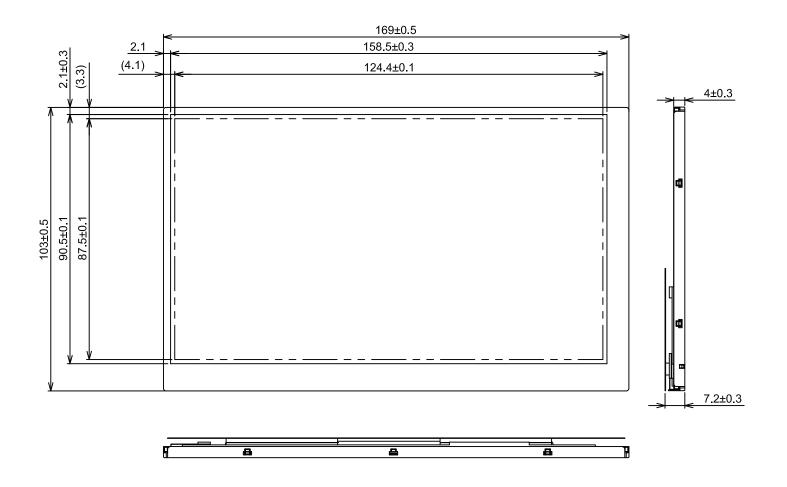
Note 1: Definition of gray scale : Color(n) Number in parenthesis indicates gray scale level. Larger number corresponds to brighter level.

Note 2: Data Signal : 1 : High, 0 : Low

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## 10. OUTLINE DIMENSIONS

#### 10.1 FRONT VIEW

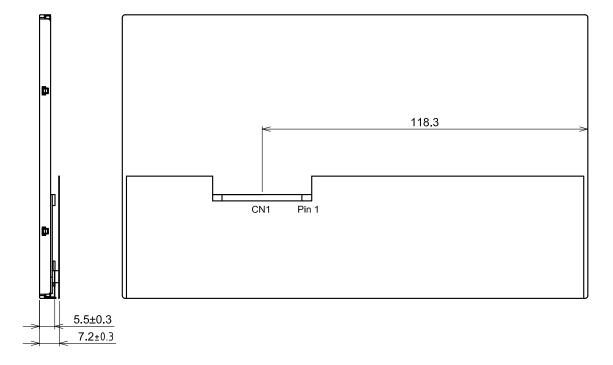


General Tolerance:±0.5mm

Scale: NTS Unit: mm

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#### 10.2 REAR VIEW



General Tolerance:±0.5mm

Scale : NTS Unit : mm

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#### 11. APPEARANCE STANDARD

The appearance inspection is performed in a dark room around 100 lx based on the conditions as below:

- The distance between inspector's eyes and display is 30 cm.
- The viewing zone is defined with angle  $\theta$  shown in Fig. 11. The inspection should be performed within 45° when display is shut down. The inspection should be performed within 5° when display is power on.

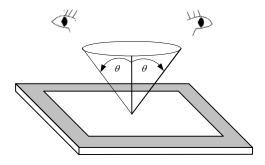


Fig. 11.1

#### 11.1 THE DEFINITION OF LCD ZONE

LCD panel is divided into 2 areas as shown in Fig.11.2 for appearance specification in next section. A zone is the LCD active area (dot area); B zone is the area between A zone and metal frame.

In terms of housing design, B zone is the recommended window area customers' housing should be located in.

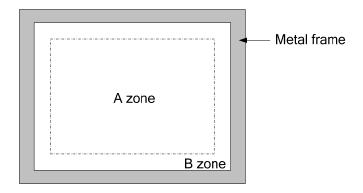


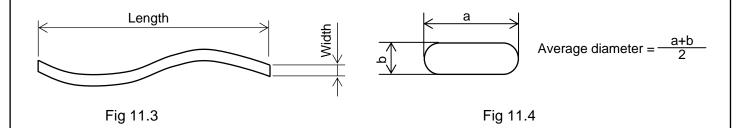
Fig. 11.2

#### 11.2 LCD APPEARANCE SPECIFICATION

The specification as below is defined as the amount of unexpected phenomenon or material in different zones of LCD panel. The definitions of length, width and average diameter using in the table are shown in Fig. 11.3 and Fig. 11.4.

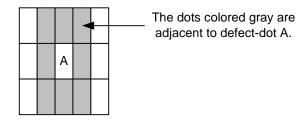
Item			Cri	iteria			Applied zone	
	Length (mm)	Width	n (mm)	Maximum nu	umber	Minimum space		
	Ignored	W≤	0.01	Ignored	t	-		
	L≦40	W≦	0.02	10		-		
	L≦20	W≦	0.04	10		-		
Scratches			Round ([	Oot Shape)			Α·Β	
	Average diameter (r	mm)	Maxim	um number	Mir	nimum space		
	D≦0.2		I	gnore		-		
	D≦0.4			10		-		
Dent		Se	rious one	is not allowed			Α	
Wrinkles in polarizer		Se	rious one	is not allowed			Α	
	Average diame	eter (m	m)	Max	kimum r	number		
Bubbles on polarizer	D≦0.3	3			Ignore	ed	А	
Bubbles on polarizer	0.3 <d≦< td=""><td>0.5</td><td></td><td></td><td>10</td><td></td><td>^</td></d≦<>	0.5			10		^	
	0.5 <d≦< td=""><td>1.0</td><td></td><td></td><td>5</td><td></td><td></td></d≦<>	1.0			5			
		Fila	amentous	(Line shape)				
	Length (mm)		Width (mm)		Max	imum number		
	Ignored		W≦	<b>≦0.02</b>	Ignored		Α、B	
	L≦2.0		W≦	<b>≦0.03</b>	10			
1) Stains	L≦1.0		W≦	<b>60.06</b>		10		
2) Foreign Materials		Round (Dot shape)						
3) Dark Spot	Average diameter (m	ım)	Maximu	m number	Min	imum Space		
o) Bark opot	D≦0.3		lgn	ored	-			
	0.3 <d≦0.5< td=""><td></td><td></td><td>5</td><td></td><td>-</td><td>Α·Β</td></d≦0.5<>			5		-	Α·Β	
	D>0.5			0		-		
	In total		Filamentous + Round=10					
	-	Those	wiped out e	easily are accept	able			
			T	ype	Max	imum number		
			1	dot		4		
			2 adja	cent dot		1		
	Bright dot-defect	3	adjacent	dot or above	N	lot allowed		
			De	nsity	2	2(φ 20mm)		
Dot-Defect			In	total		5	А	
(Note 1)				dot		5	^	
				cent dot		2		
	Dark dot-defect	3	adjacent	dot or above	N	lot allowed		
			Density			3(φ 20mm)		
			In	total		5		
		In tota	al			10		

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Note 1: The definitions of dot defect are as below:

- The defect area of the dot must be bigger than half of a dot.
- For bright dot-defect, showing black pattern, the dot's brightness must be over 30% brighter than others.
- For dark dot-defect, showing white pattern, the dot's brightness must be under 70% darker than others.
- The definition of 1-dot-defect is the defect-dot, which is isolated and no adjacent defect-dot.
- The definition of adjacent dot is shown as Fig. 11.5.
- The Density of dot defect is defined in the area within diameter  $\phi$  =20mm.



#### 12. PRECAUTIONS

#### 12.1 PRECAUTIONS of ESD

- 1) Before handling the display, please ensure your body has been connected to ground to avoid any damages by ESD. Also, do not touch display's interface directly when assembling.
- 2) Please remove the protection film very slowly before turning on the display to avoid generating ESD.

#### 12.2 PRECAUTIONS of HANDLING

- 1) In order to keep the appearance of display in good condition; please do not rub any surfaces of the displays by sharp tools harder than 3H, especially touch panel, metal frame and polarizer.
- 2) Please do not pile the displays in order to avoid any scars leaving on the display. In order to avoid any injuries, please pay more attention for the edges of glasses and metal frame, and wear finger cots to protect yourself and the display before working on it.
- 3) Touching the display area or the terminal pins with bare hand is prohibited. This is because it will stain the display area and cause poor insulation between terminal pins, and might affect display's electrical characteristics furthermore.
- 4) Do not use any harmful chemicals such as acetone, toluene, and isopropyl alcohol to clean display's surfaces.
- 5) Please use soft cloth or absorbent cotton with ethanol to clean the display by gently wiping. Moreover, when wiping the display, please wipe it by horizontal or vertical direction instead of circling to prevent leaving scars on the display's surface, especially polarizer.
- 6) Please wipe any unknown liquids immediately such as saliva, water or dew on the display to avoid color fading or any permanently damages.
- 7) Maximum pressure to the surface of the display must be less than  $1.96 \times 10^4$  Pa. If the area of adding pressure is less than  $1 \text{ cm}^2$ , the maximum pressure must be less than  $1.96 \times 10^4$  Pa.

#### 12.3 PRECAUTIONS of OPERATING

- 1) Please input signals and voltages to the displays according to the values defined in the section of electrical characteristics to obtain the best performance. Any voltages over than absolute maximum rating will cause permanent damages to this display. Also, any timing of the signals out of this specification would cause unexpected performance.
- 2) When the display is operating at significant low temperature, the response time will be slower than it at 25 °C . In high temperature, the color will be slightly dark and blue compared to original pattern. However, these are temperature-related phenomenon of LCD and it will not cause permanent damages to the display when used within the operating temperature.
- 3) The use of screen saver or sleep mode is recommended when static images are likely for long periods of time. This is to avoid the possibility of image sticking.
- 4) Spike noise can cause malfunction of the circuit. The recommended limitation of spike noise is no bigger than  $\pm$  100 mV.

#### 12.4 PRECAUTIONS of STORAGE

If the displays are going to be stored for years, please be aware the following notices.

- 1) Please store the displays in a dark room to avoid any damages from sunlight and other sources of UV light.
- 2) The recommended long-term storage temperature is between 10 °C ~35 °C and 55%~75% humidity to avoid causing bubbles between polarizer and LCD glasses, and polarizer peeling from LCD glasses.
- 3) It would be better to keep the displays in the container, which is shipped from KOE, and do not unpack it.
- 4) Please do not stick any labels on the display surface for a long time, especially on the polarizer.

#### 12.5 PRECAUTIONS of IMAGE STICKING

- 1) Do not display the fixed image or very frequently repeated clips in a long period of time, it may cause image sticking on display. Even a video of several minutes, which is played in a loop, is considered as repetitive.
- 2) Screensaver or power saving mode is recommended to avoid image sticking effectively. Using moving images, scrolling text and alternating a fixed image with a moving image, are the ideal ways to reduce the possibility of image sticking.
- 3) Additionally, it is important to avoid using static bars at image boundaries. Typically, such bars are a result of difference in aspect ratio (e.g., playing 4:3 content on a 16:9 display).

### 13. DESIGNATION of LOT MARK

1) The lot mark is showing in Fig.13.1. First 4 digits are used to represent production lot, T represented made in Taiwan, and the last 6 digits are the serial number.

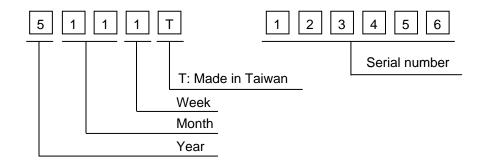


Fig. 13.1

2) The tables as below are showing what the first 4 digits of lot mark are shorted for.

Year	Lot Mark
2015	5
2016	6
2017	7
2018	8
2019	9

Month	Lot Mark	Month	Lot Mark
Jan.	01	Jul.	07
Feb.	02	Aug.	08
Mar.	03	Sep.	09
Apr.	04	Oct.	10
May	05	Nov.	11
Jun.	06	Dec.	12

Week	Lot Mark	
1~7 days	1	
8~14 days	2	
15~21 days	3	
22~28 days	4	
29~31 days	5	

- 3) Except letters I and O, revision number will be shown on lot mark and following letters A to Z.
- 4) The location of the lot mark is on the back of the display shown in Fig. 13.2.



Fig. 13.2

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